4. Ordering information

Table 1.	Ordering	information
	e ao ing	

Type number	Package			
	Temperature range	Name	Description	Version
NCX2222DP	–40 °C to +85 °C	TSSOP8	plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm	SOT505-2
NCX2222GU	–40 °C to +85 °C	HXSON8	plastic, thermal enhanced extremely thin small outline package; no leads; 8 terminals; body 1.35 \times 1.7 \times 0.5 mm	SOT972-2 ^[1]
NCX2222GT	–40 °C to +85 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 \times 1.95 \times 0.5 mm	SOT833-1
NCX2222GF	–40 °C to +85 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body $1.35 \times 1 \times 0.5$ mm	SOT1089
NCX2222GM	–40 °C to +85 °C	XQFN8	plastic, extremely thin quad flat package; no leads; 8 terminals; body $1.6 \times 1.6 \times 0.5$ mm	SOT902-2

[1] Lead pitch is 0.4 mm.

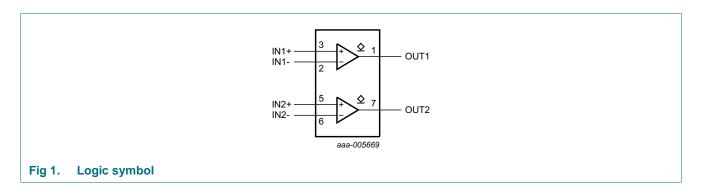
5. Marking

Table 2. Marking codes

Type number	Marking ^[1]
NCX2222DP	gb
NCX2222GU	gb
NCX2222GT	gb
NCX2222GF	gb
NCX2222GM	gb

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram



Low voltage comparator; open-drain output

7. Pinning information

7.1 Pinning

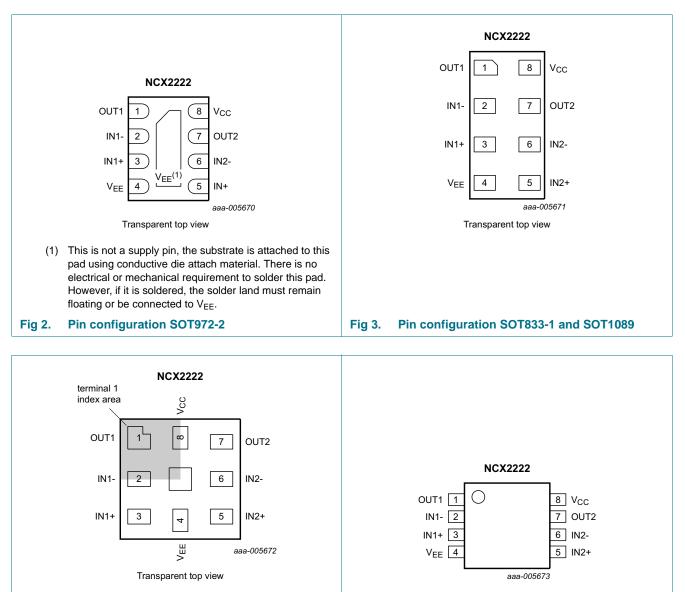


Fig 4. Pin configuration SOT902-2

Fig 5.

Pin configuration SOT505-2

NCX2222

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7.2 Pin description

Table 3.	Pin description		
Symbol		Pin	Description
OUT1		1	comparator output 1
IN1–		2	comparator input 1 (negative)
IN1+		3	comparator input 1 (positive)
V_{EE}		4	supply voltage
IN2+		5	comparator input 2 (positive)
IN2-		6	comparator input 2 (negative)
OUT2		7	comparator output 2
V _{CC}		8	supply voltage

8. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to V_{EE}.

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-	7.0	V
VI	input voltage	IN1-, IN1+, IN2-, IN2+ inputs	-0.5	V _{CC} + 0.5	V
Vo	output voltage		$V_{\text{EE}}-0.5$	7.0	V
t _{sc(o)}	output short-circuit time		<u>[1]</u> _	indefinite	S
T _{j(max)}	maximum junction temperature		-	+150	°C
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \ to +85 \ ^{\circ}C$	-	250	mW

[1] Do not exceed the maximum total power dissipation.

9. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage	V_{CC} to V_{EE}				
		full spec operating range	1.6	-	5.5	V
		functional operating range	1.3	-	5.5	V
VI	input voltage		V_{EE}	-	V _{CC}	V
Vo	output voltage		V_{EE}	-	5.5	V
T _{amb}	ambient temperature		-40	-	+85	°C

Low voltage comparator; open-drain output

10. Static characteristics

Table 6. Static characteristics

At recommended operating conditions. $V_{CC} = 1.6 \text{ V}$ to 5.5 V, $V_{EE} = 0 \text{ V}$; $V_{CM} = 0.5 V_{CC}$ unless otherwise specified.

Symbol	Parameter	Conditions			25 °C		–40 °C t	to +85 ℃	Unit
				Min	Тур	Max	Min	Max	
V _H	hysteresis voltage			6	9	13	-	-	mV
		V _{CC} = 1.3 V		-	20	-	-	-	mV
V _{I(offset)}	offset input voltage		[1]	-30	+0.5	+30	-30	+30	mV
		V _{CC} = 1.3 V	[1]	-	3	-	-	-	mV
V _{OL}	LOW-level output	I_{O} = 0.5 mA; V_{CC} = 1.3 V		-	0.05	-	-	-	V
	voltage	$I_{O} = 0.5 \text{ mA}; V_{CC} = 1.6 \text{ V}$		-	0.04	-	-	0.25	V
		$I_0 = 3 \text{ mA}; V_{CC} = 3.0 \text{ V}$		-	0.14	-	-	0.3	V
		$I_0 = 5 \text{ mA}; V_{CC} = 5.5 \text{ V}$		-	0.20	-	-	0.3	V
I _{OZ}	OFF-state output current	$\label{eq:integral} \begin{array}{l} \text{IN-}=\text{V}_{\text{EE}}; \ \text{IN+}=\text{V}_{\text{CC}}; \ \text{V}_{\text{O}}=5.5 \\ \text{V} \end{array}$		-	3	-	-	-	nA
V _{CM}	common-mode voltage	V_{CC} = 1.3 V to 5.5 V		-	V_{EE} to V_{CC}	-	-	-	V
I _{OS}	output short-circuit current	V_{CC} = 5.5 V; V_O = V_{EE} or V_{CC}		-	68	-	-	-	mA
CMRR	common-mode rejection ratio	$\Delta V_{CM} = V_{CC}$		-	70	-	-	-	dB
PSRR	power supply rejection ratio	ΔV_{CC} = 1.95 V		45	80	-	-	-	dB
I _{IB}	input bias current			-	1.0	-	-	-	pА
I _{CC}	supply current	per comparator		-	5.0	-	-	7.0	μΑ

[1] Differential input switching level is guaranteed at the minimum or maximum offset voltage, minus or plus half the maximum hysteresis voltage.

11. Dynamic characteristics

Table 7. Dynamic characteristics

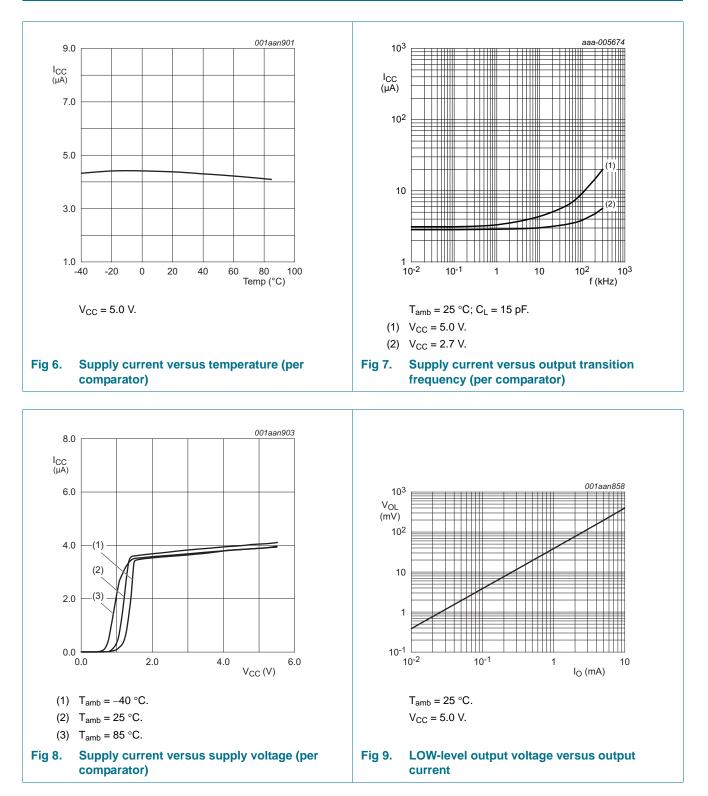
Voltages are referenced to V_{EE} ($V_{EE} = 0 V$); $V_{CC} = 1.6 V$ to 5.5 V; $V_{CM} = 0.5 V_{CC}$ unless otherwise specified.

Symbol	Parameter	Conditions			25 °C		Unit
				Min	Тур	Max	
t _{pd}	propagation delay	20 mV overdrive; $C_L = 15 \text{ pF}$	<u>[1]</u>	-	0.8	-	μs
t _t	transition time	HIGH to LOW; V_{CC} = 5.5 V; C_L = 50 pF	[2]	-	10	-	ns

[1] t_{pd} is the same as t_{PLZ} and t_{PZL} ; t_{PLZ} is the actual time that the output is disabled.

[2] Input signal: 1 kHz, square wave signal with 10 ns edge rate.

Low voltage comparator; open-drain output

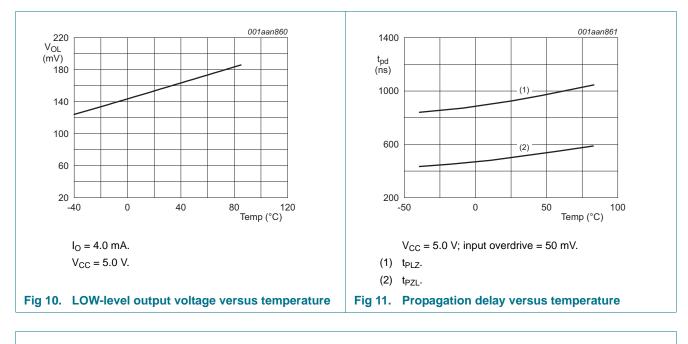


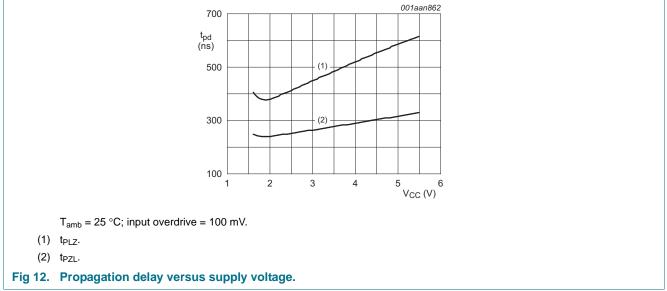
12. Graphs

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NCX2222

Low voltage comparator; open-drain output





Low voltage comparator; open-drain output

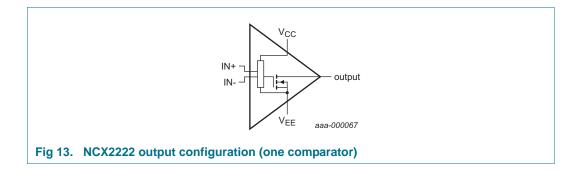
13. Application information

13.1 Operating description

The NCX2222 is a dual, low voltage, low-power comparator with open-drain output. This device is designed for use with a pull-up resistor to define the output switching levels. This device consumes only 5 μ A per comparator of supply current while achieving a typical propagation delay of 0.8 μ s at a 20 mV input overdrive. Figure 11 and Figure 12 show propagation delay with various input overdrives. This comparator is guaranteed to operate at a low voltage of 1.3 V up to 5.5 V. The common-mode input voltage range extends 0.1 V beyond the upper and lower rail without phase inversion or other adverse effects. This device has a typical internal hysteresis of 9.0 mV which allows for greater noise immunity and clean output switching.

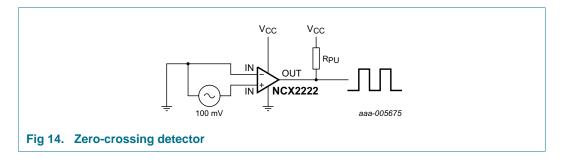
13.2 Output stage

The NCX2222 has an N-channel output stage that has the capability of sinking the output to V_{EE} with a load ranging up to 5.0 mA (see Figure 13).



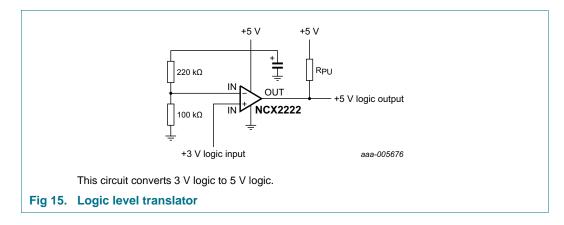
13.3 Zero-crossing detector

Figure 14 shows the NCX2222 configured as a zero-crossing detector.



13.4 Logic level translator

Figure 15 shows the NCX2222 configured as a logic level translator.



14. Package outline

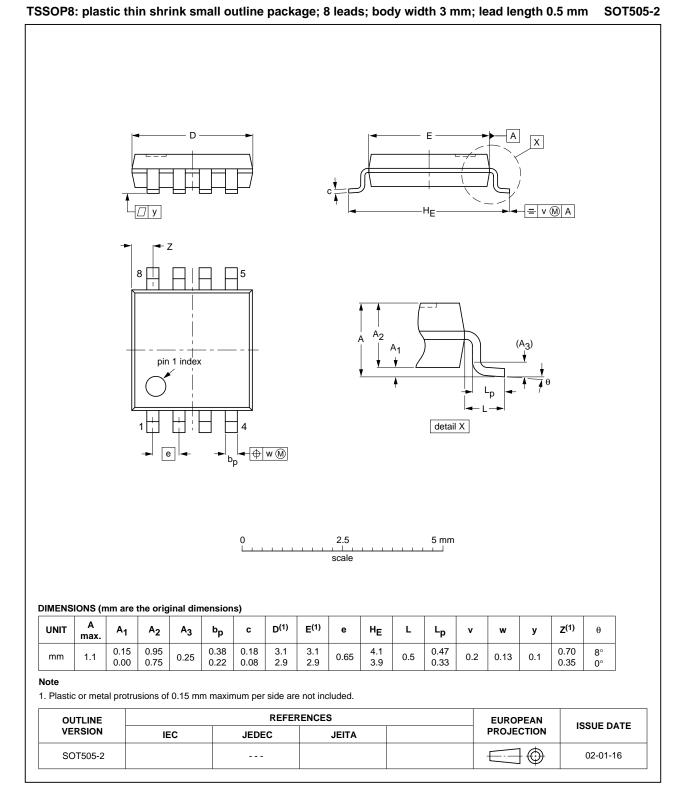
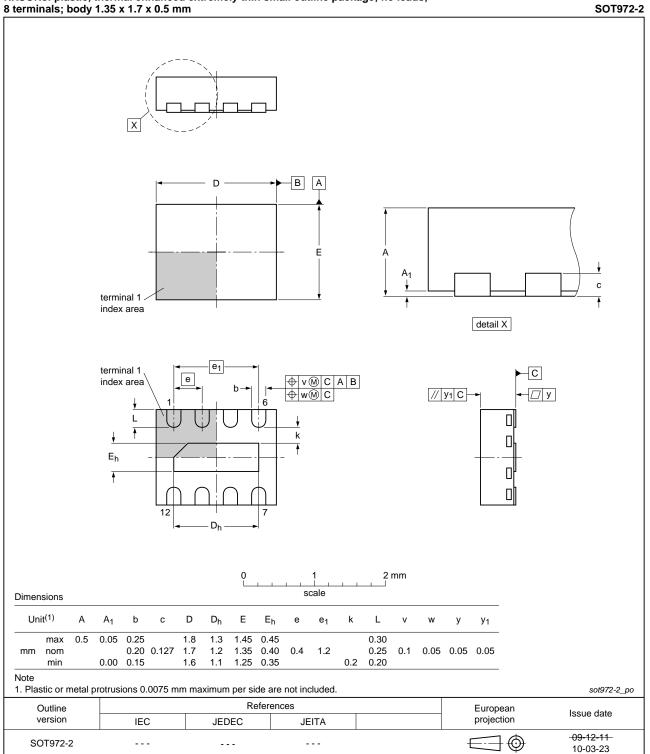


Fig 16. Package outline SOT505-2 (TSSOP8)

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Low voltage comparator; open-drain output



HXSON8: plastic, thermal enhanced extremely thin small outline package; no leads;

Fig 17. Package outline SOT972-2 (HXSON8)

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Low voltage comparator; open-drain output

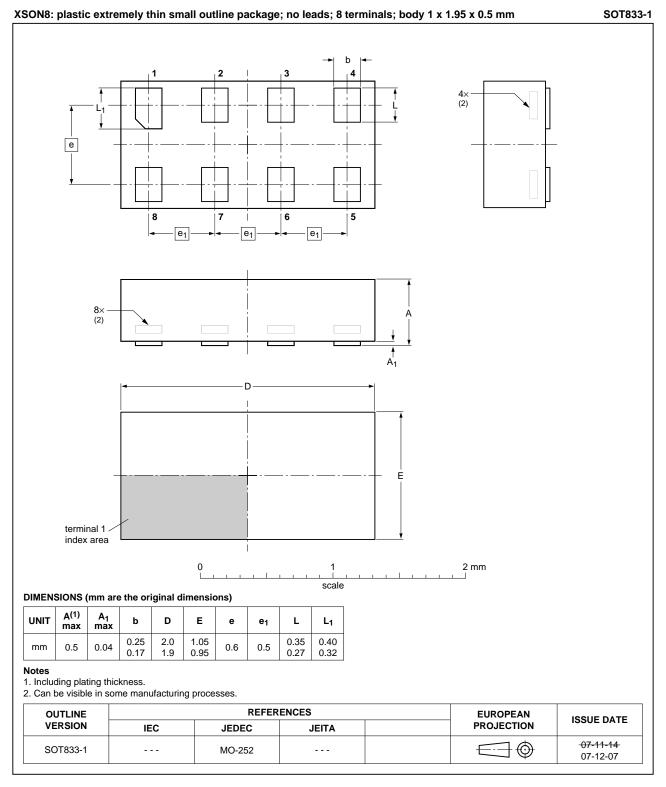
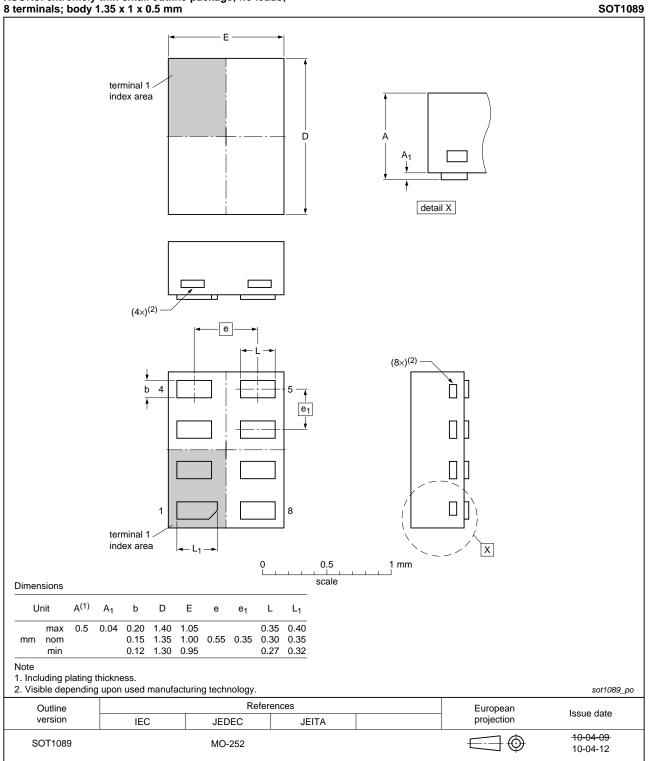


Fig 18. Package outline SOT833-1 (XSON8)

NCX2222 Product data sheet

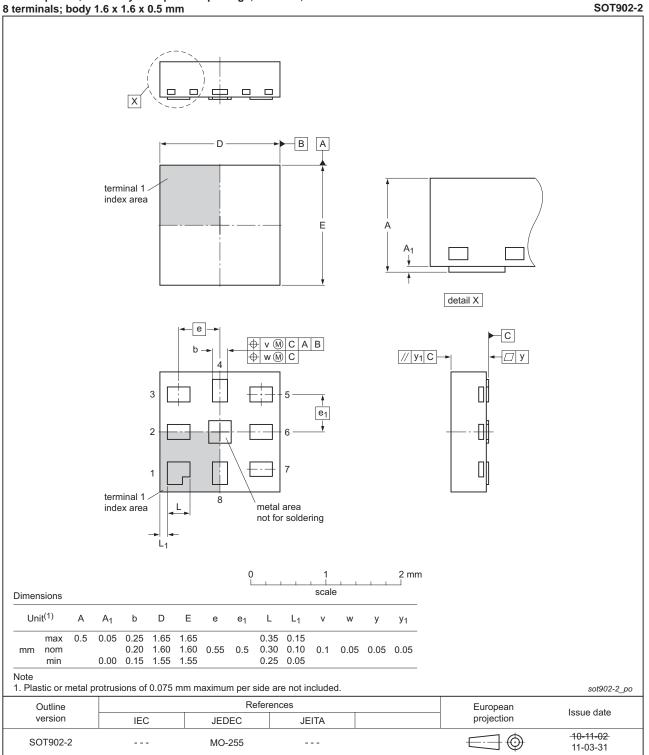
Low voltage comparator; open-drain output



XSON8: extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1 x 0.5 mm

Fig 19. Package outline SOT1089 (XSON8)

Low voltage comparator; open-drain output



XQFN8: plastic, extremely thin quad flat package; no leads; 8 terminals; body 1.6 x 1.6 x 0.5 mm

Fig 20. Package outline SOT902-2 (XQFN8)

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15. Abbreviations

AcronymDescriptionCDMCharged Device ModelESDElectroStatic DischargeHBMHuman Body Model	Table 8.	Abbreviations
ESD ElectroStatic Discharge	Acronym	Description
	CDM	Charged Device Model
HBM Human Body Model	ESD	ElectroStatic Discharge
	HBM	Human Body Model

16. Revision history

Table 9.	Revision histo	ory			
Documen	t ID	Release date	Data sheet status	Change notice	Supersedes
NCX2222	v.1	20121220	Product data sheet	-	-

17. Legal information

17.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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Low voltage comparator; open-drain output

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