

Table 1 – Specifications		
PARAMETER	CSM2512	CSM3637
Resistance Range	1 mΩ to 200 mΩ	
Power Rating at 70°C	1 W <sup>(1)</sup>	3 W (1 mΩ to 10 mΩ) 2 W (>10 mΩ to 200 mΩ)
Maximum Current <sup>(2)</sup>	31 A	54 A
Tolerance	±0.5% (1 mΩ to <3 mΩ) ±0.1% (3 mΩ to 200 mΩ)	±0.5% (1 mΩ to <2 mΩ) ±0.1% (2 mΩ to 200 mΩ)
Temperature Coefficient Max. (–55°C to +125°C, +25°C Ref.)	±50 ppm/°C (1 mΩ to <3 mΩ) ±15 ppm/°C (3 mΩ to 200 mΩ) <sup>(3)</sup> ±10 ppm/°C (3 mΩ to 10 mΩ) is available on request <sup>(4)</sup>	±25 ppm/°C (1 mΩ to <3 mΩ) ±15 ppm/°C (3 mΩ to 200 mΩ) <sup>(3)</sup> ±10 ppm/°C (1 mΩ to 10 mΩ) is available on request <sup>(4)</sup>
Operating Temperature Range	–65°C to +170°C	
Maximum Working Voltage	$(P \times R)^{1/2}$	
Weight (Maximum)	0.09 g	0.29 g

### Notes

- <sup>(1)</sup> For values above 0.1 Ω derate linearly to 80% rated power at 0.5 Ω
- <sup>(2)</sup> Maximum current for a given resistance value is calculated using  $I = \sqrt{P/R}$
- <sup>(3)</sup> Loose TCR is available on request: 35ppm/C (3mΩ to 200mΩ)
- <sup>(4)</sup> Please contact application engineering: [foil@vpgsensors.com](mailto:foil@vpgsensors.com)

### ABOUT CSM (Low Ohm Value 1 mΩ to 200 mΩ)

New high-precision Bulk Metal<sup>®</sup> surface-mount Power Metal Strip<sup>®</sup> resistor of 1 mΩ to 200 mΩ that features an improved load-life stability of ±0.2% at +70°C for 2000 h at rated power, an absolute TCR of ±15 ppm/°C maximum from –55°C to +125°C, +25°C ref., and a tolerance of ±0.1%.

Typical current sensing resistors offer a load-life stability of ≥ 1% through a 2000 h workload. The improved resistance stability of the CSM Series makes it ideal for tightened-stability voltage division and precision current sensing applications in switching linear power supplies, power amplifiers, measurement instrumentation, bridge networks, and medical and test equipment. In addition, the CSM Series complies with EEE-INST-002 (MIL-PRF 55342 and MIL-PRF 49465) for military and space applications.

Traditional Passive current sensors and shunts generate heat under power, which changes their resistance, and thus their voltage output. The CSM's low absolute TCR reduces errors due to temperature gradients, thus reducing a major source of uncertainty in current measurement. The CSM can withstand unconventional environmental conditions, including the extremely high temperatures and radiation-rich environments of down-hole oil exploration and well logging, or the deep-sea underwater repeaters in cross-ocean communications.

The stability of the CSM can be further enhanced by post-manufacturing operations (PMO), such as temperature cycling, short-time overload, and accelerated load life which are uniquely applicable to Bulk Metal<sup>®</sup> Foil resistors.

### Notes

- \* This datasheet provides information about parts that are ROHS-compliant and/or parts that are non-ROHS-compliant. For example, parts with lead (Pb) terminations. Please see the information /tables in this datasheet for details.

The device features a low thermal electromotive force (EMF) that is critical in many precision applications.

The CSM's all-welded construction is composed of a Bulk Metal<sup>®</sup> resistive element with welded copper terminations, plated for soldering. The terminations make true ohmic contact with the resistive layer along the entire side of the resistive element, thereby minimizing temperature variations. Also, the resistor element is designed to uniformly dissipate power without creating hot spots, and the welded terminations material is compatible with the element material.

These design factors result in a very low thermal-EMF (3 μV/°C) resistor, because in addition to the low thermal EMF compatibility of the metals, the uniformity and thermal efficiency of the design minimizes the temperature differential across the resistor, thereby assuring low thermal EMF generation at the leads. This further reduces the “battery effect” exhibited by most current-sensing or voltage-reference resistors. Thus, the parasitic voltage generated at the junction of two dissimilar metals, which is especially important in low-value current-sensing resistors, is minimized, while the pure current-to-voltage conversion is protected from such interference in DC applications.

The stability problems associated with analog circuits are very pervasive, but knowledgeable selection of a few high-quality resistors, networks, or trimming potentiometers

in critical locations can greatly improve circuit performance, long-term application-related performance, as well as the designer's peace-of-mind.

Additionally, the overall system cost is often reduced when a knowledgeable designer concentrates costs in a few exceptionally stable components whose proven minimal-deviation load and environmental stability can often eliminate the necessity of additional compensating circuitry or temperature-controlling systems. The higher reliability and better overall system performances also achieve excellent product results in the field, enhancing market acceptance and product reputation.

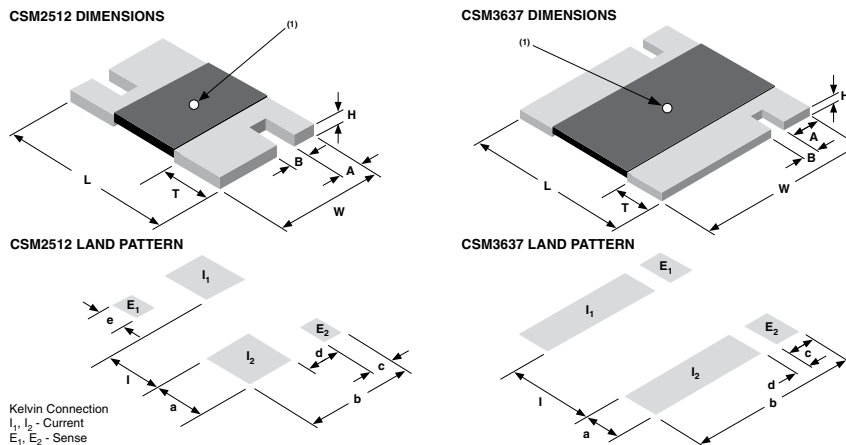
Designers often unnecessarily pay for tighter tolerances than required simply to accommodate the resistance stability shifts they know to be imminent in an application due to the large application-related changes in the components they selected. Selection of a high-stability component like the CSM in these applications eliminates the need for shift allowance due to “planned instability” and allows the use of looser initial tolerances than would be necessary with current-sensing resistors based on other technologies.

### The Key Applications

Applications requiring accuracy and repeatability under stress conditions such as the following:

- Switching and linear power supplies
- Precision current-sensing
- Power management systems
- Feedback circuits
- Power amplifiers
- Measurement instrumentation
- Precision instrumentation amplifiers
- Medical and automatic test equipment
- Satellites and aerospace systems
- Commercial and Military avionics
- Test and measurement equipment
- Electronic scales

**Figure 2 – Dimensions and Imprinting** in inches (millimeters)



**Dimensions – Tolerances  $\pm 0.010$  ( $\pm 0.254$ ),\*  $\pm 0.015$  ( $\pm 0.381$ )**

MODEL	RESISTANCE RANGE (mΩ)	L	W	H	T	A	B
CSM2512	1 to < 5	0.250 (6.350)	0.125 (3.175)	0.025 (0.635)	0.087 (2.210)	0.030 (0.762)*	0.032 (0.813)*
	5 to < 7				0.047 (1.194)		
	7 to 200				0.030 (0.762)		
CSM3637	1 to < 2	0.360 (9.144)	0.370 (9.398)	0.025 (0.635)	0.138 (3.505)	0.061 (1.55)	0.032 (0.813)
	2 to 200	0.360 (9.144)	0.370 (9.398)	0.025 (0.635)	0.086 (2.184)	0.061 (1.549)	0.032 (0.813)

**Land Pattern Dimensions – Tolerances  $\pm 0.003$  ( $\pm 0.076$ )**

MODEL	RANGE	a	b	c	d	e	l
CSM2512	0R001 to 0R0049	0.120 (3.05)	0.145 (3.68)	0.045 (1.14)	0.021 (0.53)	0.055 (1.39)	0.050 (1.27)
	0R005 to 0R0069	0.083 (2.10)	0.145 (3.68)	0.045 (1.14)	0.021 (0.53)	0.055 (1.39)	0.125 (3.17)
	0R007 to 0R2	0.065 (1.65)	0.145 (3.68)	0.045 (1.14)	0.021 (0.53)	0.055 (1.39)	0.160 (4.06)
CSM3637	0R001 to 0R0019	0.168 (4.27)	0.390 (9.91)	0.066 (1.68)	0.024 (0.610)	–	0.074 (1.88)
	0R002 to 0R2	0.116 (2.95)	0.390 (9.91)	0.066 (1.68)	0.024 (0.610)	–	0.178 (4.52)

**Note**

(1) White dot indicates top side of part for mounting purposes

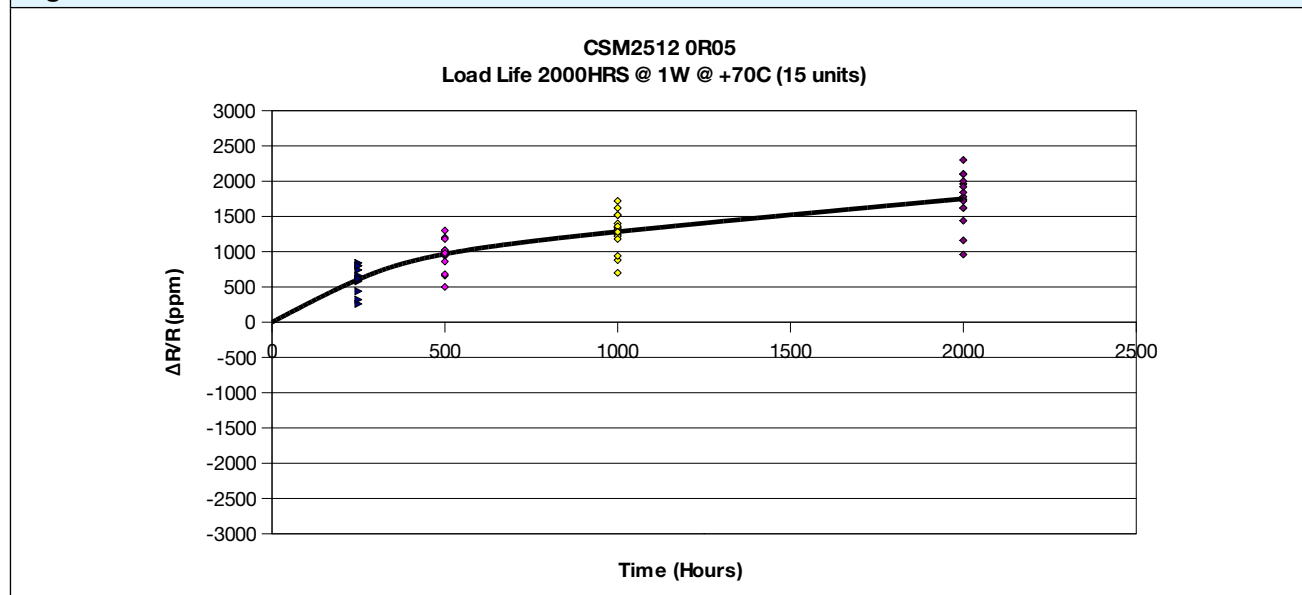
**Table 2 – CSM Series Performance Specifications**

TEST	CONDITIONS	MIL-PRF-49465B ΔR LIMITS	CSM2512/CSM3637	
			TYPICAL ΔR LIMITS <sup>(1)</sup>	MAXIMUM ΔR LIMITS <sup>(1)</sup>
Thermal Shock	-55°C to +150°C, 1000 cycles, 15 min at each extreme	±(0.5%+0.0005R)	0.1%	0.3%
Load Life Stability	2000 h, 70°C at rated power	±(1.0%+0.0005R)	0.2%	1.0%
Bias Humidity	+85°C, 85% humidity 10% bias, 1000 h	±(0.5%+0.0005R)	0.05%	0.2%
Short Time Overload	5 x rated power for 5 s (See note (3) from table 1)	±(0.5%+0.0005R)	0.1%	0.3%
High Temperature Exposure	1000 h, 170°C	±(1.0%+0.0005R)	0.2%	0.3%
Low Temperature Storage	-55°C for 24 h	±(0.5%+0.0005R)	0.05%	0.1%
Moisture Resistance	MIL-STD-202, method 106, 0 power, 7a and 7b not required	±(0.5%+0.0005R)	0.02%	0.05%
Shock	100 g, 6 ms, 5 pulses	±(0.1%+0.0005R)	0.02%	0.05%
Vibration	(10 Hz to 2000 Hz) 20 g	±(0.1%+0.0005R)	0.02%	0.05%
Resistance to Soldering Heat	10 s to 12 s at +260°C	±(0.25%+0.0005R)	0.05%	0.1%
Solderability	MIL-STD-202	95% coverage	-	-

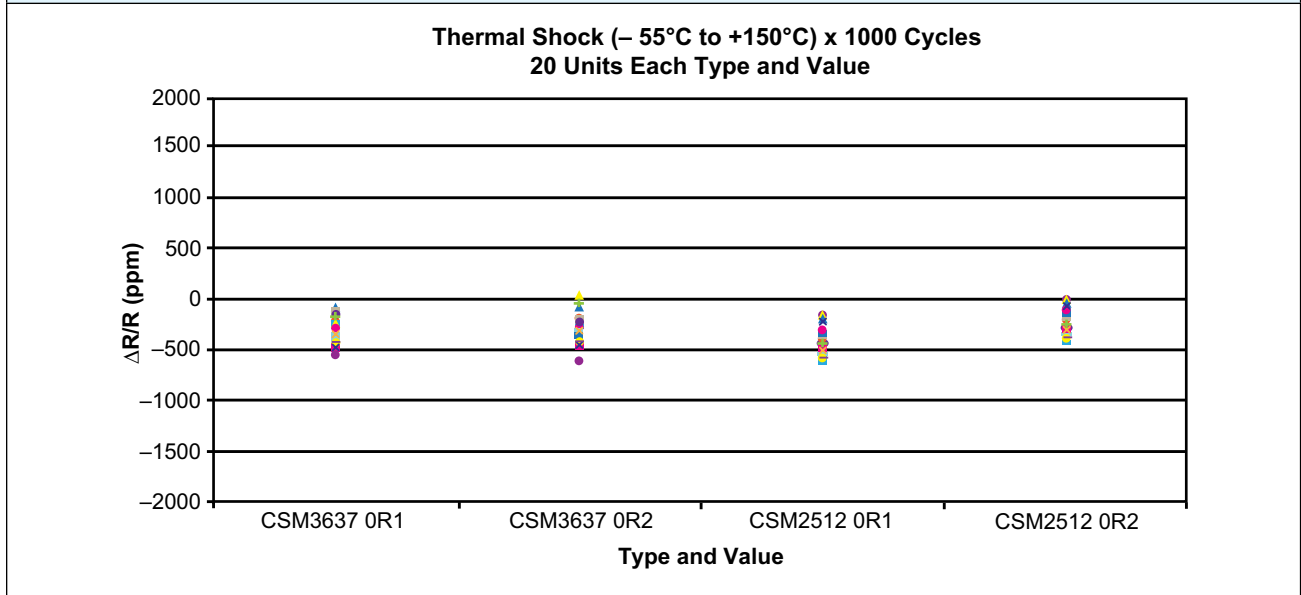
**Note**

<sup>(1)</sup> Measurement error allowed for ΔR limits: 0.0005 Ω.

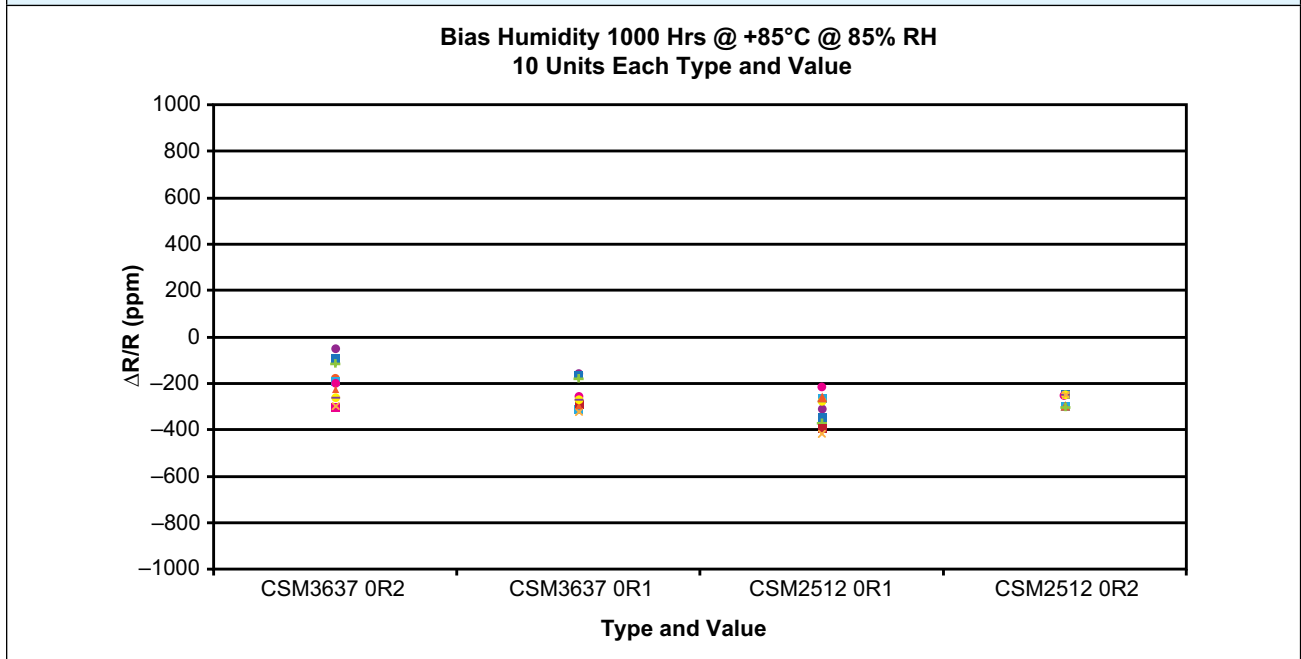
**Figure 3 – Load Life Results of CSM2512**



**Figure 4 – Thermal Shock Results of CSM3637 and CSM2512**



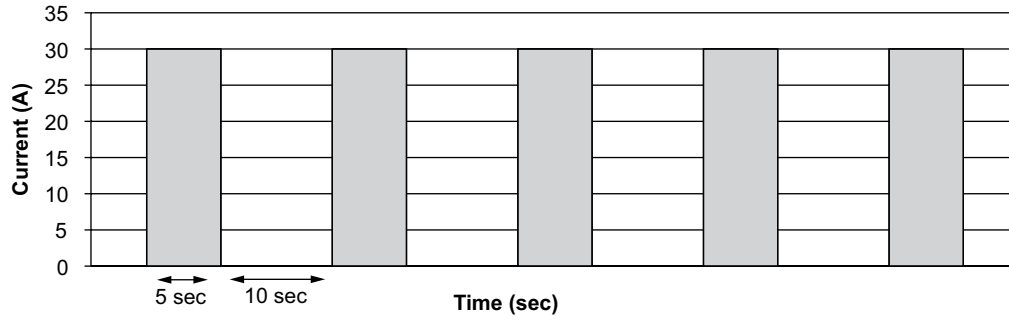
**Figure 5 – Bias Humidity Results of CSM3637 and CSM2512**



**Figure 6 - Pulse Test I**

**CSM3637 0R005 Test - 10 units**  
(5 Pulses of 5 sec @ 30 A, 10 sec @ 0 A)

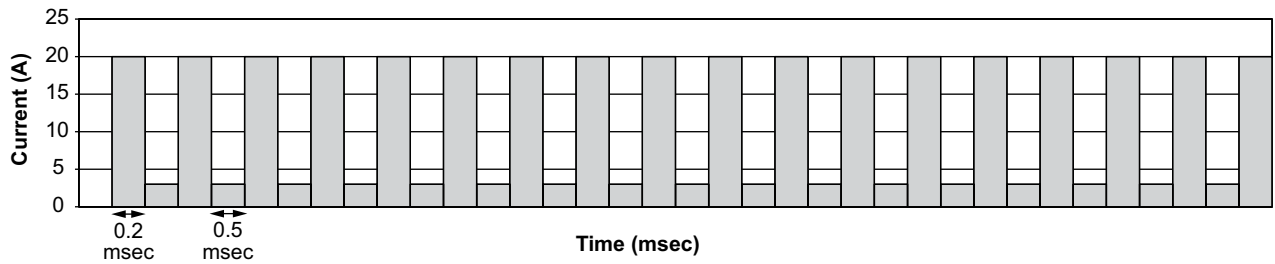
**Average Resistance Deviation: 696 ppm**  
**STD = 305 ppm, Measurement Error = 0.0005R**



**Figure 7 - Pulse Test II**

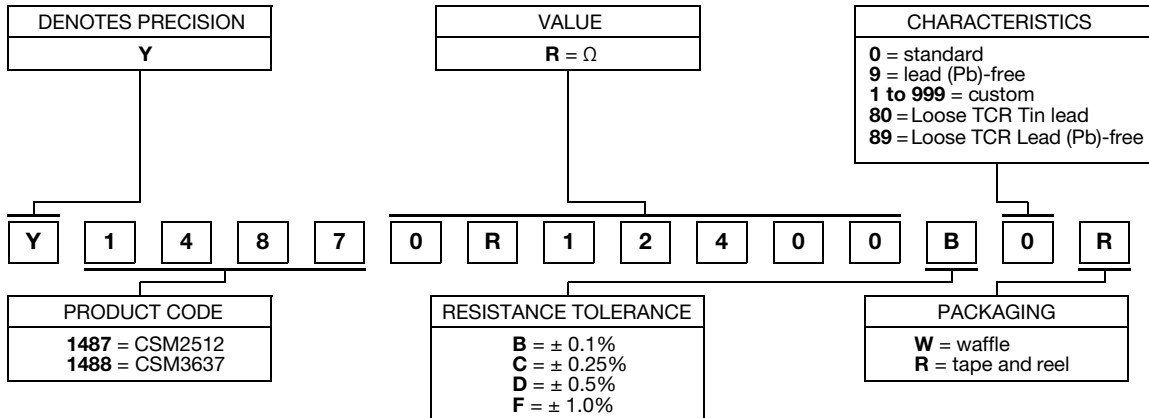
**CSM3637 0R005 Pulse Test - 10 units**  
(35 Pulses of 0.2 msec @ 20 A, 0.5 msec @ 3 A)

**Average Resistance Deviation: 13.3 ppm**  
**STD = 27.3 ppm, Measurement Error = 0.0005R**



**Table 3 – Global Part Number Information<sup>(1)</sup>**

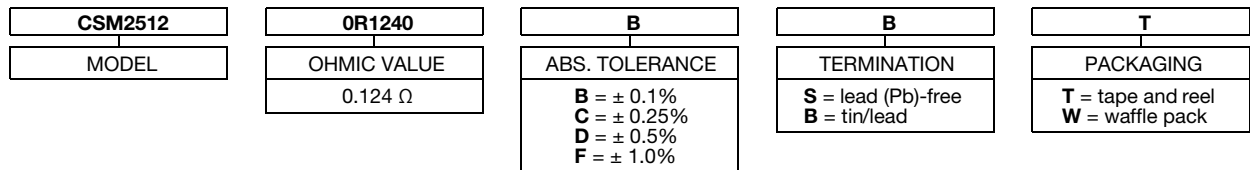
**NEW GLOBAL PART NUMBER: Y14870R12400B0R (preferred part number format)**



FOR EXAMPLE: ABOVE GLOBAL ORDER Y1487 0R12400 B 0 R:

TYPE: CSM2512  
 VALUE: 124.0 m $\Omega$   
 ABSOLUTE TOLERANCE:  $\pm 0.1\%$   
 TERMINATION: standard tin/lead  
 PACKAGING: tape and reel

**HISTORICAL PART NUMBER: CSM2512 0R1240 B B T (will continue to be used)**



**Note**

<sup>(1)</sup> For non-standard requests, please contact application engineering.



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