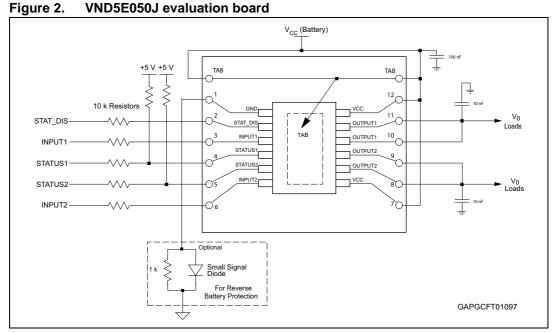
#### 1 Design recommendations

This evaluation board provides mounting solution and some heat sinking capability for prototype development, but there are still external components that are required to make these devices work in any application. For further information on how the evaluation board has to be used you can refer to the AN4210 (see *Appendix A: Reference documents*).

Figure 2 illustrates the necessary components for any application.



ST has produced a user manual for safe designs using ST's VIPower devices. This is UM1556 (see *Appendix A: Reference documents*). UM1556 is a VIPower Hardware design guide that provides all necessary information to successfully design your circuit using our VIPower drivers.

All designs have different needs and requirements. Whatever design you decide to use, it will still need to be verified in order to meet your application specifications. ST implies no guarantee or warranty (see *Appendix A: Reference documents*).

EV-VND5E050J Thermal data

## 2 Thermal data

Table 2. VND5E050J-E thermal data

	Symbol	Parameter	Max. value	Unit
Ī	R <sub>thj-amb</sub>	Thermal resistance junction-ambient (MAX)	31	°C/W

Table 3. PCB specifications

Parameter	Value	Unit
Board dimensions	38 x 38	mm
Number of Cu layer	2	_
Layer Cu thickness	70	μm
Board finish thickness	1.6 +/- 10%	mm
Board Material	FR4	_
Thermal vias separation	1.2	mm
Thermal vias diameter	0.3 +/- 0.08	mm

#### 3 Board connector reference

Figure 3. Board layout



Table 4. Board connector specification

Connector	Board lead number	Device pin function <sup>(1)</sup>
J1	TAB	V <sub>CC</sub>
J1	1	GND
J1	2	STAT_DIS
J1	3	INPUT1
J1	4	STATUS1
J1	5	STATUS2
J1	6	INPUT2
J2	TAB	V <sub>CC</sub>
J2	7	V <sub>CC</sub>
J2	8	OUTPUT2
J2	9	OUTPUT2
J2	10	OUTPUT1
J2	11	OUTPUT1
J2	12	V <sub>cc</sub>

For further clarification on pin functions please refer to the related datasheet (see Appendix A: Reference documents).

EV-VND5E050J Package information

## 4 Package information

# 4.1 ECOPACK® packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="www.st.com">www.st.com</a>.

ECOPACK® is an ST trademark.

Reference documents EV-VND5E050J

# Appendix A Reference documents

 Double channel high side driver for automotive applications (VND5E050J-E, VND5E050K-E, DocID 14472)

- 2. VIPower M0-5 and M0-5Enhanced high-side drivers (UM1556, DocID 023520)
- 3. PowerSSO-12 devices evaluation bord (AN4210, DocID 023963)
- 4. Evaluation Product Licence Agreement on <a href="www.st.com">www.st.com</a>

EV-VND5E050J Revision history

# **Revision history**

Table 5. Document revision history

Date	Revision	Changes
28-Nov-2012	1	Initial release.
07-Dec-2012	2	Updated Figure 2: VND5E050J evaluation board Updated Table 3: PCB specifications
17-Sep-2013	3	Updated disclaimer.

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