Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Drain-Source voltage	V_{DSS}	60	V
Gate-Source voltage	V_{GS}	± 20	V
Continuous Drain current @ V _{GS} = 10V; T _A =25°C (b)	I _D	5.7	Α
@ V_{GS} = 10V; T_A =70°C (D)		4.5	
@ V_{GS} = 10V; T_A =25°C ^(a)		4.3	
@ V _{GS} = 10V; T _L =25°C ^{(a)(d)}		7.0	
Pulsed Drain current (c)	I _{DM}	25.7	А
Continuous Source current (Body diode) (b)	Is	4.1	А
Pulsed Source current (Body diode) (c)	I _{SM}	25.7	А
Power dissipation at T _A =25°C ^(a) Linear derating factor	P _D	1.56 12.5	W mW/°C
Power dissipation at T _A =25°C ^(b) Linear derating factor	P _D	2.8 22.2	W mW/°C
Power dissipation at T _L =25°C ^(d) Linear derating factor	P _D	4.14 33.1	W mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to 150	°C

Thermal resistance

Parameter	Symbol	Value	Unit	
Junction to ambient ^(a)	$R_{ heta JA}$	80	°C/W	
Junction to ambient ^(b)	$R_{ heta JA}$	45	°C/W	
Junction to lead ^(d)	$R_{ heta JL}$	30.2	°C/W	

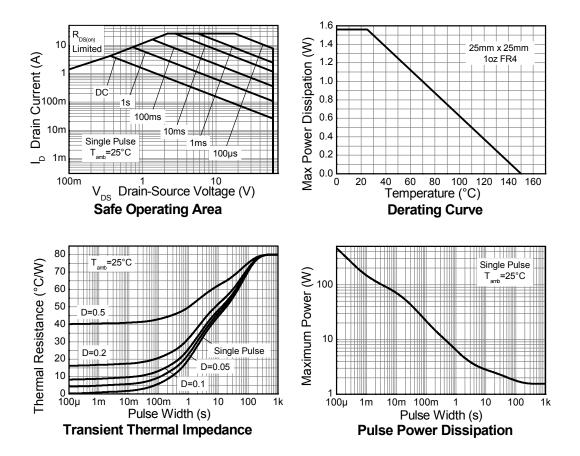
NOTES:

⁽a) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

⁽b) Mounted on FR4 PCB measured at $t \le 10$ sec. (c) Repetitive rating on 25mm x 25mm FR4 PCB, D=0.02, pulse width 300us – pulse width limited by maximum junction temperature.

⁽d) Thermal resistance from junction to solder-point (at the end of the drain lead).

Thermal characteristics



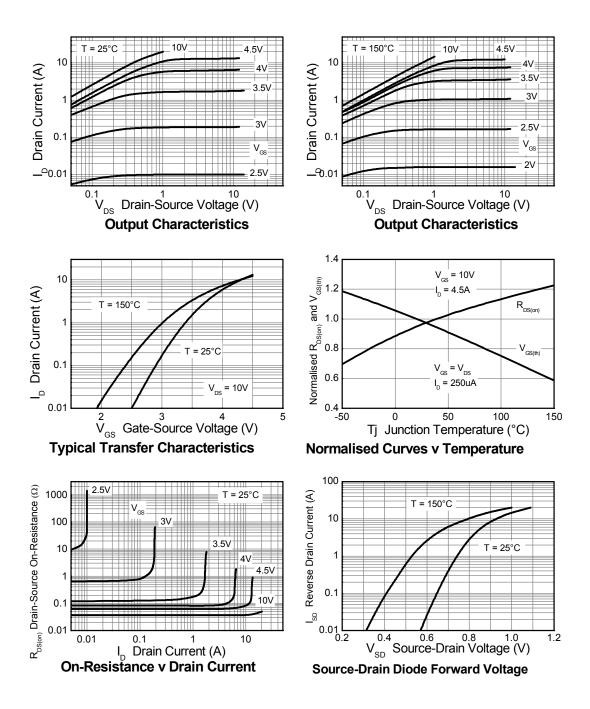
Electrical characteristics (at $T_{amb} = 25$ °C unless otherwise stated)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Static	,		,	•	ı	1		
Drain-Source breakdown voltage	V _{(BR)DSS}	60			V	$I_D = 250 \mu A, V_{GS} = 0V$		
Zero gate voltage drain current	I _{DSS}			1.0	μA	V _{DS} =60V, V _{GS} =0V		
Gate-Body leakage	I _{GSS}			100	nA	V_{GS} =±20V, V_{DS} =0V		
Gate-Source threshold voltage	V _{GS(th)}	1		3	V	I_D =250 μ A, V_{DS} = V_{GS}		
Static Drain-Source on-state resistance (*)	R _{DS(on)}			0.050 0.070	Ω	V _{GS} = 10V, I _D = 3.6A V _{GS} = 4.5V, I _D = 3.0A		
Forward Transconductance (*)(†)	9 _{fs}		10.2		S	V _{DS} = 15V, I _D = 4.5A		
Dynamic ^(†)								
Input capacitance	C _{iss}		1063		pF			
Output capacitance	C _{oss}		104		pF	V _{DS} = 30V, V _{GS} =0V		
Reverse transfer capacitance	C _{rss}		64		pF	f=1MHz		
Switching (‡)(†)								
Turn-on-delay time	t _{d(on)}		3.8		ns			
Rise time	t _r		4.0		ns	V _{DD} = 30V, V _{GS} = 10V		
Turn-off delay time	t _{d(off)}		26.2		ns	I _D = 1A R _G ≅ 6.0Ω,		
Fall time	t _f		10.6		ns	-1 $G = 0.052$,		
Gate charge	Qg		11.0		nC	V _{DS} = 30V, V _{GS} = 5V I _D = 4.5A		
Total gate charge	Qg		20.4		nC			
Gate-Source charge	Q _{gs}		4.1		nC	V _{DS} = 30V, V _{GS} = 10V		
Gate-Drain charge	Q _{gd}		5.1		nC	I _D = 4.5A		
Source-Drain diode			ı		1			
Diode forward voltage (*)	V _{SD}		0.85	0.95	V	I _S = 5.5A,V _{GS} =0V		
Reverse recovery time (‡)	t _{rr}		22.0		ns	L = 2.24 di/dt=4004/:-		
Reverse recovery charge ^(‡)	Q _{rr}		21.4		nC	-I _S = 2.2A,di/dt=100A/μs		

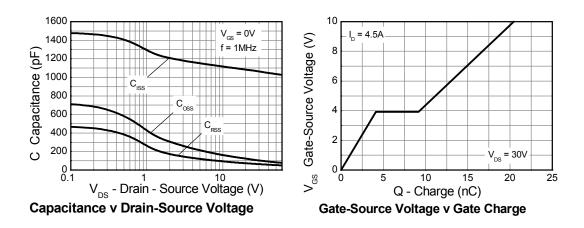
NOTES:

^(*) Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$. (†)Switching characteristics are independent of operating junction temperature. (‡)For design aid only, not subject to production testing

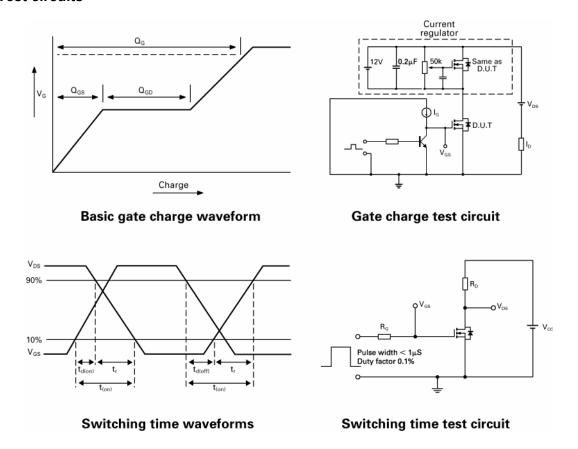
Typical characteristics



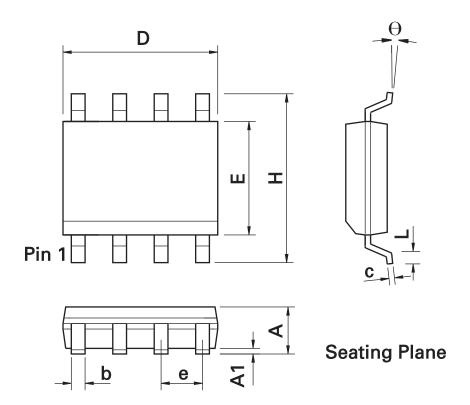
Typical characteristics



Test circuits



Package outline SO8



SO8 Package Information

DIM	Inc	hes	Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
Α	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	U	0°	8°	0°	8°
E	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Note: Controlling dimensions are in inches. Approximate dimensions are provided in millimeters

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"Active product status recommended for new designs

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However, changes to the test conditions and specifications may occur, at any time and without notice.

However, changes to the test conditions and specifications may occur, at any time and without notice This term denotes an issued datasheet containing finalized specifications. However, changes to

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Zetex sales offices

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH Kustermann-Park Balanstraße 59 D-81541 München	Zetex Inc 700 Veterans Memorial Highway Hauppauge, NY 11788 USA	Zetex (Asia Ltd) 3701-04 Metroplaza Tower 1 Hing Fong Road, Kwai Fong Hong Kong	Zetex Semiconductors plc Zetex Technology Park, Chadderton Oldham, OL9 9LL United Kingdom
Germany Telefon: (49) 89 45 49 49 0 Fax: (49) 89 45 49 49 europe.sales@zetex.com	Telephone: (1) 631 360 2222 Fax: (1) 631 360 8222 usa.sales@zetex.com	Telephone: (852) 26100 611 Fax: (852) 24250 494 asia.sales@zetex.com	Telephone (44) 161 622 4444 Fax: (44) 161 622 4446 hq@zetex.com

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