

2.2 Green features

- Enables no-load power consumption < 20 mW
- Very low supply current in no-load condition with energy save mode
- Incorporates a high voltage start-up circuit with zero current consumption under normal switching operation
- Available in halogen-free and Restriction of Hazardous Substances (RoHS) SO8 package

2.3 Protection features

- OverVoltage Protection (OVP) on feedback control pin (FB) with safe restart
- UnderVoltage LockOut (UVLO) on IC supply pin
- OverTemperature Protection (OTP)
- Soft-start by reduced peak current for zero and low output voltage
- Demagnetization protection for guaranteed discontinuous conduction mode operation
- Open and short-circuit protection of the Feedback control (FB) pin
- Short-circuit protection of the charger output

3. Applications

- Battery chargers for smart phones and media tablets up to 12.5 W
- Standby supply for TV and desktop PC

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Power switch (pin EMITTER)						
R_{on}	on-state resistance	emitter switch; $I_{ds} = 100 \text{ mA}$; $T_j = 25 \text{ }^\circ\text{C}$	0.75	0.85	1.0	Ω
Oscillator (pins EMITTER and SENSE)						
f_{burst}	burst mode frequency	burst frequency in CVB mode, without jitter	350	400	450	Hz
$f_{osc(high)}$	HIGH oscillator frequency	maximum switching frequency in CV and CC mode, without jitter	46	52	58	kHz
Supply (pin VCC)						
$V_{CC(startup)}$	start-up supply voltage		15	17	19	V
$V_{CC(stop)}$	stop supply voltage	undervoltage lockout of IC	7.5	8.5	9.5	V

5. Ordering information

Table 2. Ordering information

Type number	Package		Version
	Name	Description	
TEA1720BT/1	SO8	plastic small outline package; 8 leads; body width 3.9 mm	SOT96-1

6. Block diagram

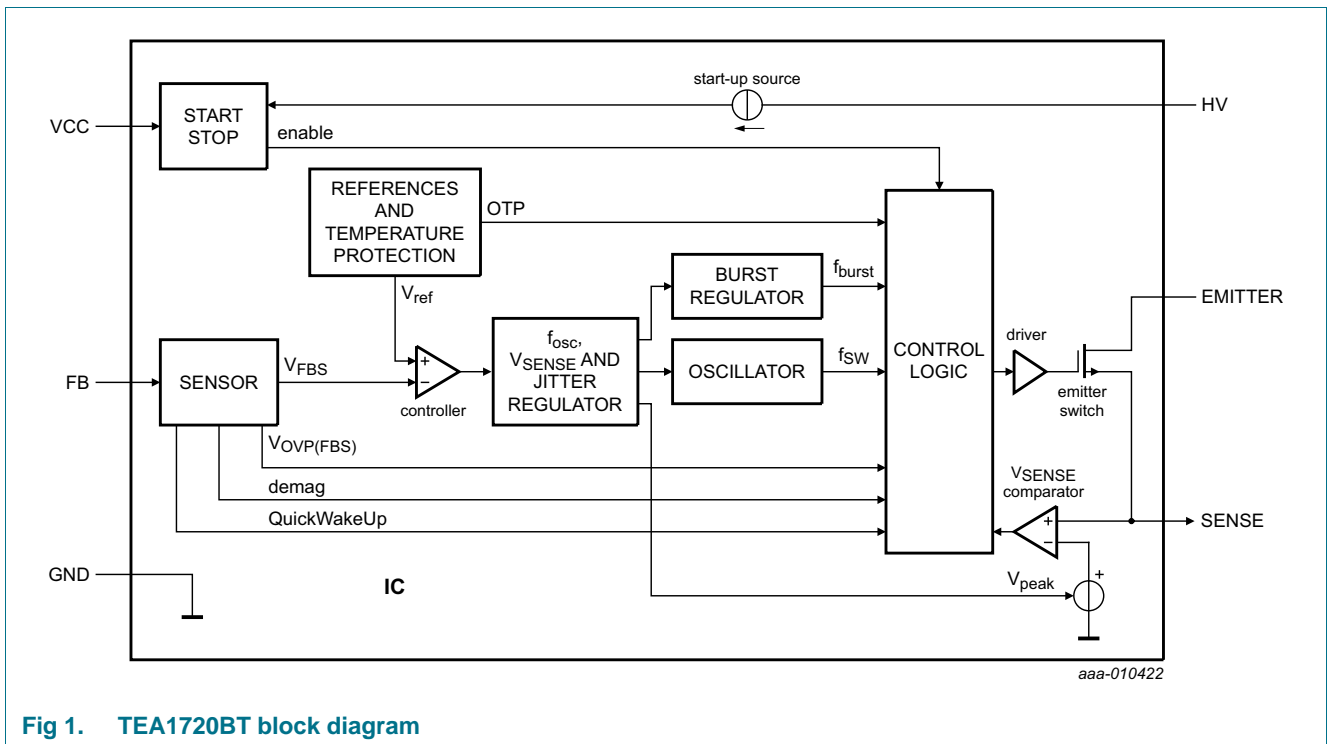


Fig 1. TEA1720BT block diagram

7. Pinning information

7.1 Pinning

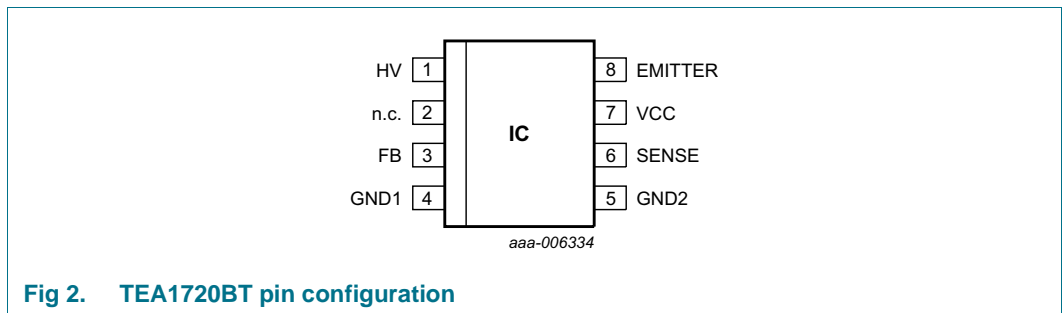


Fig 2. TEA1720BT pin configuration

7.2 Pin description

Table 3. Pin description

Pin	Pin name	I/O type	Description
1	HV	I	high-voltage start-up
2	n.c.	-	not connected; high-voltage spacer
3	FB	I	feedback input for voltage sensing
4	GND1	-	ground
5	GND2	-	ground
6	SENSE	I	source emitter switch and sense input
7	VCC	I	supply input
8	EMITTER	I	emitter connection of external NPN power transistor

8. Functional description

8.1 Start-up

The TEA1720BT starts up by charging the VCC capacitor until the $V_{CC(\text{startup})}$ level. The charging current flows from the HV pin via an internal start-up current source to the VCC pin.

Once the start-up level has been reached the start-up current source is switched off. During the switching operation, the start-up current source remains current-less and has zero bleeder loss.

8.2 Primary sensing

The FB input senses the reflected secondary voltage on the primary side. The FB input has a sample and hold function that samples the FB voltage on the secondary stroke to control the output voltage.

The sampled V_{FBS} voltage is the input for the TEA1720BT's control loop and defines the operating mode.

8.3 Operating modes

The TEA1720BT operates in three modes, one of which is active at the time. The three modes in order of decreasing load impedance are:

- CVB: Constant Voltage with Burst mode
- CV: Constant Voltage mode
- CC: Constant Current mode

The converter acts as a voltage source in CVB and CV modes.

The converter acts as a current source in CC mode.

8.3.1 Constant Voltage with Burst mode (CVB)

At low power, the TEA1720BT operates in Burst mode.

Burst mode operates with a $V_{\text{ref-peak}} = 120 \text{ mV}$, a switching frequency of 23 kHz and burst duty cycle regulation by sensing the FB voltage.

The TEA1720BT features an energy save function that puts the main part of the analogue blocks in a sleep mode with low supply current in burst mode. The burst mode enables the energy save mode in the non-switching part of the burst. The IC switches to the nominal supply just before new burst starts.

Transition from burst mode to CV mode happens at 100 % burst duty cycle: a burst completely filled with 32 pulses. This 100 % pulse train is identical to the lowest power level of the CV mode. The TEA1720BT changes directly from burst mode to CV mode if the FB voltage drops below 2.4 V in burst mode.

8.3.2 Constant Voltage mode (CV)

At higher power levels, the TEA1720BT operates in CV mode. The output voltage is sensed by the FB pin and the control keeps the output voltage constant over the power range.

CV mode starts at 23 kHz switching frequency and peak current regulation at the $V_{\text{ref-peak}}$ minimum level of 120 mV.

With an increasing power output, the $V_{\text{ref-peak}}$ level and the switching frequency are also increased.

CV mode is exited when the maximum power level is reached. Maximum power occurs at peak current regulation at the $V_{\text{ref-peak}}$ maximum level of 575 mV and a maximum switching frequency of 52 kHz.

8.3.3 Constant Current mode (CC)

The CC mode starts at maximum power delivery and keeps the output current constant for decreasing output voltage.

CC mode is enabled when the converter is operating at the maximum switching frequency, with the maximum primary peak current when the FB voltage drops below the regulated level.

Regulation of the switching frequency from 52 kHz down to 23 kHz controls the CC mode operation.

8.4 Jitter

The TEA1720BT features a jitter function for ElectroMagnetic Interference (EMI) reduction. The switching frequency is 7 % typical for the spread spectrum. The sweep frequency is a low frequency of approximately 220 Hz. To keep the output power constant, the V_{SENSE} level is jittered with the opposite polarity. The jitter is active in all operation modes except burst mode.

8.5 Fast transient response

In combination with the TEA1705 transient controller at the secondary side the TEA1720BT can achieve a fast transient response on load steps.

In the Energy save state the TEA1720BT monitors the FB pin. When the device detects a load step a signal is submitted through the transformer that pulls the FB pin below the -0.5 V threshold.

The TEA1720BT wakes up quickly and reacts with the maximum peak current and the switching frequency to keep the drop of the output voltage to its minimum. The device's reaction allows the smallest size output capacitor.

8.6 Short pin SENSE protection

The TEA1720BT has an integrated protection against faults caused by a shorted SENSE pin or a shorted sense resistor. During every primary stroke, the voltage on the SENSE pin is monitored. In normal situations, the voltage on the SENSE pin reaches the sense protection trigger level after the sense protection time has passed. When the SENSE voltage does not reach this level, the SENSE pin is shorted and the protection is activated. The IC stops switching and restarts. The value of the sense protection time is automatically adapted to the mains input voltage.

8.7 Hiccup mode protection

The hiccup mode protects the power supply from damage in case of current overload situations. The output voltage is sensed via the FB pin. If the voltage on the FB pin drops below the $V_{th(hiccup)}$ level for a time exceeding $t_{blank(hiccup)}$, the TEA1720BT stops switching and restarts. As long as the fault condition is present the IC makes repetitive restart attempts. Once the fault condition is removed, normal operation is resumed.

8.8 OverTemperature Protection (OTP)

The IC incorporates an accurate internal temperature protection. When the junction temperature exceeds the overtemperature protection threshold temperature, the IC stops switching. Once the temperature has dropped with the overtemperature protection temperature hysteresis ($T < T_{pl(IC)} - T_{pl(IC)hys}$), the IC restarts.

9. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
General					
T_{amb}	ambient temperature		-40	+85	°C
T_j	junction temperature		-40	+160	°C
T_{stg}	storage temperature		-55	+150	°C
V_{ESD}	electrostatic discharge voltage	CDM; all pins	-500	+500	V
		HBM; all pins [1]	-2000	+2000	V
Voltages					
V_{HV}	voltage on pin HV		-0.3	+550	V
V_{SENSE}	voltage on pin SENSE		-0.3	+5	V
V_{CC}	supply voltage		-0.3	+40	V
$V_{EMITTER}$	voltage on pin EMITTER		-0.3	+40	V

Table 4. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{FB}	voltage on pin FB		-20	+5	V
Currents					
$I_{EMITTER}$	current on pin EMITTER		-1.1	1.1	A
I_{SENSE}	current on pin SENSE		-1.1	1.1	A

[1] Human body model: equivalent to discharging a 100 pF capacitor through a 1.5 k Ω series resistor.

10. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air; JEDEC test board	150	K/W
		in free air; on open PCB of 2.2 cm \times 2.6 cm; 2-layer; 35 μ m Cu	200	K/W

11. Characteristics

Table 6. Characteristics $V_{CC} = 20$ V; $FB = 0$ V; $R_{source} = 0.75$ Ω ; $T_j = 25$ $^{\circ}$ C; all voltages referenced to GND; positive currents flow into the IC, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Supply (pin VCC)						
$I_{CC(startup)}$	start-up supply current	$V_{CC} = 0$ V	-1.5	-1.2	-0.9	mA
$I_{CC(startup)}$	start-up supply current	$V_{CC} = V_{CC(startup)} - 1$	-1.3	-1.0	-0.7	mA
I_{CC}	supply current	$V_{FB} = 2.8$ V; non-switching; in energy save	130	170	210	μ A
		CC mode	0.6	0.8	1.0	mA
$V_{CC(startup)}$	start-up supply voltage		15	17	19	V
$V_{CC(stop)}$	stop supply voltage	undervoltage lockout of IC	7.5	8.5	9.5	V
$T_{pl(IC)}$	IC protection level temperature		140	150	160	$^{\circ}$ C
$T_{pl(IC)hys}$	hysteresis of IC protection level temperature		-	50	-	$^{\circ}$ C
Feedback (pin FB)						
$V_{th(ovp)FB}$	overvoltage protection threshold voltage on pin FB		3.1	3.2	3.3	V
$V_{ref(FB)}$	reference voltage on pin FB	CV mode	2.45	2.5	2.55	V
$V_{th(FB)}$	threshold voltage on pin FB	transition from burst mode to constant voltage	2.35	2.4	2.45	V
$V_{det(demag)FB}$	demagnetization detection voltage on FB pin		25	50	75	mV

Table 6. Characteristics ...continued

$V_{CC} = 20\text{ V}$; $FB = 0\text{ V}$; $R_{source} = 0.75\ \Omega$; $T_j = 25\text{ }^\circ\text{C}$; all voltages referenced to GND; positive currents flow into the IC, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{det(trt)FB}$	transient detection voltage on pin FB	in energy save state	-0.6	-0.5	-0.4	V
$t_d(s)$	sample delay time	moment of sampling V_{FB} after start of secondary stroke; $V_{FB} = 2.35\text{ V}$	4.4	5.4	6.4	μs
Oscillator (pins EMITTER and SENSE)						
f_{burst}	burst mode frequency	burst frequency in CVB mode, without jitter	350	400	450	Hz
f_{jit}/f_{sw}	jitter frequency to switching frequency ratio	in all operation modes except in CVB mode	5	7	9	%
$f_{osc(high)}$	high oscillator frequency	maximum switching frequency in CV and CC mode, without jitter	46	52	58	kHz
$f_{osc(low)}$	low oscillator frequency	minimum switching frequency in CV and CC mode, without jitter. switching frequency in CVB mode	20	23	26	kHz
f_{mod}	modulation frequency	in current source operation	170	220	270	Hz
δ_{max}	maximum duty cycle		72	75	78	%
HV start-up (pin HV)						
$I_{off(HV)}$	off-state current on pin HV	$V_{HV} = 325\text{ V}$	-	1	-	μA
Emitter switch (pin EMITTER)						
R_{on}	on-state resistance	$I_{ds} = 100\text{ mA}$	0.75	0.85	1.0	Ω
Peak current comparator (pin SENSE)						
t_{PD}	propagation delay	$dV/dt = 0.2\text{ V}/\mu\text{s}$	-	100	-	ns
t_{leb}	leading edge blanking time		270	320	370	ns
$V_{sense(high)}$	high sense voltage	maximum peak voltage in CV and CC modes, without jitter; $V_{FB} = -10\text{ V}$	540	575	610	mV
$V_{sense(low)}$	low sense voltage	CVB mode; $V_{FB} = -10\text{ V}$	100	120	140	mV
$V_{sense}^2 f_{osc}$	square sense voltage times oscillator frequency	$V_{sense(high)}^2 * f_{osc(high)}$; in CV and CC modes, without jitter; $V_{FB} = -10\text{ V}$	15.8	17.0	18.2	V^2kHz
Sense pin short protection (pin SENSE)						
$V_{scp(high)}$	high short-circuit protection voltage		-	125	-	mV
$V_{scp(low)}$	low short-circuit protection voltage on pin SENSE		-	105	-	mV
$t_{blank(scp)SENSE}$	short-circuit protection blanking time on pin SENSE	$V_{FB} = -10\text{ V}$	1	1.35	1.7	μs
Hiccup protection (pin FB)						
$V_{th(hiccup)}$	hiccup release threshold voltage		1.0	1.1	1.2	V
$V_{th(rel)(hiccup)}$	hiccup release voltage		1.3	1.4	1.5	V
$t_{blank(hiccup)}$	hiccup blanking time	at minimum switching frequency	15	21	27	ms

12. Application information

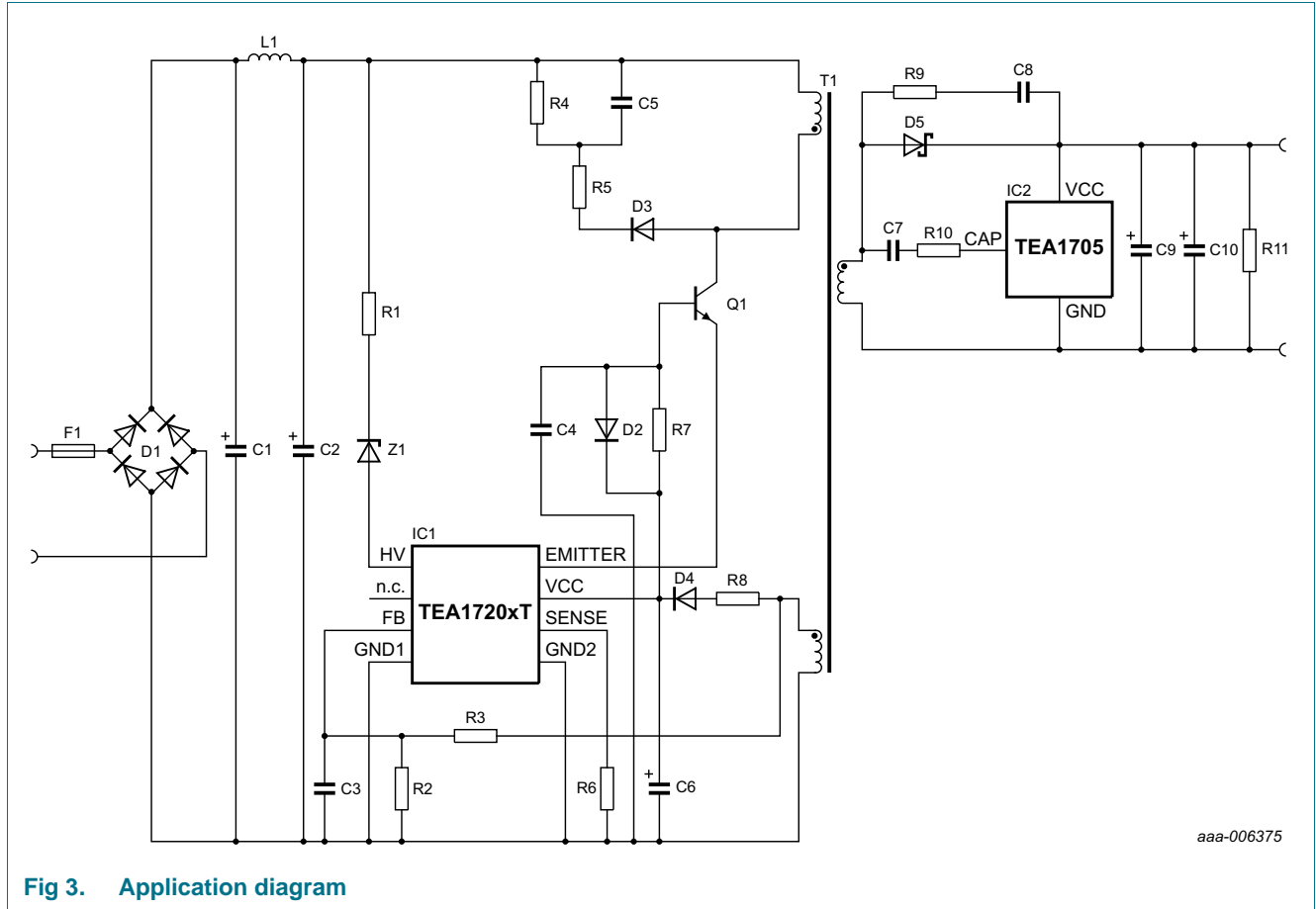


Fig 3. Application diagram

13. Package outline

SO8: plastic small outline package; 8 leads; body width 3.9 mm

SOT96-1

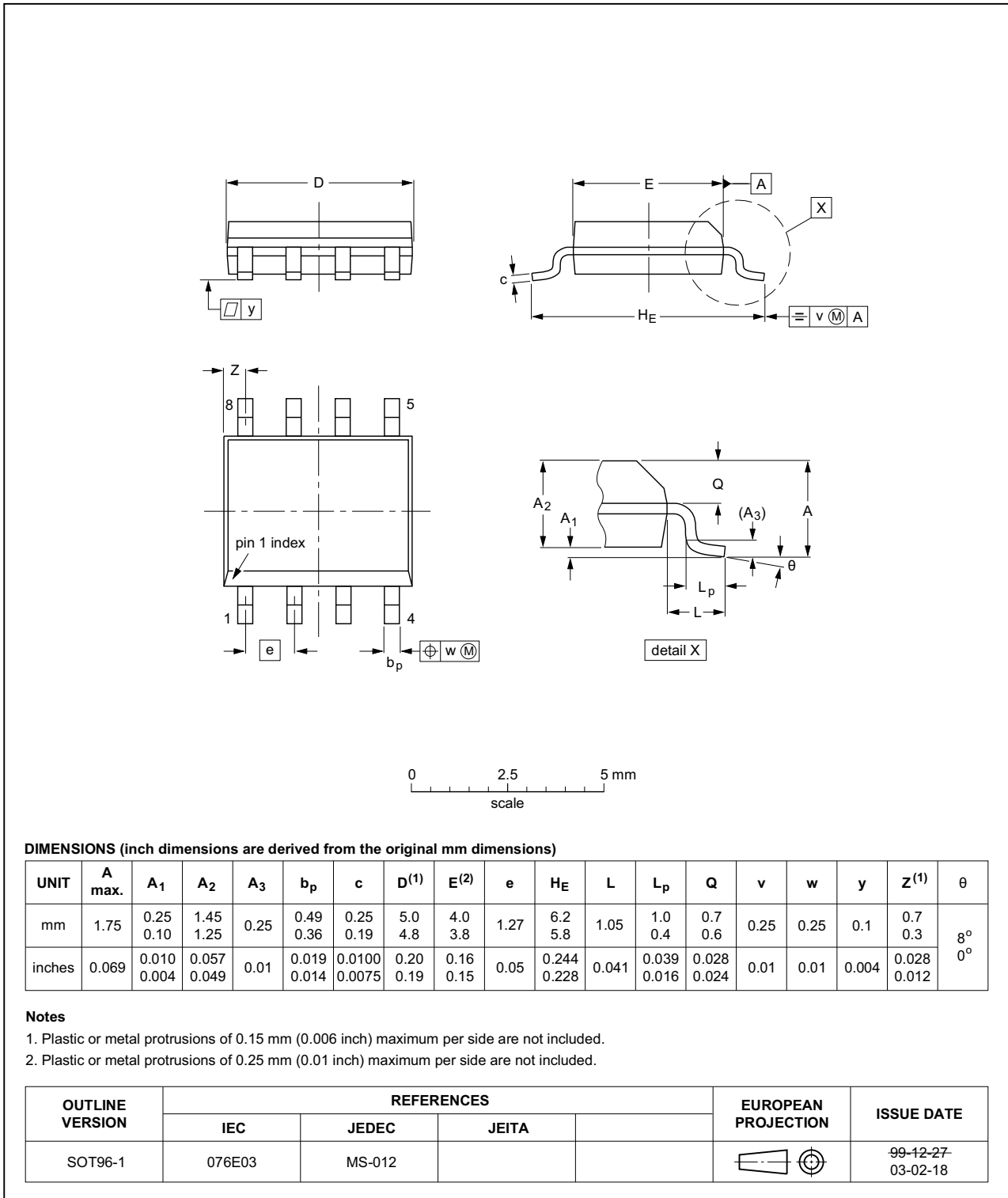


Fig 4. Package outline SOT96-1 (SO8)

14. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
TEA1720BT v.2	20140307	Product data sheet	-	TEA1720BT v.1
Modifications:	• The data sheet status has changed from objective to product.			
TEA1720BT v.1	20131204	Objective data sheet	-	-

15. Legal information

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Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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