PIN CONFIGURATION

S	1	56	NC
1A1	2	55	NC
NC	3	54	1B1
2A1	4	53	1B2
NC	5	52	2B1
3A1	6	51	2B2
NC	7	50	3B1
GND	8	49	GND
4A1	9	48	3B2
NC	10	47	4B1
5A1	11	46	4B2
NC	12	45	5B1
6A1	13	44	5B2
NC	14	43	6B1
7A1	15	42	6B2
NC	16	41	7B1
Vcc	17	40	7B2
8A1	18	39	8B1
GND	19	38	GND
NC	20	37	8B2
9A1	21	36	9B1
NC	22	35	9B2
10A1	23	34	10B1
NC	24	33	10B2
11A1	25	32	11B1
NC	26	31	11B2
12A1	27	30	12B1
NC	28	29	12B2

TOP VIEW

Package Type	Package Code	Order Code
TSSOP	PAG56	PAG

ABSOLUTE MAXIMUM RATINGS[1]

Symbol	Description	Max.	Unit
V_{CC}	Supply Voltage Range	−0.5 to 4.6	V
V_I	Input Voltage Range	−0.5 to 4.6	V
	Continuous Channel Current	128	mA
I_{IK}	Input Clamp Current, $V_{I/O} < 0$	−50	mA
T_{STG}	Storage Temperature Range	−65 to +150	°C

NOTE:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

PIN DESCRIPTION

Pin Names	Description
S	Select Input
xAx	Port A Inputs or Outputs
xBx	Port B Inputs or Outputs

FUNCTION TABLE[1]

Input	Operation
S	
L	A Port = B1 Port $R_{PULLDOWN}$ = B2 Port
H	A Port = B2 Port $R_{PULLDOWN}$ = B1 Port

NOTE:
1. H = HIGH Voltage Level
   L = LOW Voltage Level

OPERATING CHARACTERISTICS[1]

Symbol	Parameter	Test Conditions	Min.	Max.	Unit
V_{CC}	Supply Voltage		2.3	3.6	V
V_{IH}	High-Level Control Input Voltage	V_{CC} = 2.3V to 2.7V	1.7	—	V
		V_{CC} = 2.7V to 3.6V	2	—	
V_{IL}	Low-Level Control Input Voltage	V_{CC} = 2.3V to 2.7V	—	0.7	V
		V_{CC} = 2.7V to 3.6V	—	0.8	
T_A	Operating Free-Air Temperature		−40	+85	°C

NOTE:
1. All unused control inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Operating Condition: T_A = –40°C to +85°C

Symbol	Parameter	Test Conditions		Min.	Typ.[1]	Max.	Unit
V_{IK}	Control Inputs, Data I/O	V_{CC} = 3V, I_I = –18mA		—	—	–1.2	V
I_I	Control Inputs	V_{CC} = 3.6V, V_I = V_{CC} or GND		—	—	±1	µA
I_{OFF}		V_{CC} = 0V, V_I or V_O = 0V or 3.6V		—	—	10	µA
I_{CC}		V_{CC} = 3.6V, I_O = 0, V_I = V_{CC} or GND		—	—	10	µA
ΔI_{CC}[2]	Control Inputs	V_{CC} = 3.6V, one input at 3V, other inputs at V_{CC} or GND		—	—	300	µA
C_I	Control Inputs	V_I = 3.3V or 0		—	3.5	—	pF
$C_{IO(OFF)}$	A port or B port	V_O = 3.3V or 0		—	22.5	—	pF
R_{ON}[3]	Max. at V_{CC} = 2.3V	V_I = 0	I_O = 64mA	—	5	8	Ω
	Typ. at V_{CC} = 2.5V		I_O = 24mA	—	5	8	
		V_I = 1.7V	I_O = 15mA	—	11	40	
	V_{CC} = 3V	V_I = 0	I_O = 64mA	—	3	7	
			I_O = 24mA	—	3	7	
		V_I = 2.4V	I_O = 15mA	—	7	15	

NOTES:
1. Typical values are at 3.3V, +25°C ambient.
2. The increase in supply current is attributable to each input that is at the specified voltage level rather than V_{CC} or GND.
3. This is measured by the voltage drop between the A and B terminals at the indicated current through the switch.

SWITCHING CHARACTERISTICS

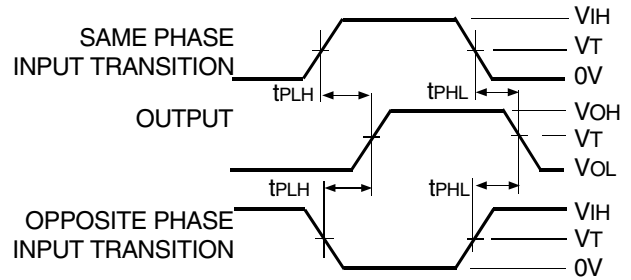| Symbol	Parameter	V_{CC} = 2.5V ± 0.2V		V_{CC} = 3.3V ± 0.3V		Unit
		Min.	Max.	Min.	Max.	
t_{PD}[1]	Propagation Delay A to B or B to A	—	0.15	—	0.25	ns
t_{PD}[2]	Propagation Delay S to A	2.5	7.1	2.5	6.7	ns
t_{EN}	Output Enable Time S to B	1	5.6	1	5	ns
t_{DIS}	Output Disable Time S to B	1	5	1	4.5	ns
$t_{MB/B}$[3,4]	Make-Before-Break Time	0	2	0	2	ns

NOTES:
1. The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance when driven by an ideal voltage source (zero output impedance).
2. The condition to measure this propagation delay is by observing the change of voltage on the A port introduced by static fields equal to 3V or 0V for 3.3V±0.3V or V_{CC} or 0 for 2.5V±0.2V on B_1 and B_2 ports to get the required transition.
3. The make-before-break time is the duration between the make and break, during transition from one selected port to another.
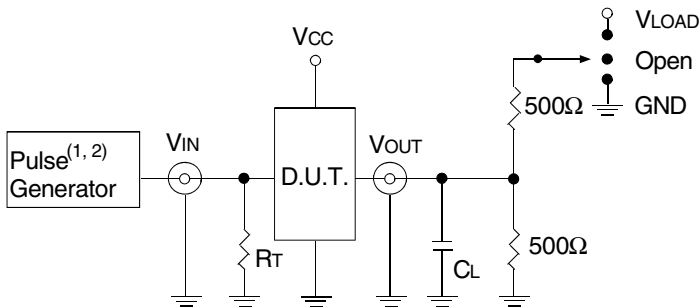4. This parameter is guaranteed by design but not production tested.

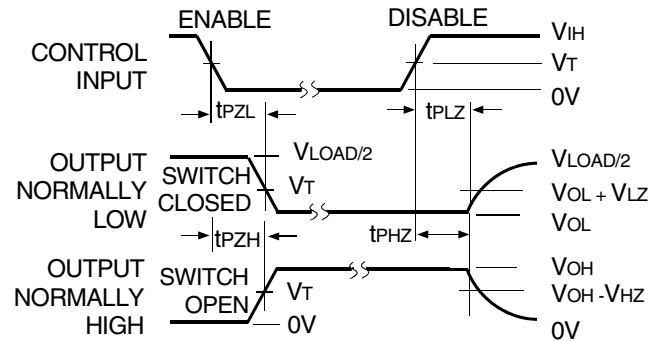# TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	$V_{CC}^{(1)}$ = 3.3V±0.3V	$V_{CC}^{(2)}$ = 2.5V±0.2V	Unit
V_{LOAD}	6	2 x V_{CC}	V
V_{IH}	3	V_{CC}	V
V_T	1.5	V_{CC} / 2	V
V_{LZ}	300	150	mV
V_{HZ}	300	150	mV
C_L	50	30	pF

Propagation Delay

Test Circuits for All Outputs

DEFINITIONS:

C_L = Load capacitance: includes jig and probe capacitance.

R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator.

NOTES:
1. Pulse Generator for All Pulses: Rate ≤ 10MHz; t_F ≤ 2.5ns; t_R ≤ 2.5ns.
2. Pulse Generator for All Pulses: Rate ≤ 10MHz; t_F ≤ 2ns; t_R ≤ 2ns.

NOTES:
1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.
2. Disable Low waveform applies to outputs that are LOW, except when disabled by the output control S.

Enable and Disable Times

SWITCH POSITION

Test	Switch
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND
t_{PD}	Open

ORDERING INFORMATION

XX	CBTLV	XXX	XX	X	X
Temp. Range		Device Type	Package		

Blank	Tube
8	Tape and Reel
G	Green
PA	Thin Shrink Small Outline Package (PAG56)
16292	Low-Voltage 12-Bit Mux / Demux Bus Switch with Internal Pull Down Resistors
74	−40°C to +85°C
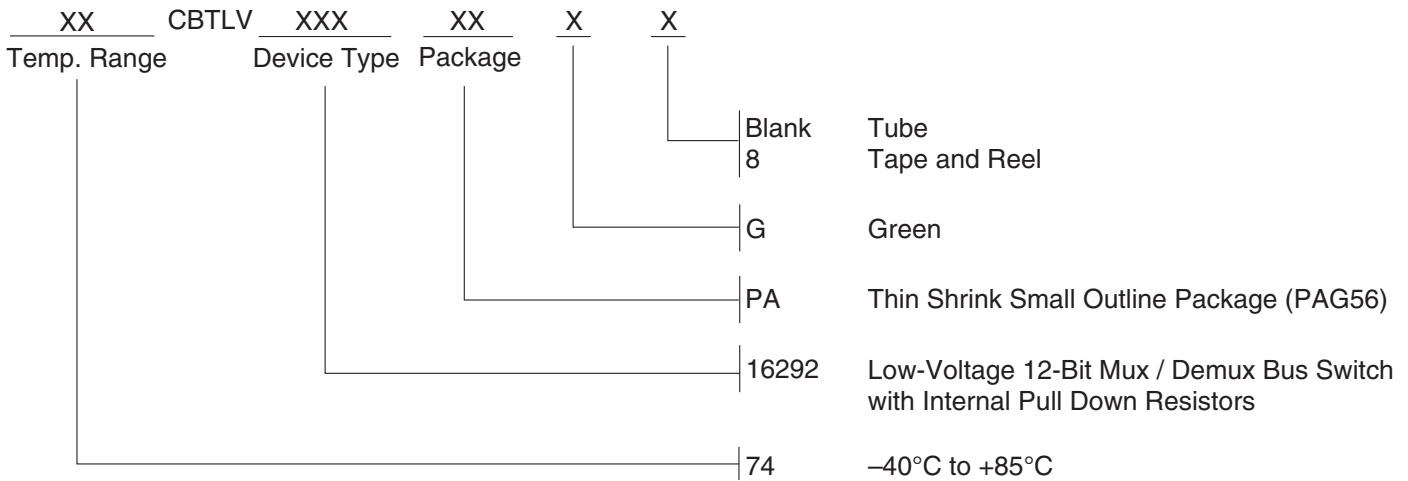
Orderable Part Information

Speed (ns)	Orderable Part ID	Pkg. Code	Pkg. Type	Temp. Grade
	74CBTLV16292PAG	PAG56	TSSOP	I
	74CBTLV16292PAG8	PAG56	TSSOP	I

Datasheet Document History

12/04/2014	Pg. 5	Updated the ordering information by removing the "IDT" notation and non RoHS part and by adding Tape and Reel information.
06/01/2019	Pg. 2,5	Added table under pin configuration diagram with detailed package information and orderable part information table. Updated the ordering information diagram in clearer detail.

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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

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