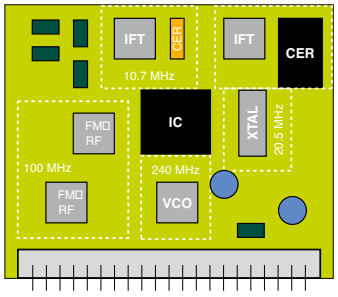
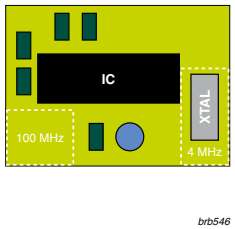
as well as the expensive external components needed for LNA and AGC in the AM and FM front-ends. The TEF662x family also integrates a VCO, so there's no need for external LC tank components.

The low-IF architecture simplifies PCB layout, since there aren't any critical high-frequency lines and there's no need for alignments. Even designers with limited RF know-how or resources can design the TEF662x onto the main PCB board, saving significant costs by making a tuner module design obsolete. Manufacturing is also easier, since full SMD applications, without hand mounting, are possible. Production throughput can be higher, too, because there are fewer small external components to handle, and there are no mechanical or software alignments to be made.

Space savings delivered by TEF662x devices

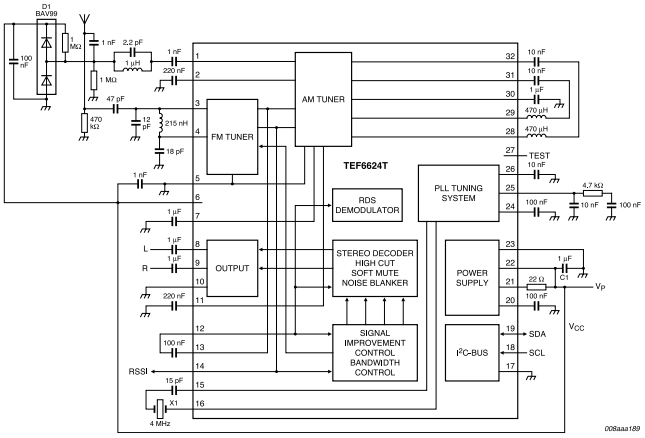


Conventional Tuner Module



Tiger main board application

TEF662x block and low-cost application diagram



TEF662x selection guide

Type number		TEF6621	TEF6623	TEF6624
System	Power supply	8.5 V	8.5 V	8.5 V
	Output	L/R, MPX	L/R, MPX, RDS	L/R, MPX, RDS
	PACS	-	•	•
R(B)DS	Demodulator / decoder	-/-	• / -	• / -
FM	Japan / EU / US bands	•	•	•
	OIRT	-	•	•
	NB	•	•	•
	Stereo decoder	•	•	•
	Weak signal handling	•	•	•
AM	LW / MW	•	-	•
	SW	-	-	-
	Weak signal handling	•	-	•
General	Package	SO32	SO32	SO32
	T_{amb}	-20 to 85 °C	-20 to 85 °C	-20 to 85 °C
	Automotive	No	No	No
	Product release	Available	Available	Available

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