

electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS			MIN	TYP	MAX	UNIT
I_{DRM} Repetitive peak off-state current	V_D = rated V_{DRM}	$R_{GK} = 1 \text{ k}\Omega$	$T_C = 110^\circ\text{C}$			400	μA
I_{RRM} Repetitive peak reverse current	V_R = rated V_{RRM}	$I_G = 0$	$T_C = 110^\circ\text{C}$			1	mA
I_{GT} Gate trigger current	$V_{AA} = 12 \text{ V}$	$R_L = 100 \Omega$	$t_{p(g)} \geq 20 \mu\text{s}$	0.2	0.5	1	mA
V_{GT} Gate trigger voltage	$V_{AA} = 12 \text{ V}$ $t_{p(g)} \geq 20 \mu\text{s}$	$R_L = 100 \Omega$ $R_{GK} = 1 \text{ k}\Omega$	$T_C = -40^\circ\text{C}$			1.2	V
	$V_{AA} = 12 \text{ V}$ $t_{p(g)} \geq 20 \mu\text{s}$	$R_L = 100 \Omega$ $R_{GK} = 1 \text{ k}\Omega$		0.4	0.6	1	
	$V_{AA} = 12 \text{ V}$ $t_{p(g)} \geq 20 \mu\text{s}$	$R_L = 100 \Omega$ $R_{GK} = 1 \text{ k}\Omega$	$T_C = 110^\circ\text{C}$	0.2			
	$V_{AA} = 12 \text{ V}$ $t_{p(g)} \geq 20 \mu\text{s}$	$R_{GK} = 1 \text{ k}\Omega$					
I_H Holding current	$V_{AA} = 12 \text{ V}$ Initiating $I_T = 20 \text{ mA}$	$R_{GK} = 1 \text{ k}\Omega$	$T_C = -40^\circ\text{C}$		3.5	15	mA
	$V_{AA} = 12 \text{ V}$ Initiating $I_T = 20 \text{ mA}$	$R_{GK} = 1 \text{ k}\Omega$			2	10	
V_T On-state voltage	$I_T = 5 \text{ A}$	(see Note 6)			1.3	1.7	V
dv/dt Critical rate of rise of off-state voltage	V_D = rated V_D	$R_{GK} = 1 \text{ k}\Omega$	$T_C = 110^\circ\text{C}$		20		$\text{V}/\mu\text{s}$

NOTE 6: This parameter must be measured using pulse techniques, $t_p = 300 \mu\text{s}$, duty cycle $\leq 2\%$. Voltage sensing-contacts, separate from the current carrying contacts, are located within 3.2 mm from the device body.

thermal characteristics

PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$ Junction to case thermal resistance			3.5	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$ Junction to free air thermal resistance			62.5	$^\circ\text{C}/\text{W}$

PRODUCT INFORMATION

THERMAL INFORMATION

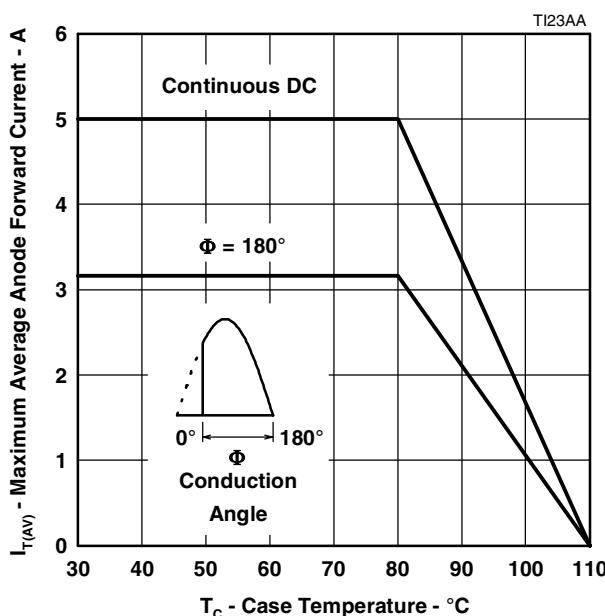
AVERAGE ANODE ON-STATE CURRENT
DERATING CURVE

Figure 1.

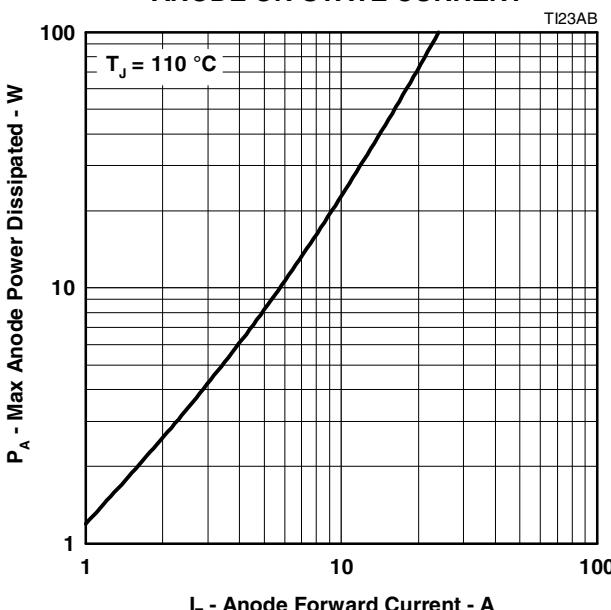
MAX ANODE POWER DISSIPATED
vs
ANODE ON-STATE CURRENT

Figure 2.

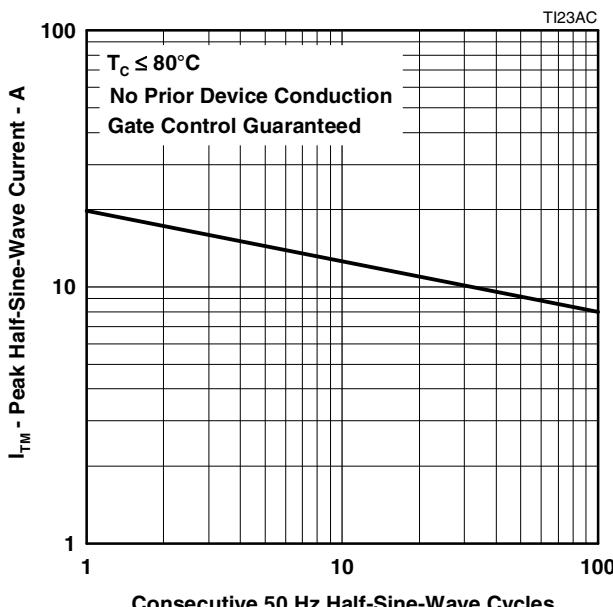
SURGE ON-STATE CURRENT
vs
CYCLES OF CURRENT DURATION

Figure 3.

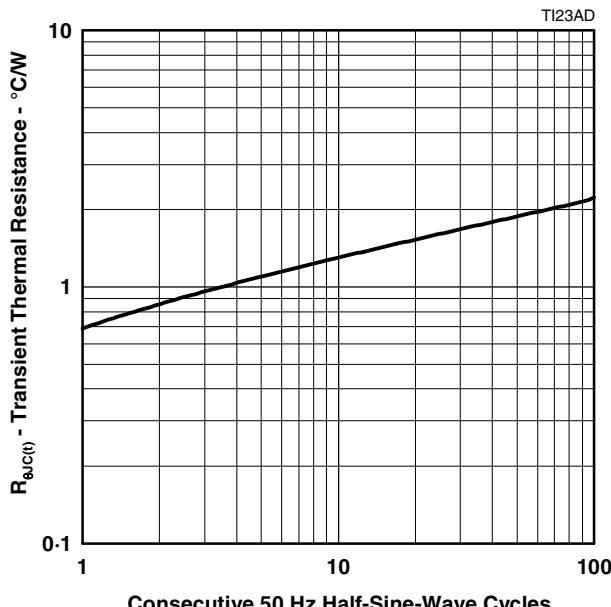
TRANSIENT THERMAL RESISTANCE
vs
CYCLES OF CURRENT DURATION

Figure 4.

PRODUCT INFORMATION

TYPICAL CHARACTERISTICS

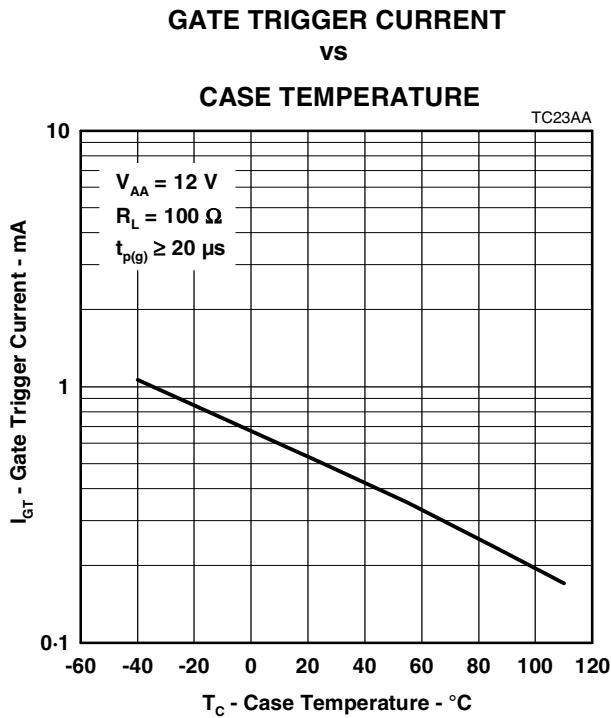


Figure 5.

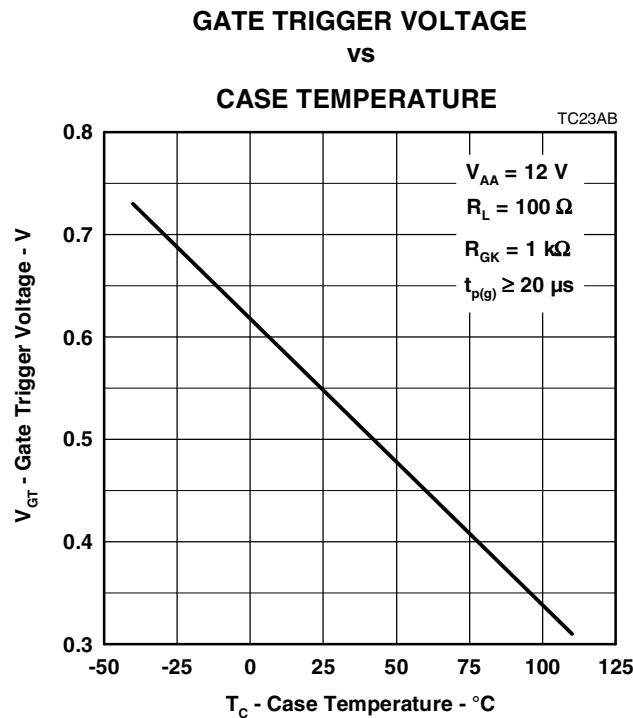


Figure 6.

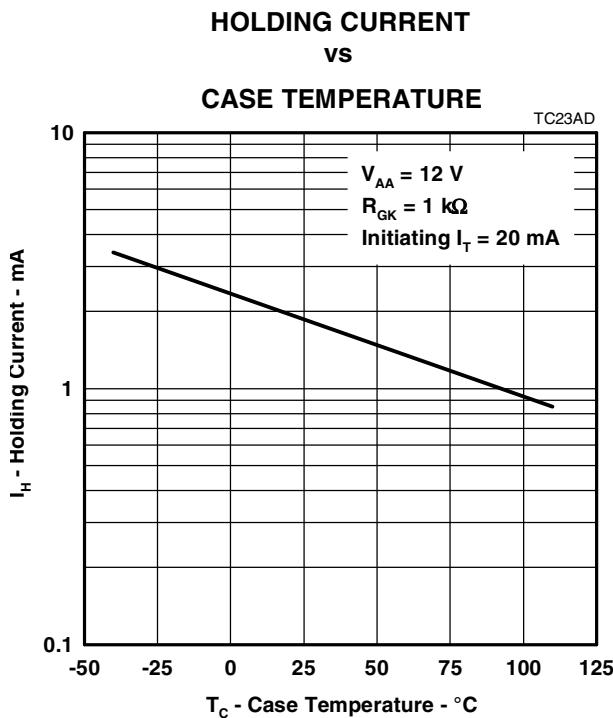


Figure 7.

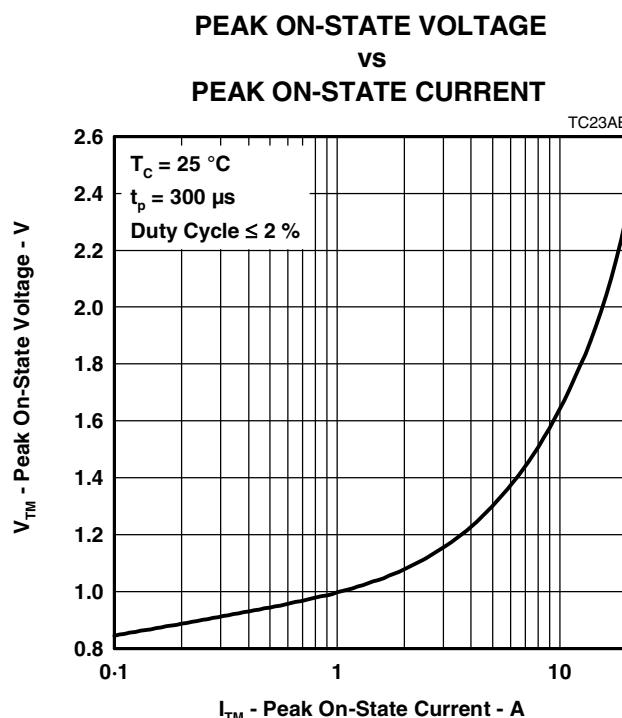


Figure 8.

PRODUCT INFORMATION