

Maximum Ratings (@ $T_A = +25^\circ\text{C}$ unless otherwise specified.)

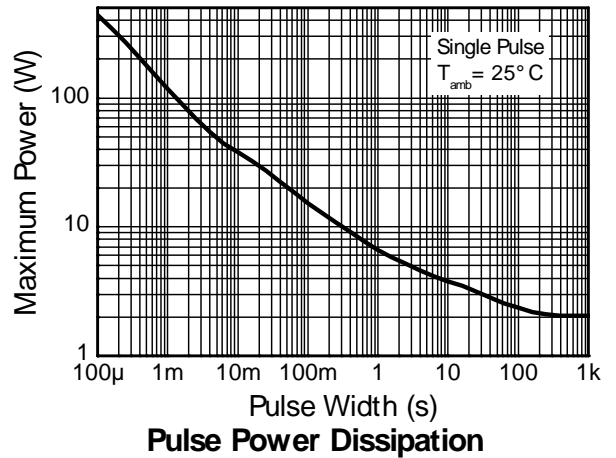
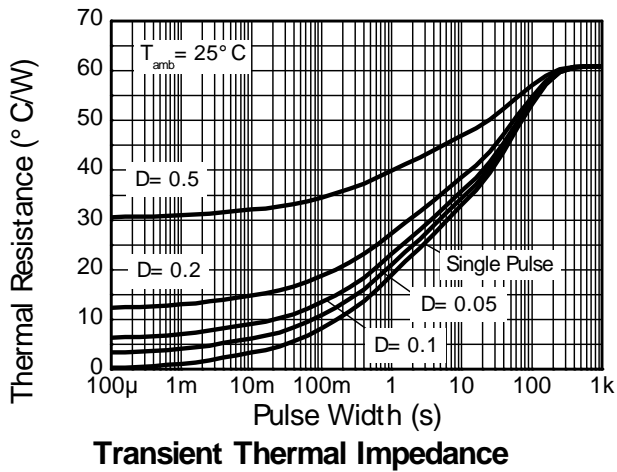
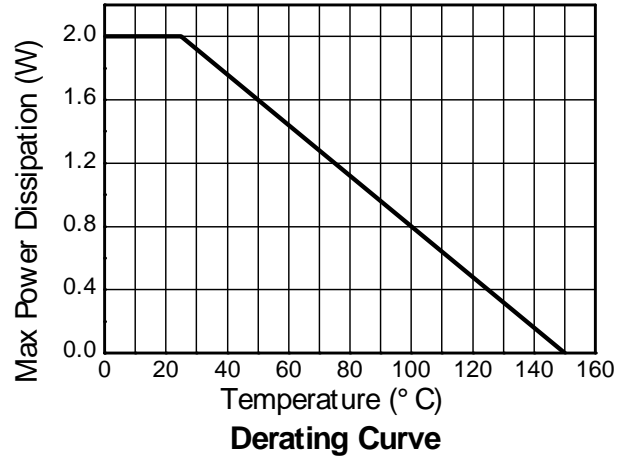
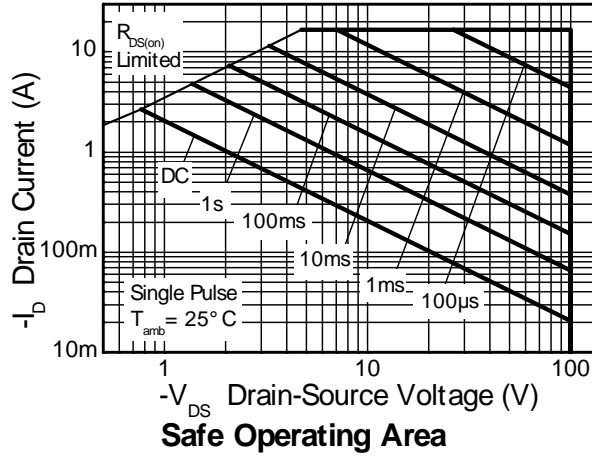
Characteristic			Symbol	Value	Unit	
Drain-Source Voltage			V_{DSS}	-100	V	
Gate-Source Voltage			V_{GSS}	± 20	V	
Continuous Drain Current	$V_{GS} = 10\text{V}$	(Note 6)	I_D	-3.7	A	
		$T_A = +70^\circ\text{C}$ (Note 6)		-3.0		
		(Note 5)		-2.6		
Pulsed Drain Current	$V_{GS} = 10\text{V}$	(Note 7)	I_{DM}	-16.5	A	
Continuous Source Current (Body diode)			(Note 6)	I_S	-3.7	A
Pulsed Source Current (Body diode)			(Note 7)	I_{SM}	-16.5	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$ unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	P_D	2.0	W
			16	
Linear Derating Factor	(Note 6)		3.9	$\text{mW}/^\circ\text{C}$
			31	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
	(Note 6)		32.2	
Thermal Resistance, Junction to Lead	(Note 8)	$R_{\theta JL}$	7.65	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

- Notes:
5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
 6. Same as Note 5, except the device is measured at $t \leq 10$ seconds.
 7. Same as Note 5, except the device is pulsed with $D = 0.02$ and pulse width 300 μs . The pulse current is limited by the maximum junction temperature.
 8. Thermal resistance from junction to solder-point (at the end of the drain lead).

Thermal Characteristics

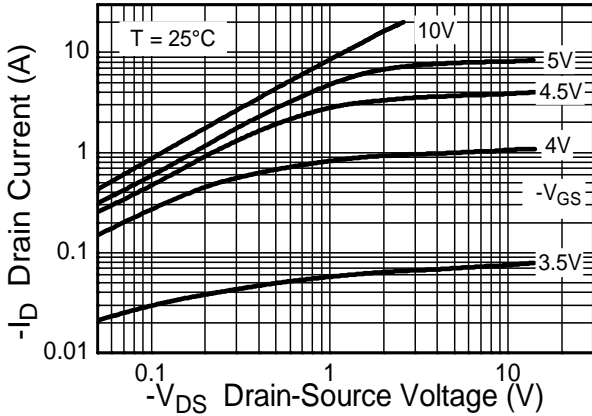


Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

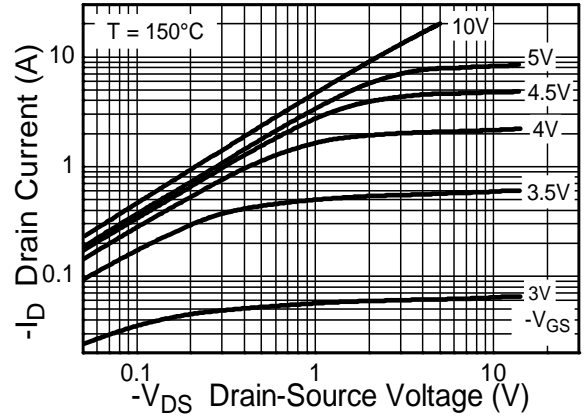
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	-100	—	—	V	I _D = -250μA, V _{GS} = 0V
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -100V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	-2.0	—	-4.0	V	I _D = -250μA, V _{DS} = V _{GS}
Static Drain-Source On-Resistance (Note 9)	R _{DS(on)}	—	—	150	mΩ	V _{GS} = -10V, I _D = -2.8A
				190		V _{GS} = -6V, I _D = -2.4A
Forward Transconductance (Notes 9 & 10)	g _{fs}	—	6.0	—	S	V _{DS} = -15V, I _D = -2.8A
Diode Forward Voltage (Note 9)	V _{SD}	—	-0.85	-0.95	V	I _S = -3.5A, V _{GS} = 0V, T _J = +25°C
Reverse Recovery Time (Note 10)	t _{rr}	—	49	—	ns	I _S = -2.8A, di/dt = 100A/μs,
Reverse Recovery Charge (Note 10)	Q _{rr}	—	107	—	nC	T _J = +25°C
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C _{iSS}	—	1055	—	pF	V _{DD} = -50V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	90	—	pF	
Reverse Transfer Capacitance	C _{rSS}	—	76	—	pF	
Total Gate Charge (Note 11)	Q _g	—	26.9	—	nC	V _{GS} = -10V, V _{DS} = -50V I _D = -2.8A
Gate-Source Charge (Note 11)	Q _{gs}	—	3.9	—	nC	
Gate-Drain Charge (Note 11)	Q _{gd}	—	10.2	—	nC	
Turn-On Delay Time (Note 11)	t _{D(on)}	—	4.6	—	ns	V _{DD} = -50V, V _{GS} = -10V I _D = -1A, R _G ≅ 6.0Ω
Turn-On Rise Time (Note 11)	t _r	—	6.8	—	ns	
Turn-Off Delay Time (Note 11)	t _{D(off)}	—	33.9	—	ns	
Turn-Off Fall Time (Note 11)	t _f	—	17.9	—	ns	

- Notes:
9. Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%
 10. For design aid only, not subject to production testing.
 11. Switching characteristics are independent of operating junction temperatures.

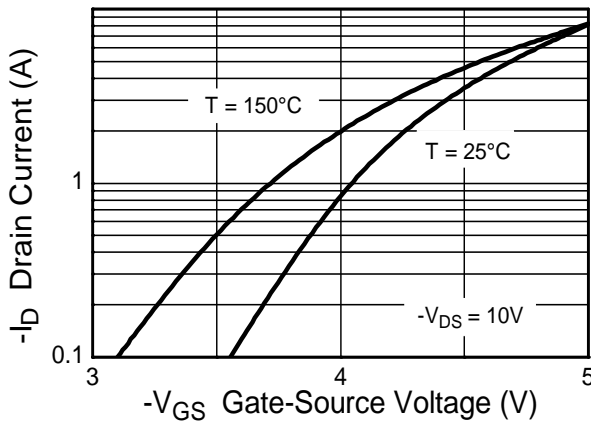
Typical Characteristics



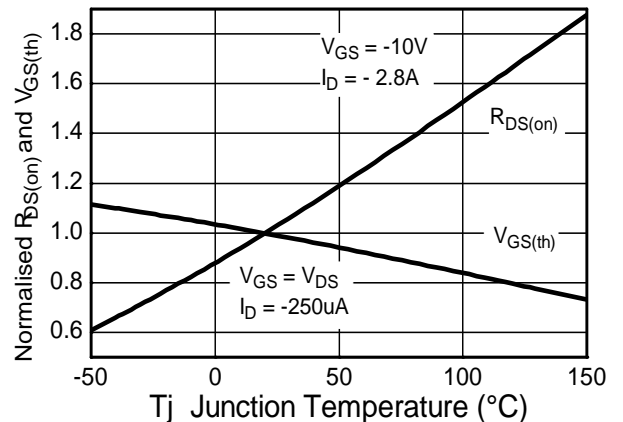
Output Characteristics



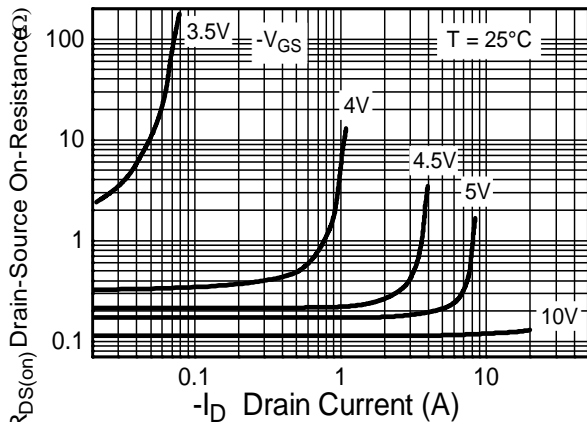
Output Characteristics



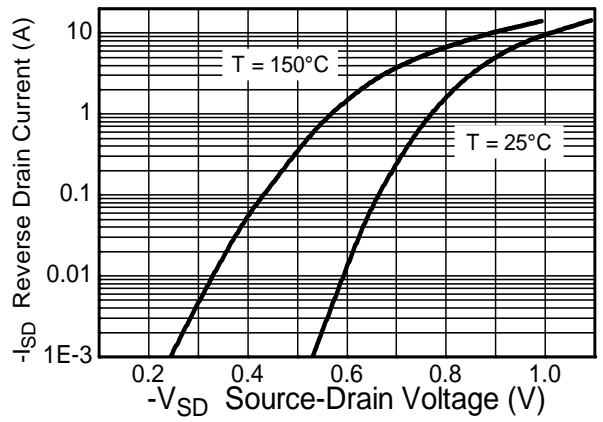
Typical Transfer Characteristics



Normalised Curves v Temperature

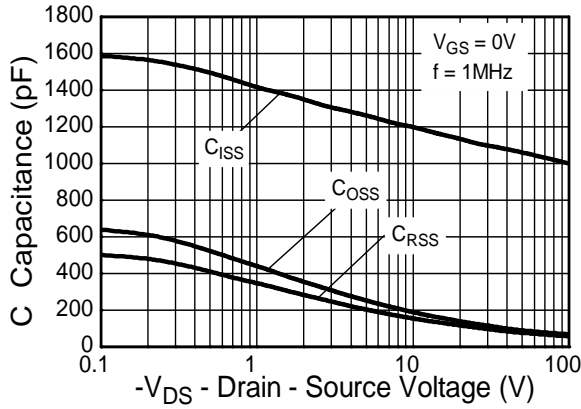


On-Resistance v Drain Current

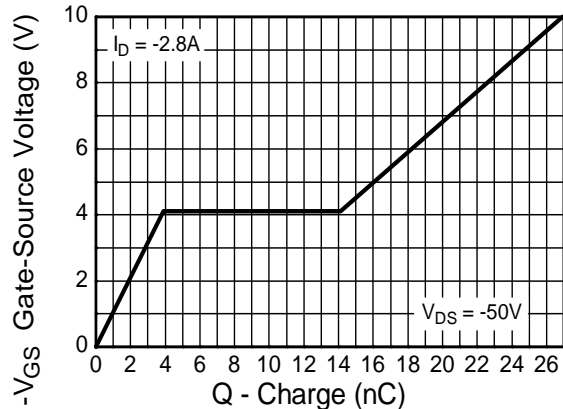


Source-Drain Diode Forward Voltage

Typical Characteristics (continued)

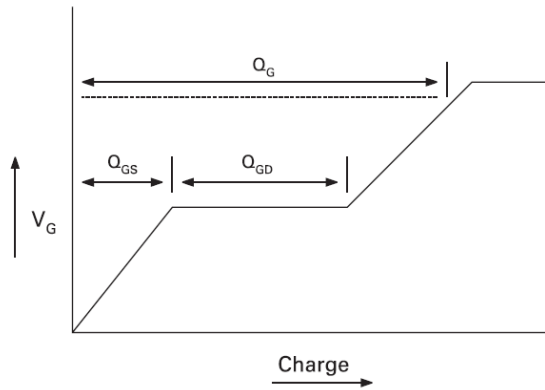


Capacitance v Drain-Source Voltage

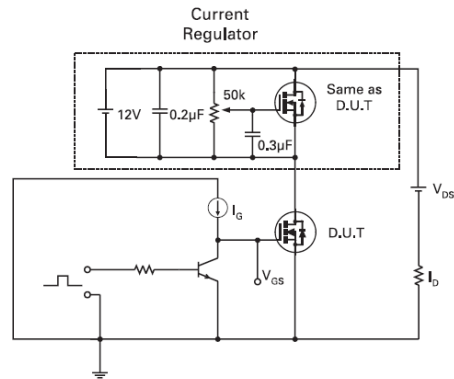


Gate-Source Voltage v Gate Charge

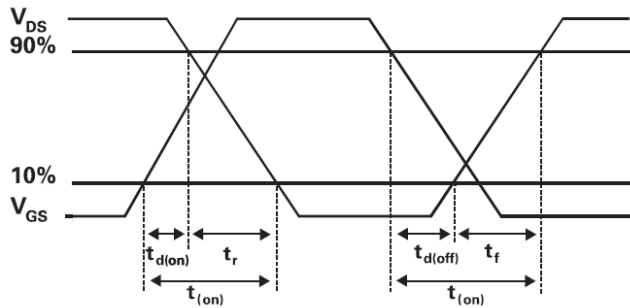
Test Circuits



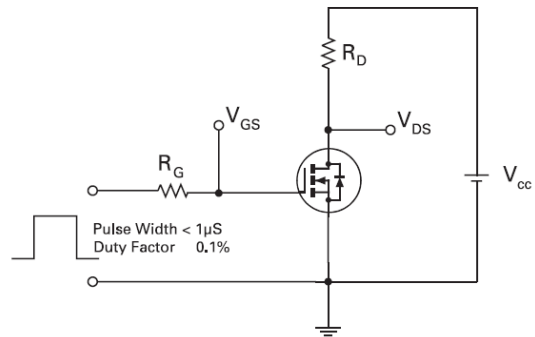
Basic Gate Charge Waveform



Gate Charge Test Circuit



Switching Time Waveforms

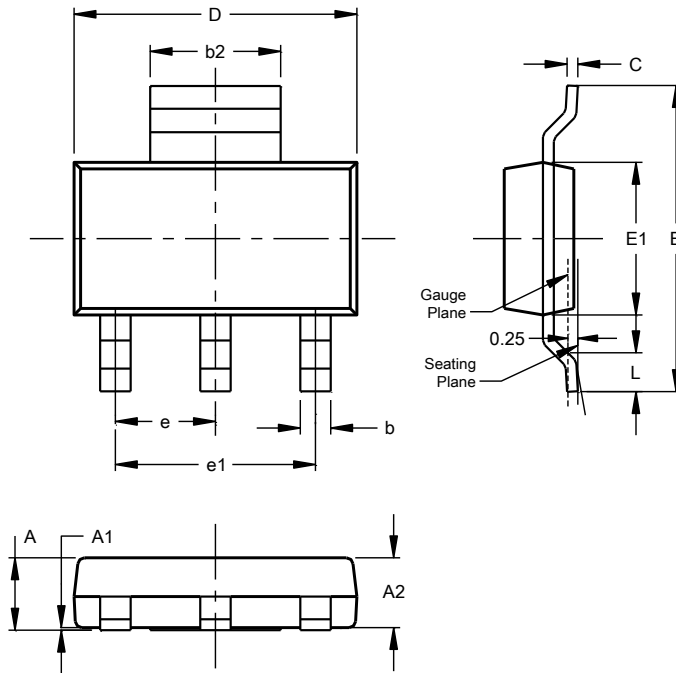


Switching Time Test Circuit

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)

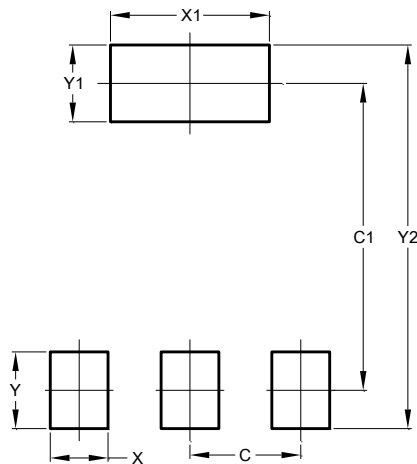


SOT223 (Type DN)			
Dim	Min	Max	Typ
A	--	1.70	--
A1	0.01	0.15	--
A2	1.50	1.68	1.60
b	0.60	0.80	0.70
b2	2.90	3.10	--
c	0.20	0.32	--
D	6.30	6.70	--
E	6.70	7.30	--
E1	3.30	3.70	--
e	--	--	2.30
e1	--	--	4.60
L	0.85	--	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

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