THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction-to-lead thermal resistance	R _{OJL}	15	°C/W	
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	40	°C/W	

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage per diode ⁽¹⁾	DBLS151G DBLS152G DBLS153G DBLS154G DBLS155G DBLS156G DBLS157G	I _F = 1.5A, T _J = 25°C	V _F	-	1.10	V
	DBLS158G DBLS159G			-	1.25	V
Reverse current @ rated V _R per diode ⁽²⁾		T _J = 25°C	- I _R	-	2	μΑ
		T _J = 125°C		-	500	μΑ

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE ⁽¹⁾⁽²⁾	PACKAGE	PACKING	
DBLS15xG	DBLS	1,500 / Tape & Reel	
DBLS15xGH	DBLS	1,500 / Tape & Reel	

Notes:

- 1. "x" defines voltage from 50V(DBLS151G) to 1400V(DBLS159G)
- 2. "H" means AEC-Q101 qualified



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.1 Forward Current Derating Curve

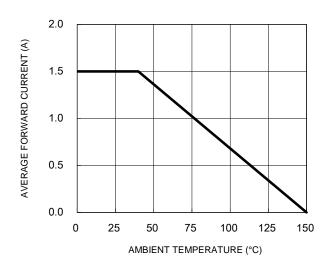


Fig.3 Typical Reverse Characteristics

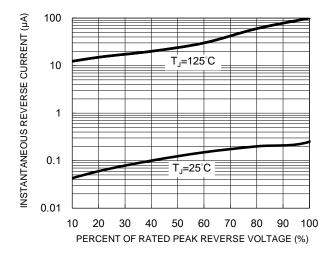


Fig.2 Typical Junction Capacitance

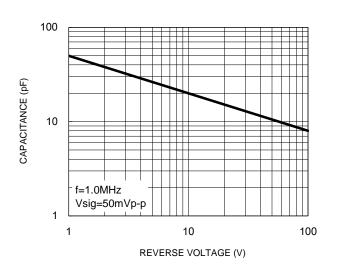


Fig.4 Typical Forward Characteristics

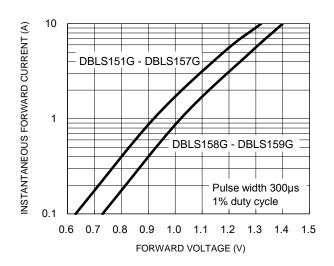
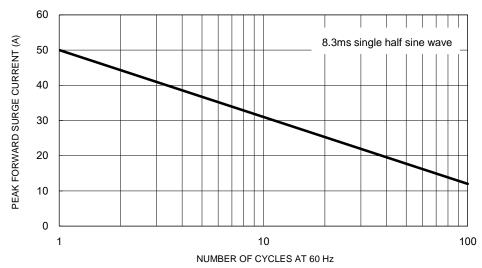
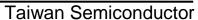


Fig.5 Maximum Non-Repetitive Forward Surge Current

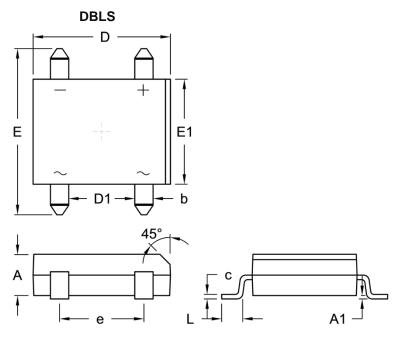


3



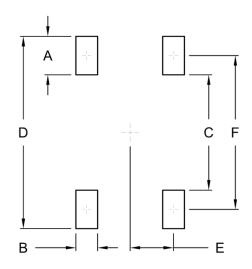


PACKAGE OUTLINE DIMENSIONS



DIM.	Unit (mr		Unit (mm)		Unit (inch)	
DIN.	Min.	Max.	Min.	Max.		
Α	2.40	2.60	0.094	0.102		
A1	0.076	0.330	0.003	0.013		
b	1.02	1.20	0.040	0.047		
С	0.22	0.33	0.009	0.013		
D	8.13	8.51	0.320	0.335		
D1	3.90	4.10	0.154	0.161		
E	9.80	10.30	0.386	0.406		
E1	6.20	6.50	0.244	0.256		
е	5.00	5.20	0.197	0.205		
L	1.02	1.53	0.040	0.060		

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	2.30	0.091
В	1.30	0.051
С	6.90	0.272
D	11.50	0.453
E	2.60	0.102
F	9.20	0.362

MARKING DIAGRAM



P/N = Marking Code

G = Green Compound

YW = Date CodeF = Factory Code



Taiwan Semiconductor

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