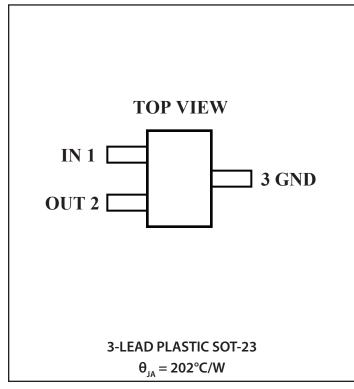
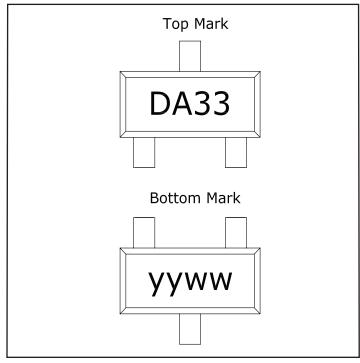


Pin Configuration



Marking Information



Ordering Information

Device	Package
SC4437SK-3.3TRT	3-Lead Plastic SOT-23
SC4437EVB	Evaluation Board

Notes:

- (1) Available in tape and reel only. A reel contains 3,000 devices.
- (2) Pb-Free, Halogen Free, RoHS/WEEE compliant product.



Absolute Maximum Ratings

V_{IN} Voltage	30V
Reverse Voltage	15V
Output Short Circuit Duration Inde	efinite ⁽¹⁾
Lead Temperature (Soldering) 10 seconds (2)	260°C
ESD Rating	2kV

Recommended Operating Conditions

V _{IN} Voltage	4.2 to 28V
Maximum Output Current	20mA
Temperature Range40°C	to 125°C

Thermal Information

Thermal Resistance, Junction t	o Ambient ⁽³⁾ 202 °C/W
Maximum Junction Temperatu	ıre +150°C
Storage Temperature Range ⁽⁴⁾	65°C to +150°C

Exceeding the above specifications may result in permanent damage to the device or device malfunction. Operation outside of the parameters specified in the Electrical Characteristics section is not recommended.

NOTES:

- (1) For $V_{IN} \le 10 \text{ V}$.
- (2) Compatible for Wave Soldering and IR Reflow.
- (3) Calculated from package in still air, mounted to 3" x 4.5", 4 layer FR4 PCB per JESD51 standards.
- (4) If the part is stored outside of the specified temperature range, the output may drift.

Electrical Characteristics -

Unless specified: $V_{IN} = V_{OUT} + 2.5V$, $I_{OUT} = 0$, $C_{IN} = 0.1\mu$ F, $C_{O} = 0\mu$ F, $-40^{\circ}C < T_{A} = T_{J} < 125^{\circ}C$

Parameter	Symbol	Conditions		Min	Тур	Max	Units
INPUT		I		1	1	11	
Input Voltage Range	V _{IN}	Refer to Line Regulation	on Spec.	4.2		28	V
		I _{OUT} = 0			0.6	0.9	V
Drop Out Voltage	V _{IN} -V _{OUT}	$I_{out} = 10 \text{mA}, T_A = 25^{\circ}\text{C}$			0.9	1.28	V
		I _{оυт} = 10mA				1.38	V
Leakage Current for Input Voltage Reverse Connection	I _{REV}	V _{IN} = -15V			2	6	μΑ
Quiescent Current	Ι _Q	$T_A = 25^{\circ}C$			110	180	μΑ
						220	μΑ
OUTPUT				-			
Output Voltage	V _{OUT}	T _J =25°C			3.3		V
Output Voltage Accuracy	%	0°C≤T _J < 100°C, 0mA <i<sub>OI 4.2V ≤V_{IN} ≤28V</i<sub>		-1		1	%
	dV _{out} /dV _{IN}	$V_{_{OUT}} + 0.9V \le V_{_{IN}} \le V_{_{OUT}} + 2.5V$	T _A = 25°C		100	500	ppm/V
						3300	ppm/V
Line Regulation		$V_{OUT} + 2.5V \le V_{IN} \le 28V$	$T_A = 25^{\circ}C$		50	100	ppm/V
						300	ppm/V



Electrical Characteristics (continued)

Unless specified: $V_{IN} = V_{OUT} + 2.5V$, $I_{OUT} = 0$, $C_{IN} = 0.1 \mu$ F, $C_{O} = 0 \mu$ F, $-40^{\circ}C < T_{A} = T_{J} < 125^{\circ}C$

Parameter	Symbol	Conditions		Min	Тур	Max	Units
	dV _{out} /dl _{load}	Ι _{ουτ} = 100μΑ	$T_A = 25^{\circ}C$		3000	4000	ppm/mA
						6000	ppm/mA
Load Regulation		10mm	$T_A = 25^{\circ}C$		100	150	ppm/mA
		I _{out} = 10mA				250	ppm/mA
		$I_{OUT} = 20 \text{mA}$ $T_{A} = 25^{\circ}\text{C}$	$T_A = 25^{\circ}C$		75	100	ppm/mA
						150	ppm/mA
Output Voltage Temperature Coefficient	dV _{out} /dT	0°C≤ T _J < 105°C			80	120	ppm/°C
Output Short Circuit Current	I _{sc}	Short V _{OUT} to GND, $T_A = 25^{\circ}C$			55		mA
DYNAMIC							
Turn-On Settling Time	T _{on}	$V_{_{\rm IN}} > V_{_{\rm OUT}} + 2.5V, V_{_{\rm OUT}}$ settles to ±0.05% of final value				3 (1)	ms

NOTES:

(1) Guaranteed by design.



Pin Descriptions

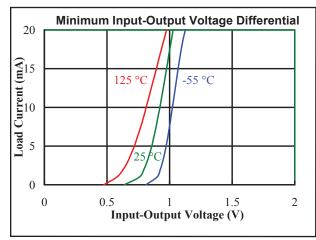
Pin #	Pin Name	Pin Function	
1	V _{IN}	Input supply voltage for the precision voltage reference.	
2	V _{OUT}	Output voltage.	
3	GND	Ground connection for the circuit.	

5

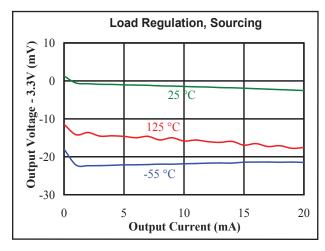


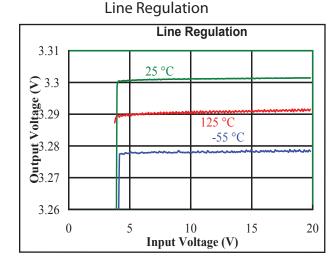
Typical Waveforms

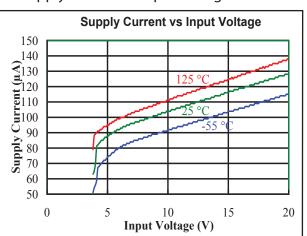
Minimum Input - Output Voltage Differential



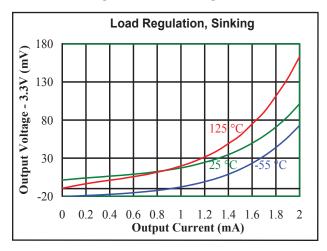
Load Regulation - Sourcing



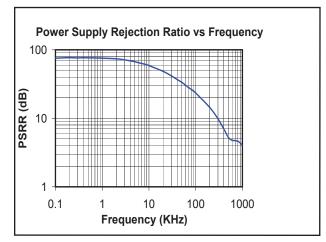




Load Regulation - Sinking



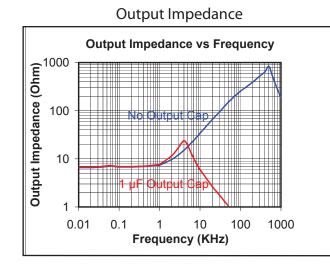
Power Supply Rejection Ratio vs Frequency



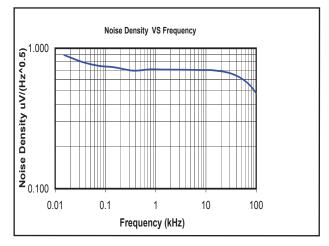
Supply Current vs Input Voltage



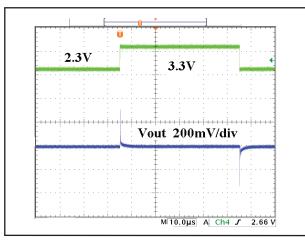
Typical Waveforms (Continued)

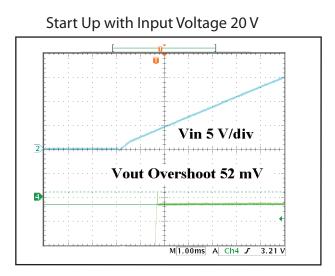


Output Noise Performance 10 Hz to 100 kHz

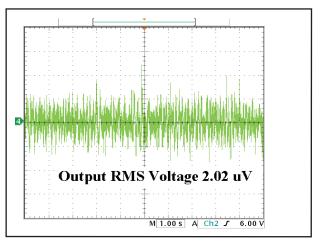


1 mA Transient Response without Output Cap

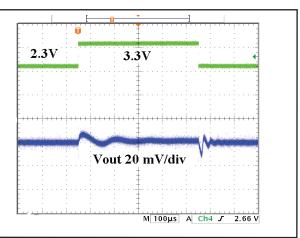




Output Noise Waveform 0.1Hz to 10 Hz

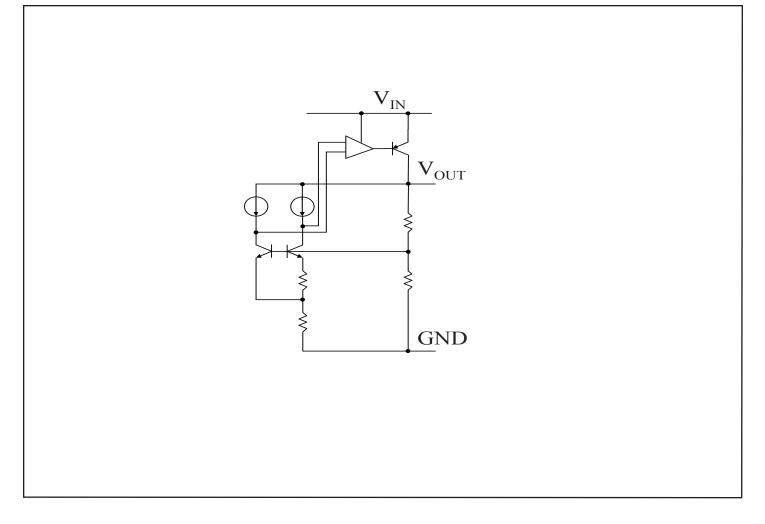


1 mA Transient Response with 1 μF Output Cap





Block Diagram





Applications Information

Advantage Over Shunt References

The SC4437 is a low power precision series voltage reference. The series reference has major advantages over shunt references when the load current has variations and the reference is powered by a battery. A shunt reference requires a resistance to connect the input and the output. This resistance will be designed to consume the maximum load current. When the actual load current is smaller than the maximum load current, the difference will be consumed by the reference. This behavior not only reduce efficiency but also increase the thermal stress on the device, thus is not desirable. On the contrary, the series reference consumes the same current as the load. When there is no load current required, the device itself consumes very little current (in the scale of 10s of μ A).

Precision Reference Over Wide Input Voltage Range, Load Conditions and Operating Temperatures

The SC4437 can maintain 1% precision regulation over wide load range, operating temperature condition and input voltage range. To provide 3.3V output voltage, the load can range from 0 to 20mA; the temperature can range from -40 °C to 125 °C; the input voltage can range from V_{OUT} + 0.9V to 28V, depending on the load and temperature conditions. The input voltage range should be selected based on the drop out voltage specification. Although the SC4437 can maintain tight regulation over wide operating conditions, attention should be paid to the thermal stress of the device. For example, with the input voltage of 28V and the load current of 20mA, the power consumption on the device is

P = (28V-3.3V)*0.02A = 0.494W

With the thermal resistance of 202 °C/W, the power consumption on the chip gives an ambient to junction temperature increase of 99.8 °C. If the ambient temperature is higher than 25 °C, then the junction temperature will be higher than 125°C, thus should be avoided.

Input and Output Capacitances

The SC4437 is stable with or without output capacitors under all load conditions. Adding output capacitances can

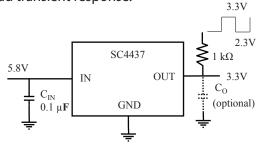
improve the dynamic response under load transient. In order to obtain optimum performance under load transient, it is recommended that a $0.1 \ \mu$ F ceramic capacitor placed at the input and a $1 \ \mu$ F (and up) ceramic capacitor placed at the output.

Reverse Battery Connection Protection

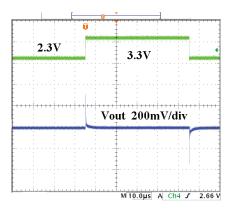
The SC4437 has reverse battery connection protection. When the input and ground is reversely connected, the leakage current from the input is very small (maximum 6 μ A), protecting both the battery and the device itself.

Load Transient Responses

The following figure shows the circuit setup to test the 1 mA load transient response.



Without output capacitance, the transient response is shown below.

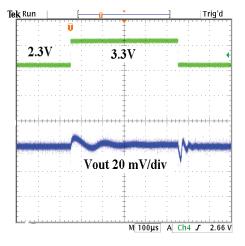


The high overshoot is due to the high resistance at high frequency without output capacitance.

With 1μ F capacitance, the transient response is improved significantly. The figure below shows the 1mA transient response with 1 μ F capacitance at the output.

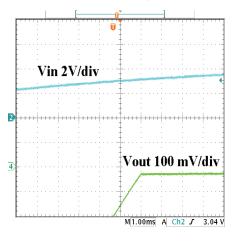


Applications Information (continued)



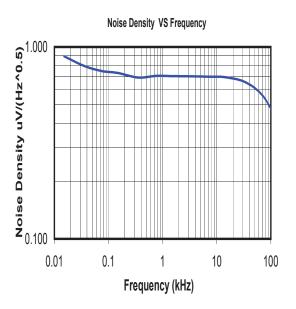
Start Up

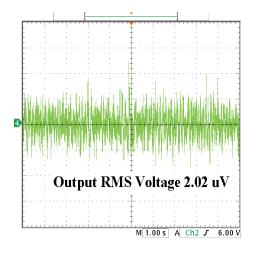
With the input voltage 5.8V, the device shows very small start up overshoot, as shown by the figure below.



Low Noise Operation

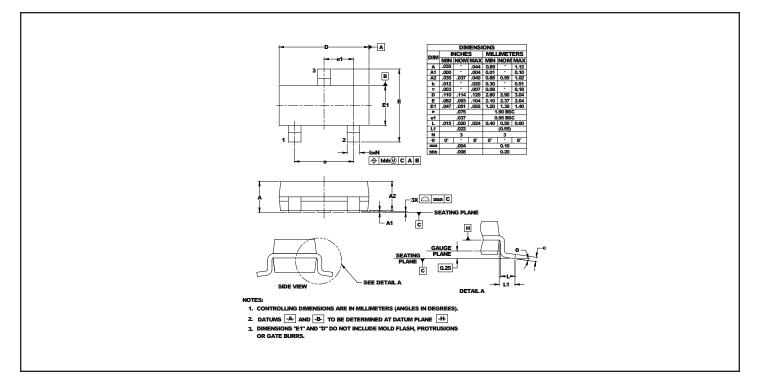
The SC4437 is a low noise reference. The noise is measured with high gain amplification and band filtering. Without any output capacitor the noise level is measured to be around $0.7\,\mu\text{V}/\sqrt{\text{Hz}}$ from 0 to 100 kHz. With output capacitor the noise level will be significantly lower. The following figure shows the noise measurement result under room temperature without output capacitor from 10Hz to 100 kHz and the scope picture of noise measurement result from 0.1 Hz to 10 Hz .





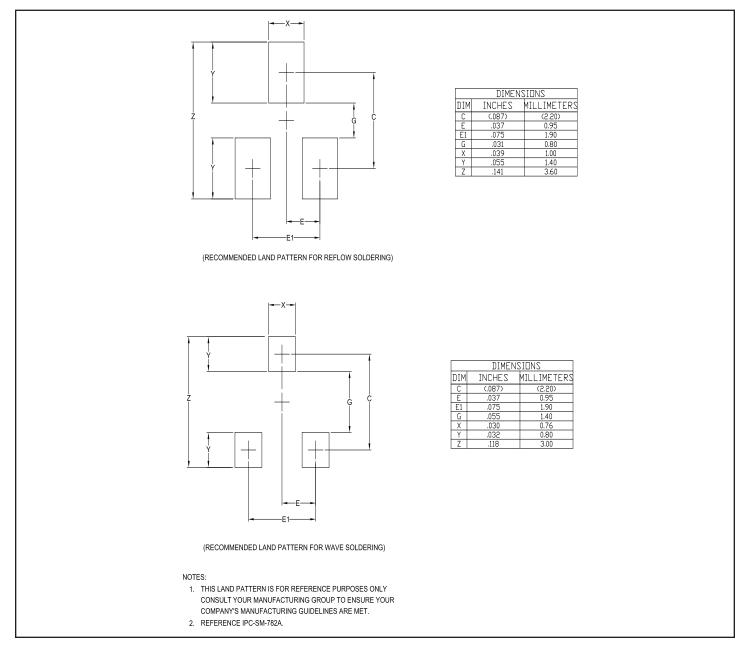


Outline Drawing – SOT-23 3L





Land Pattern – SOT-23



Contact Information

Semtech Corporation Power Management Products Division 200 Flynn Road, Camarillo, CA 93012 Phone: (805) 498-2111 Fax: (805) 498-3804

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