

Absolute Maximum Ratings (Voltage relative to GND, @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	V _{IN}	-0.3 to 100	V
Continuous Input & Output Current	I _{IN} , I _{OUT}	450	mA
Peak Pulsed Input & Output Current	I _{IM} , I _{OM}	2	A
Maximum Voltage applied to V _{OUT}	V _{OUT(max)}	Smaller of V _{IN} +5V or 11V	V

Maximum Current at V_{IN} = 48V (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Continuous Output Current	I _{OUT}	50	mA
Pulsed Output Current	I _{OM}	830	mA
		170	

Thermal Characteristics

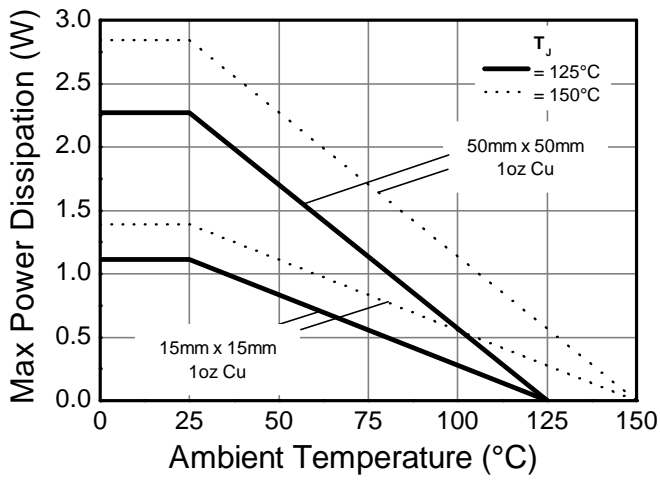
Characteristic	Symbol	Value	Unit
Power Dissipation	P _D	2.3	W
		1.1	
Thermal Resistance, Junction to Ambient	R _{θJA}	44	°C/W
		90	
Thermal Resistance, Junction to Lead	R _{θJL}	8.4	°C/W
Thermal Resistance, Junction to Case	R _{θJC}	14.6	
Recommended Operating Junction Temperature Range	T _J	-40 to +125	°C
Maximum Operating Junction and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

ESD Ratings (Note 11)

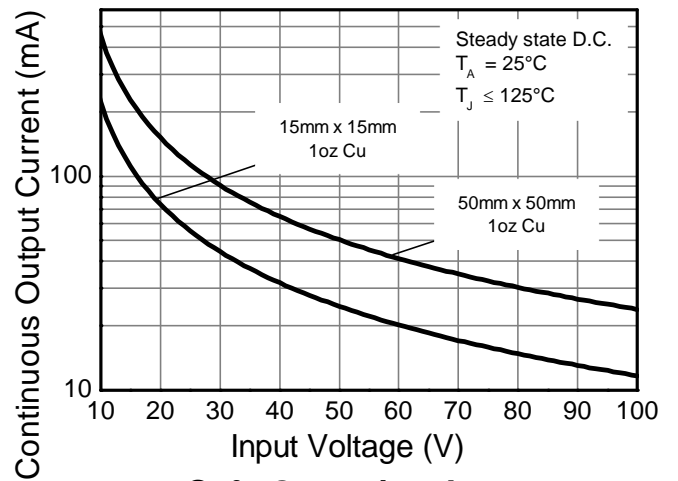
Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	C

- Notes:
- For a device mounted with the exposed V_{IN} pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.
 - Same as note 5, except mounted on 15mm x 15mm 1oz copper.
 - Same as note 5, whilst operating at V_{IN} = 48V. Refer to Safe Operating Area for other Input Voltages.
 - Same as note 5, except measured with a single pulse width = 100µs and V_{IN} = 48V.
 - Same as note 5, except measured with a single pulse width = 10ms and V_{IN} = 48V.
 - R_{θJL} = Thermal resistance from junction to solder-point (on the exposed V_{IN} pad).
 - R_{θJC} = Thermal resistance from junction to the top of case.
 - Refer to JEDEC specification JESD22-A114 and JESD22-A115.

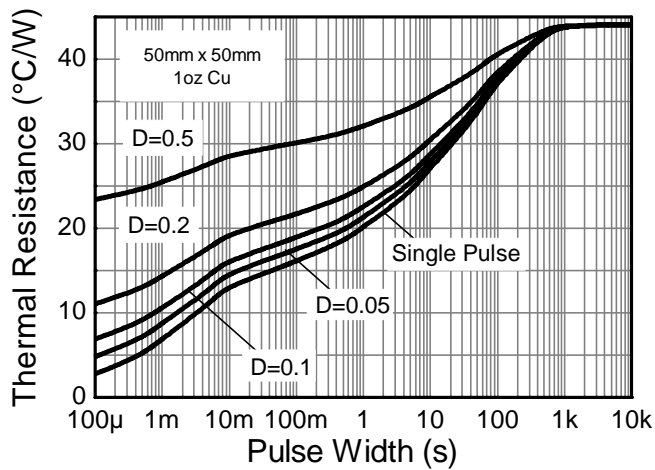
Thermal Characteristics and Derating Information



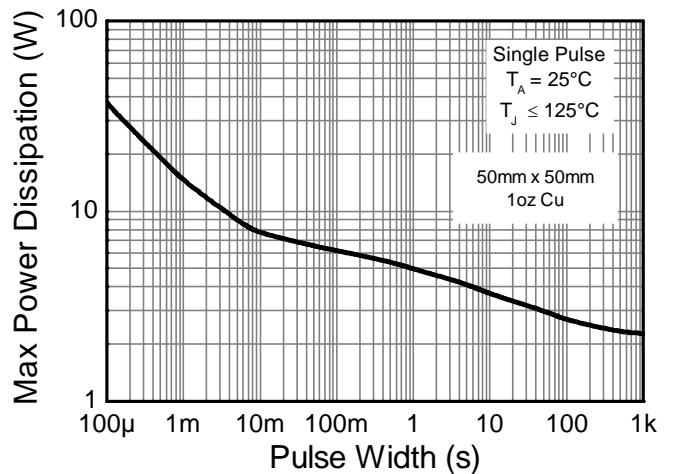
Derating Curve



Safe Operating Area



Transient Thermal Impedance

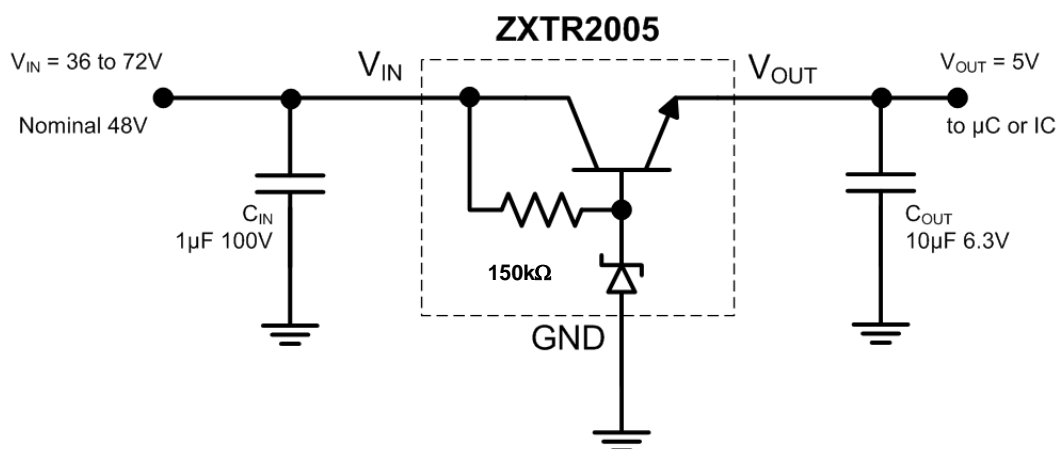


Pulse Power Dissipation

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Output Voltage (Note 12)	V_{OUT}	4.5	5.0	5.5	V	$V_{IN} = 48\text{V}$, $I_{OUT} = 15\text{mA}$
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	—	195	300	mV	$V_{IN} = 10$ to 72V , $I_{OUT} = 15\text{mA}$
Temperature Coefficient	$\Delta V_{OUT}/\Delta T$	—	7.0	—	mV/ $^\circ\text{C}$	$T_J = -40^\circ\text{C}$ to $+125^\circ\text{C}$ $V_{IN} = 48\text{V}$, $I_{OUT} = 15\text{mA}$
Load Regulation (Notes 12 & 14)	ΔV_{OUT}	—	-185 -205	-350 -400	mV	$I_{OUT} = 0.1$ to 30mA , $V_{IN} = 48\text{V}$ $I_{OUT} = 0.1$ to 100mA , $V_{IN} = 48\text{V}$
Minimum Value of Input Voltage Required to Maintain Line Regulation	$V_{IN(MIN)}$	10	—	—	V	—
Quiescent Current	I_Q	—	260 550	500 900	μA	$V_{IN} = 48\text{V}$, $I_{OUT} = 10\mu\text{A}$ $V_{IN} = 100\text{V}$, $I_{OUT} = 10\mu\text{A}$
Power Supply Rejection Ratio	$\Delta V_{IN}/\Delta V_{OUT}$	—	45	—	dB	$C_{OUT} = 100\text{nF}$, $I_{OUT} = 15\text{mA}$, $V_{OUT} = 5\text{V}$, $V_{IN} = 10$ to 100V , $f = 100\text{Hz}$

- Notes:
12. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$
 13. Line regulation $\Delta V_{OUT} = V_{OUT} (@ V_{IN} = 72\text{V}) - V_{OUT} (@ V_{IN} = 10\text{V})$
 14. Load regulation $\Delta V_{OUT} = V_{OUT} (@ I_{OUT} = 30\text{mA}) - V_{OUT} (@ I_{OUT} = 0.1\text{mA})$
 $\Delta V_{OUT} = V_{OUT} (@ I_{OUT} = 100\text{mA}) - V_{OUT} (@ I_{OUT} = 0.1\text{mA})$

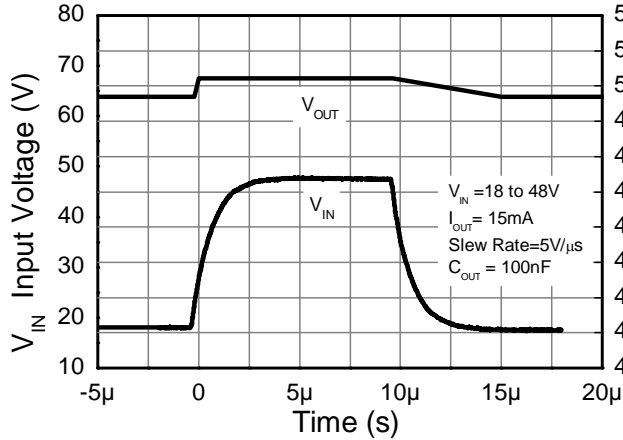
Typical Application Circuit


Example of a 5V regulated supply from a nominal 48V for powering a Controller IC.

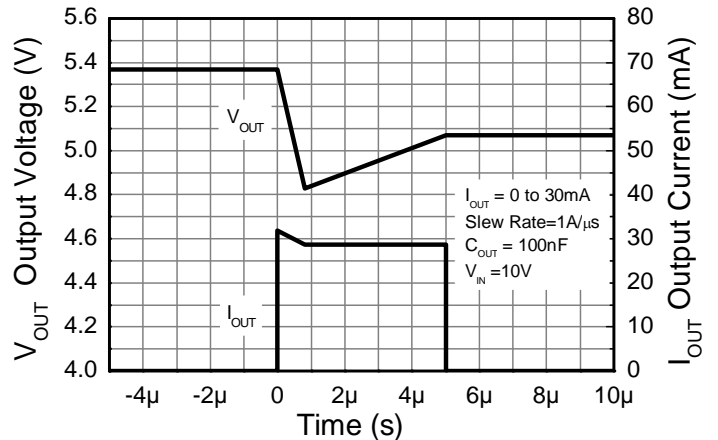
Pin Functions

Pin Name	Pin Function	Notes
VIN	Input Supply	Input voltage can vary from -0.3V to 100V with respect to GND; for V_{OUT} regulated then $10\text{V} \leq V_{IN} \leq 100\text{V}$. It is recommended to connect a $1\mu\text{F}$ capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
VOUT	Voltage Output	Outputs a regulated 5V when $10\text{V} \leq V_{IN} \leq 100\text{V}$. When $V_{IN} < 10\text{V}$, then V_{OUT} maximum = $V_{IN} - 1.5\text{V}$. The pin can be pulled high to a maximum of $+11\text{V}$ with respect to GND, or $+5\text{V}$ with respect to V_{IN} , whichever is lower. It is recommended to connect a $10\mu\text{F}$ capacitor to GND and a minimum of $10\mu\text{A}$ to be drawn from V_{OUT} to maintain regulation.

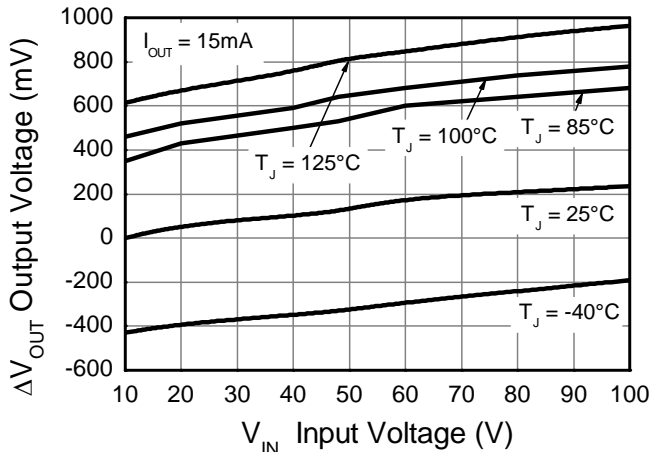
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



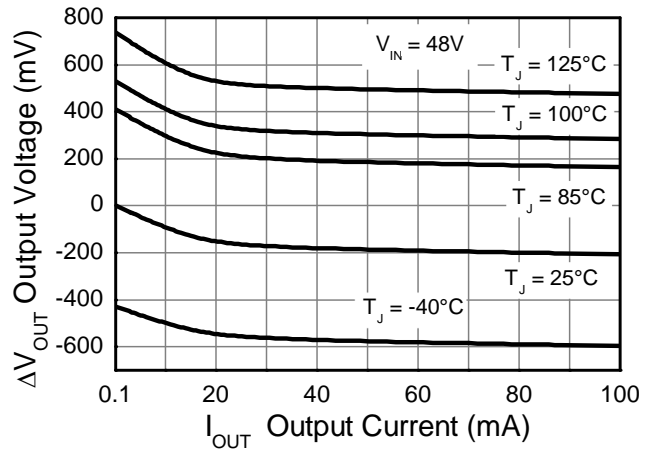
Line transient response



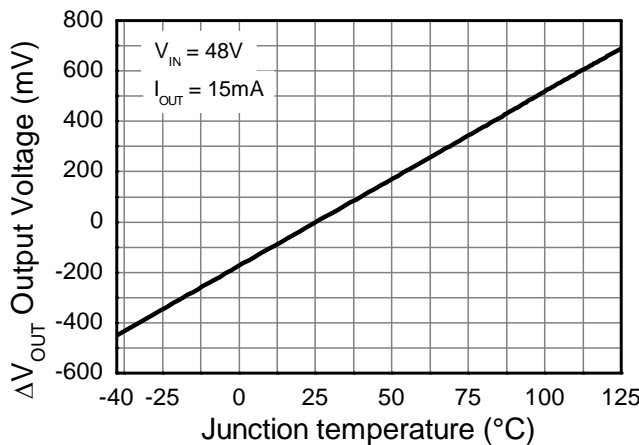
Load transient response



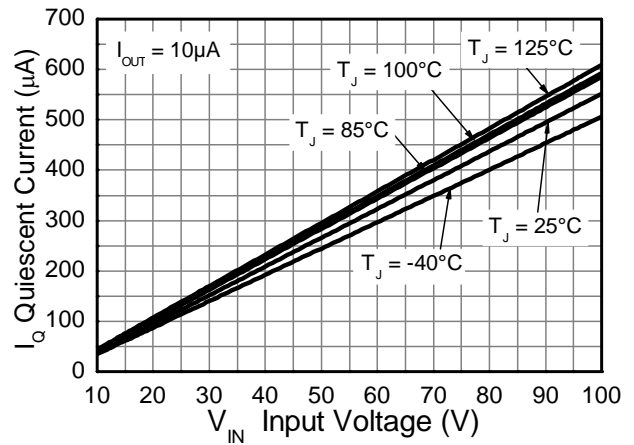
Line Regulation (Note 15)



Load Regulation (Note 16)



Temperature Coefficient (Note 17)

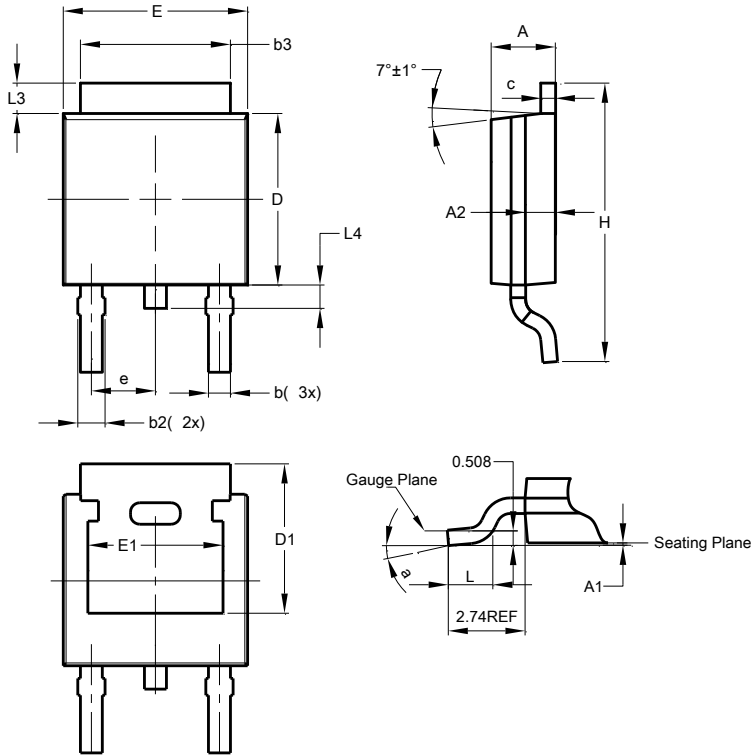


Quiescent Current

- Notes:
- 15. Line regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@ V_{IN} = 10\text{V}, I_{OUT} = 15\text{mA}, T_J = +25^\circ\text{C})$
 - 16. Load regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@ V_{IN} = 48\text{V}, I_{OUT} = 0.1\text{mA}, T_J = +25^\circ\text{C})$
 - 17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@ V_{IN} = 48\text{V}, I_{OUT} = 15\text{mA}, T_J = +25^\circ\text{C})$

Package Outline Dimensions

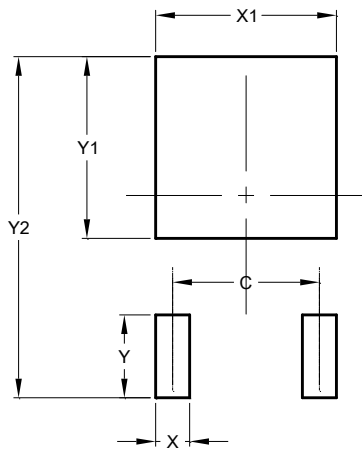
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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