

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage		V _{DSS}	70	V
Gate-Source Voltage		V _G	±20	V
Continuous Drain Current, V _{GS} = 10V,	$T_A = +25^{\circ}C$ (Note 6) $T_A = +70^{\circ}C$ (Note 6) $T_A = +25^{\circ}C$ (Note 5)	ID	3.8 3.0 2.7	A
Maximum Continuous Body Diode Forward Current (Note 6)		Is	5	А
Pulsed Drain Current		I _{DM}	10	А
Pulsed Source Current (Body Diode)		I _{SM}	10	А

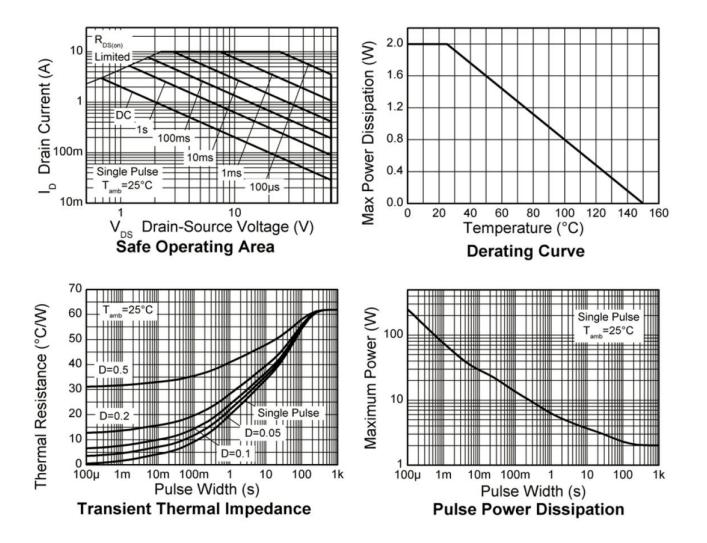
Thermal Resistance (@T_A = +25°C, unless otherwise specified.)

Symbol	Value	Units
D	2.0	W
PD	16	mW/°C
PD	3.9 31	W mW/°C
R _{0JA}	62.5	°C/W
R _{0JA}	32	°C/W
T _{J,} T _{STG}	-55 to +150	°C
-	P _D P _D R _{θJA} R _{θJA}	$ \begin{array}{c} $

5. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
6. For a device surface mounted on FR4 PCB measured at t ≤ 5 sec.
7. Repetitive rating 25mm x 25mm FR4 PCB, D=0.05 pulse width=10µs - pulse width limited by maximum junction temperature.



Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

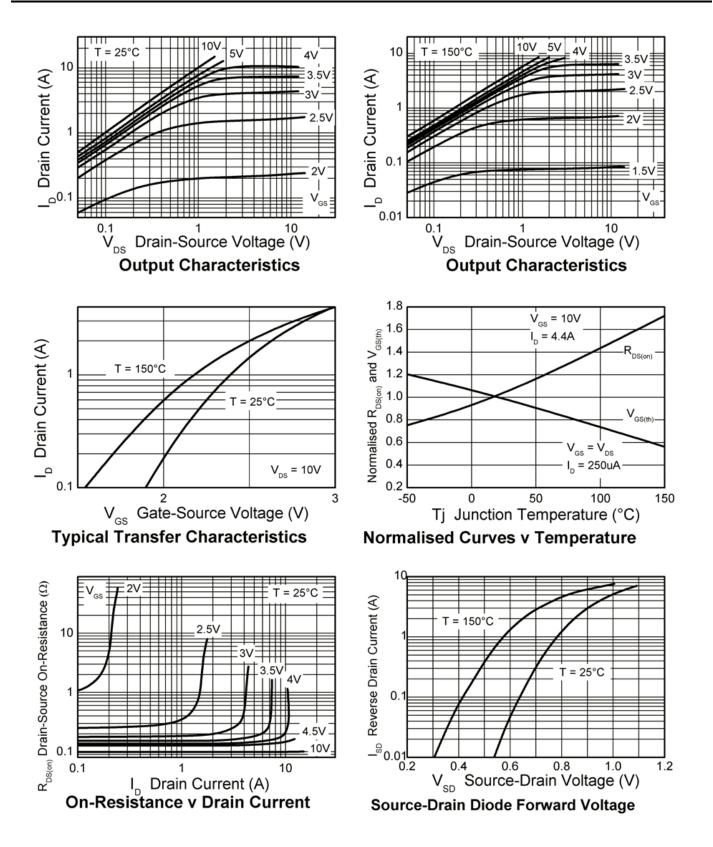
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	70	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	$V_{DS} = 70V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	1.0	—	_	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Statia Drain Source On Desistance (Note 9)	P	—	_	0.13	Ω	$V_{GS} = 10V, I_D = 4.4A$
Static Drain-Source On-Resistance (Note 8)	R _{DS} (ON)	—	—	0.19	Ω	$V_{GS} = 4.5V, I_D = 3.8A$
Forward Transfer Admittance	g fs	—	4.66		S	$V_{DS} = 15V, I_D = 4.4A$
Diode Forward Voltage (Note 8)	V _{SD}	—	0.85	0.95	V	T_J = +25°C , V_{GS} = 0V, I_S = 2.5A
DYNAMIC CHARACTERISTICS (Notes 9 & 10)						
Input Capacitance	C _{iss}	—	298			$V_{DS} = 50V, V_{GS} = 0V$ f = 1.0MHz
Output Capacitance	Coss	—	35		pF	
Reverse Transfer Capacitance	Crss	_	21			
Total Gate Charge	Qg	_	4.35	_	nC	V _{DS} = 35V, V _{GS} = 5.0V, I _D = 4.4A
Total Gate Charge	Qg		7.4	_		$V_{DS} = 35V, V_{GS} = 10V, I_D = 4.4A$
Gate-Source Charge	Q _{gs}		1.06	—	nC	
Gate-Drain Charge	Q _{gd}	_	1.8	—		
Turn-On Delay Time	t _{D(on)}	_	1.9	—		$V_{DS}=35V, V_{GS}=10V, \label{eq:VDS}$ $I_D=1$ A, $R_G\cong 6.0\Omega$
Turn-On Rise Time	tr	_	2	_		
Turn-Off Delay Time	t _{D(off)}		11.5	_	ns	
Turn-Off Fall Time	t _f	_	5.8	_	1	
Body Diode Reverse Recovery Time	t _{rr}	_	19.8		ns	T _J = +25°C, IS = 2.5A,
Body Diode Reverse Recovery Charge	Q _{rr}	_	14	_	nC dl/dt = $100A/\mu s$	

Notes:

Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.
 For design aid only, not subject to production testing.

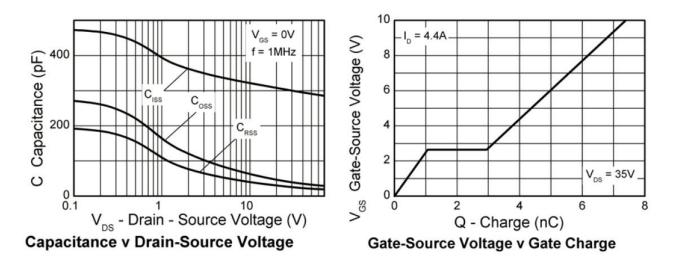


Typical Characteristics



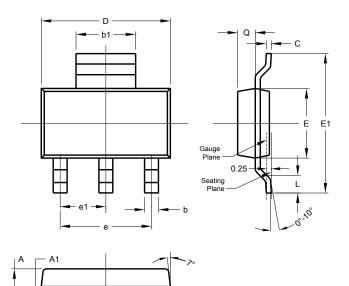


Typical Characteristics





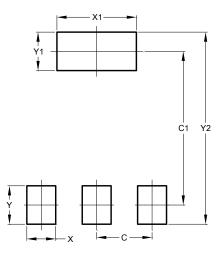
Package Outline Dimensions Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
C	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00



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