# MAX803/MAX809/ MAX810

# 3-Pin Microprocessor Reset Circuits

### **Absolute Maximum Ratings**

Terminal Voltage (with respect to GND)	
V <sub>CC</sub>	0.3V to +6.0V
RESET, RESET (push-pull)	
RESET (open drain)	0.3V to +6.0V
Input Current, V <sub>CC</sub>	20mA
Output Current, RESET, RESET	20mA
Rate of Rise, V <sub>CC</sub>	100V/µs

Continuous Power Dissipation ( $T_A = +70^{\circ}C$ )	
3-Pin SC70 (derate 2.17mW/°C above +70	0°C)174mW
3-Pin SOT23 (derate 4mW/°C above +70°	C)320mW
Operating Temperature Range	
3-Pin SC70	40°C to +125°C
3-Pin SOT23	40°C to +105°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	+260°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **Electrical Characteristics**

 $(V_{CC}$  = full range,  $T_A$  = -40°C to +105°C (SOT23) or  $T_A$  = -40°C to +125°C (SC70), unless otherwise noted. Typical values are at  $T_A$  = +25°C,  $V_{CC}$  = 5V for L/M/J versions,  $V_{CC}$  = 3.3V for T/S versions,  $V_{CC}$  = 3V for R version, and  $V_{CC}$  = 2.5V for Z version.) (Note 1)

PARAMETER	SYMBOL		CONDITIONS	MIN	TYP	MAX	UNITS
		$T_A = 0^{\circ}C$ to +	70°C	1.0		5.5	
V <sub>CC</sub> Range		T <sub>A</sub> = -40°C to +105°C (MAX8EUR)		1.2		5.5	V
		$T_A = -40^{\circ}C$ to	+125°C (MAX8EXR)	1.2		5.5	
		T <sub>A</sub> = -40°C	V <sub>CC</sub> < 5.5V, MAX8L/M		24	60	
Supply Current (SOT23)	la a	to +85°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T/Z		17	50	
Supply Culterit (SO123)	Icc	T <sub>A</sub> = +85°C	V <sub>CC</sub> < 5.5V, MAX8L/M			100	1
		to +105°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T/Z			100	
		T <sub>A</sub> = -40°C	V <sub>CC</sub> < 5.5V, MAX8L/M		24	35	μA
Supply Current (SC70)	la a	to +85°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T/Z		17	30	
Supply Current (SC70)	Icc	T <sub>A</sub> = +85°C	V <sub>CC</sub> < 5.5V, MAX8L/M			60	
		to +125°C	V <sub>CC</sub> < 3.6V, MAX8R/S/T/Z			60	
		MAX8L	T <sub>A</sub> = +25°C	4.56	4.63	4.70	-
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.50		4.75	
			T <sub>A</sub> = -40°C to +125°C	4.40		4.86	
			T <sub>A</sub> = +25°C	4.31	4.38	4.45	
		MAX8M	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.25		4.50	
			T <sub>A</sub> = -40°C to +125°C	4.16		4.56	
		MAX809J (SOT only)	T <sub>A</sub> = +25°C	3.93	4.00	4.06	
			T <sub>A</sub> = -40°C to +85°C	3.89		4.10	
Reset Threshold			T <sub>A</sub> = -40°C to +125°C	3.80		4.20	V
(SOT only)	V <sub>TH</sub>		T <sub>A</sub> = +25°C	3.04	3.08	3.11	- V - -
		MAX8T	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	3.00		3.15	
			$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$	2.92		3.23	
			T <sub>A</sub> = +25°C	2.89	2.93	2.96	
		MAX8S	T <sub>A</sub> = -40°C to +85°C	2.85		3.00	
			T <sub>A</sub> = -40°C to +125°C	2.78		3.08	
			T <sub>A</sub> = +25°C	2.59	2.63	2.66	
		MAX8R	T <sub>A</sub> = -40°C to +85°C	2.55		2.70	
			T <sub>A</sub> = -40°C to +125°C	2.50		2.76	

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# 3-Pin Microprocessor Reset Circuits

### **Electrical Characteristics (continued)**

 $(V_{CC}$  = full range,  $T_A$  = -40°C to +105°C (SOT23) or  $T_A$  = -40°C to +125°C (SC70), unless otherwise noted. Typical values are at  $T_A$  = +25°C,  $V_{CC}$  = 5V for L/M/J versions,  $V_{CC}$  = 3.3V for T/S versions,  $V_{CC}$  = 3V for R version, and  $V_{CC}$  = 2.5V for Z version.) (Note 1)

PARAMETER	SYMBOL		CONDITIONS	MIN	TYP	MAX	UNITS							
			T <sub>A</sub> = +25°C	4.56	4.63	4.70								
		MAX8L	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	4.50		4.75								
			T <sub>A</sub> = -40°C to +125°C	4.44		4.82	1							
			T <sub>A</sub> = +25°C	4.31	4.38	4.45								
				MAX8M	T <sub>A</sub> = -40°C to +85°C	4.25		4.50						
			T <sub>A</sub> = -40°C to +125°C	4.20		4.56								
			T <sub>A</sub> = +25°C	3.04	3.08	3.11								
		MAX8T	T <sub>A</sub> = -40°C to +85°C	3.00		3.15								
Reset Threshold	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		T <sub>A</sub> = -40°C to +125°C	2.95		3.21	.,							
(SC70 only)	V <sub>TH</sub>		T <sub>A</sub> = +25°C	2.89	2.93	2.96	V							
		MAX8S	T <sub>A</sub> = -40°C to +85°C	2.85		3.00								
			T <sub>A</sub> = -40°C to +125°C	2.81		3.05								
			T <sub>A</sub> = +25°C	2.59	2.63	2.66								
		MAX8R	T <sub>A</sub> = -40°C to +85°C	2.55		2.70								
			T <sub>A</sub> = -40°C to +125°C	2.52		2.74								
		MAX8Z	T <sub>A</sub> = +25°C	2.28	2.32	2.35								
			T <sub>A</sub> = -40°C to +85°C	2.25		2.38								
										(SC70 only)	T <sub>A</sub> = -40°C to +125°C	2.22		2.42
Reset Threshold Tempco					30		ppm/°C							
V <sub>CC</sub> to Reset Delay (Note 2)		V <sub>CC</sub> = V <sub>TH</sub> to	(V <sub>TH</sub> - 100mV)		20		μs							
Reset Active Timeout Period		$T_A = -40^{\circ}C$ to	) +85°C	140	240	560								
(SOT23)		T <sub>A</sub> = +85°C t	o +105°C	100		840	ms							
Reset Active Timeout Period		$T_A = -40^{\circ}C$ to	) +85°C	140	240	460								
(SC70)		T <sub>A</sub> = +85°C t	o +125°C	100		840	ms							
RESET Output Voltage Low	V <sub>OL</sub>		min), I <sub>SINK</sub> = 1.2mA, T/Z, MAX809R/S/T/Z			0.3								
(push-pull active low and open- drain active low, MAX803 and MAX809)			min), I <sub>SINK</sub> = 3.2mA, MAX809J/L/M			0.4	V							
WAX809)		V <sub>CC</sub> > 1.0V, I	SINK = 50µA			0.3								
RESET Output Voltage High (push-pull active low MAX809)	V <sub>OH</sub>	V <sub>CC</sub> > V <sub>TH</sub> (r	max), I <sub>SOURCE</sub> = 500µA, T/Z, MAX809R/S/T/Z	0.8V <sub>CC</sub>										
			max), I <sub>SOURCE</sub> = 800μA, MAX809J/L/M	V <sub>CC</sub> - 1.	5		V							
RESET Output Voltage Low (push-pull active high, MAX810)	.,	V <sub>CC</sub> = V <sub>TH</sub> (r MAX810R/S/	max), I <sub>SINK</sub> = 1.2mA, T/Z			0.3	.,,							
	oull active high, MAX810) VOL V <sub>CC</sub> =	V <sub>CC</sub> = V <sub>TH</sub> (r MAX810L/M	max), I <sub>SINK</sub> = 3.2mA,			0.4	V							

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### **Electrical Characteristics (continued)**

 $(V_{CC} = \text{full range}, T_A = -40^{\circ}\text{C to} + 105^{\circ}\text{C (SOT23)} \text{ or } T_A = -40^{\circ}\text{C to} + 125^{\circ}\text{C (SC70)}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}\text{C}, V_{CC} = 5\text{V for L/M/J versions}, V_{CC} = 3.3\text{V for T/S versions}, V_{CC} = 3\text{V for R version, and } V_{CC} = 2.5\text{V for Z version.}) \text{ (Note 1)}$ 

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
RESET Output Voltage High (push-pull active high, MAX810)	V <sub>OH</sub>	1.8V < V <sub>CC</sub> < V <sub>TH</sub> (min), I <sub>SOURCE</sub> = 150μA	0.8V <sub>CC</sub>			V
RESET Open-Drain Output Leakage Current (MAX803) (Note 3)		V <sub>CC</sub> > V <sub>TH</sub> , RESET deasserted			1	μA

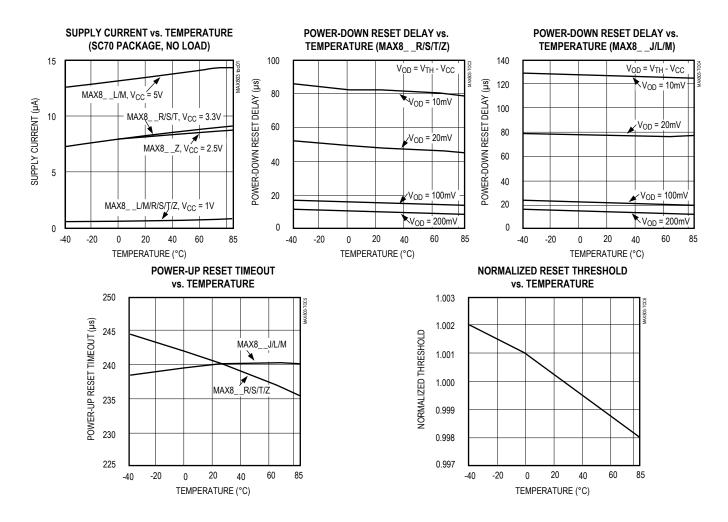
Note 1: Production testing done at T<sub>A</sub> = +25°C; limits over temperature guaranteed by design only.

Note 2: RESET output for MAX803/MAX809; RESET output for MAX810.

Note 3: Guaranteed by design, not production tested.

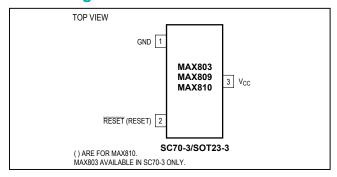
### **Typical Operating Characteristics**

 $(V_{CC}$  = full range,  $T_A$  = -40°C to +105°C, unless otherwise noted. Typical values are at  $T_A$  = +25°C,  $V_{CC}$  = +5V for L/M/J versions,  $V_{CC}$  = +3.3V for T/S versions,  $V_{CC}$  = +3V for R version, and  $V_{CC}$  = +2.5V for Z version.)



### 3-Pin Microprocessor Reset Circuits

#### **Pin Configuration**



### **Pin Description**

PIN	NAME	FUNCTION
1	GND	Ground
2	RESET (MAX803/ MAX809)	RESET Output remains low while $V_{CC}$ is below the reset threshold, and for at least 140ms after $V_{CC}$ rises above the reset threshold.
2	RESET (MAX810)	RESET Output remains high while $V_{CC}$ is below the reset threshold, and for at least 140ms after $V_{CC}$ rises above the reset threshold.
3	V <sub>CC</sub>	Supply Voltage (+5V, +3.3V, +3.0V, or +2.5V)

### **Detailed Description**

A microprocessor's ( $\mu$ P's) reset input starts the  $\mu$ P in a known state. The MAX803/MAX809/MAX810 assert reset to prevent code-execution errors during power-up, power-down, or brownout conditions. They assert a reset signal whenever the V<sub>CC</sub> supply voltage declines below a preset threshold, keeping it asserted for at least 140ms after V<sub>CC</sub> has risen above the reset threshold. The MAX803 uses an open-drain output, and the MAX809/MAX810 have a push-pull output stage. Connect a pullup resistor on the MAX803's  $\overline{\text{RESET}}$  output to any supply between 0 and 6V.

## **Applications Information**

#### **Negative-Going Vcc Transients**

In addition to issuing a reset to the  $\mu P$  during power-up, power-down, and brownout conditions, the MAX803/MAX809/MAX810 are relatively immune to short-duration negative-going  $V_{CC}$  transients (glitches).

Figure 1 shows typical transient duration vs. reset comparator overdrive, for which the MAX803/MAX809/MAX810 do **not** generate a reset pulse. The graph was generated using a negative-going pulse applied

to  $V_{CC}$ , starting 0.5V above the actual reset threshold and ending below it by the magnitude indicated (reset comparator overdrive). The graph indicates the maximum pulse width a negative-going  $V_{CC}$  transient can have without causing a reset pulse. As the magnitude of the transient increases (goes farther below the reset threshold), the maximum allowable pulse width decreases. Typically, for the MAX8\_L and MAX8\_M, a  $V_{CC}$  transient that goes 100mV below the reset threshold and lasts 20µs or less will not cause a reset pulse. A 0.1µF bypass capacitor mounted as close as possible to the  $V_{CC}$  pin provides additional transient immunity.

# Ensuring a Valid Reset Output Down to V<sub>CC</sub> = 0V

When V<sub>CC</sub> falls below 1V, the MAX809 RESET output no longer sinks current—it becomes an open circuit.

Therefore, high-impedance CMOS logic inputs connected to  $\overline{RESET}$  can drift to undetermined voltages. This presents no problem in most applications since most  $\mu P$  and other circuitry is inoperative with  $V_{CC}$  below 1V. However, in applications where  $\overline{RESET}$  must be valid down to 0V, adding a pull-down resistor to  $\overline{RESET}$  causes any stray leakage currents to flow to ground, holding  $\overline{RESET}$  low (Figure 2). R1's value is not critical;  $100k\Omega$  is large enough not to load  $\overline{RESET}$  and small enough to pull  $\overline{RESET}$  to ground.

A  $100k\Omega$  pullup resistor to V<sub>CC</sub> is also recommended for the MAX810 if RESET is required to remain valid for V<sub>CC</sub> < 1V.

# Interfacing to $\mu Ps$ with Bidirectional Reset Pins

Since the  $\overline{\text{RESET}}$  output on the MAX803 is open drain, this device interfaces easily with  $\mu\text{Ps}$  that have bidirectional reset pins, such as the Motorola 68HC11. Connecting the  $\mu\text{P}$  supervisor's  $\overline{\text{RESET}}$  output directly to the  $\mu\text{C}$ 's  $\overline{\text{RESET}}$  pin with a single pullup resistor allows either device to assert reset (Figure 3).

# MAX803 Open-Drain RESET Output Allows Use with Multiple Supplies

Generally, the pullup connected to the MAX803 will connect to the supply voltage that is being monitored at the IC's  $V_{CC}$  pin. However, some systems may use the opendrain output to level-shift from the monitored supply to reset circuitry powered by some other supply (Figure 4). Note that as the MAX803's  $V_{CC}$  decreases below 1V, so does the IC's ability to sink current at  $\overline{\text{RESET}}$ . Also, with any pullup,  $\overline{\text{RESET}}$  will be pulled high as  $V_{CC}$  decays toward 0. The voltage where this occurs depends on the pullup resistor value and the voltage to which it is connected.

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### MAX803/MAX809/ MAX810

# 3-Pin Microprocessor Reset Circuits

#### **Benefits of Highly Accurate Reset Threshold**

Most  $\mu P$  supervisor ICs have reset-threshold voltages between 5% and 10% below the value of nominal supply voltages. This ensures a reset will **not** occur within 5% of the nominal supply, but **will** occur when the supply is 10% below nominal.

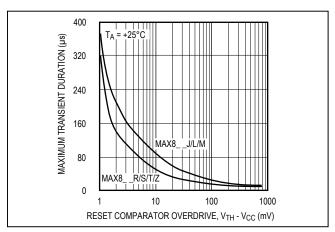


Figure 1. Maximum Transient Duration Without Causing a Reset Pulse vs. Reset Comparator Overdrive

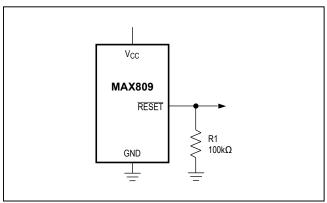


Figure 2.  $\overline{RESET}$  Valid to  $V_{CC}$  = Ground Circuit

When using ICs rated at only the nominal supply ±5%, this leaves a zone of uncertainty where the supply is between 5% and 10% low, and where the reset may or may not be asserted.

The MAX8\_\_L/T/Z use highly accurate circuitry to ensure that reset is asserted close to the 5% limit, and long before the supply has declined to 10% below nominal.

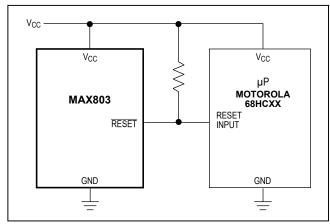


Figure 3. Interfacing to μPs with Bidirectional Reset I/O

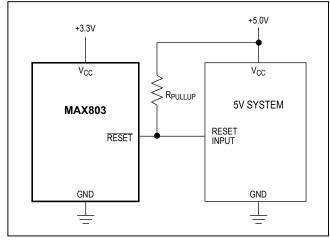


Figure 4. MAX803 Open-Drain RESET Output Allows Use with Multiple Supplies

#### **Selector Guide**

PART/SUFFIX	RESET THRESHOLD (V)	OUTPUT TYPE
MAX803L	4.63	Open-Drain RESET
MAX803M	4.38	Open-Drain RESET
MAX803T	3.08	Open-Drain RESET
MAX803S	2.93	Open-Drain RESET
MAX803R	2.63	Open-Drain RESET
MAX803Z	2.32	Open-Drain RESET
MAX809L	4.63	Push-Pull RESET
MAX809M	4.38	Push-Pull RESET
MAX809J	4.00	Push-Pull RESET
MAX809T	3.08	Push-Pull RESET
MAX809S	2.93	Push-Pull RESET
MAX809R	2.63	Push-Pull RESET
MAX809Z	2.32	Push-Pull RESET
MAX810L	4.63	Push-Pull RESET
MAX810M	4.38	Push-Pull RESET
MAX810T	3.08	Push-Pull RESET
MAX810S	2.93	Push-Pull RESET
MAX810R	2.63	Push-Pull RESET
MAX810Z	2.32	Push-Pull RESET

### **Ordering Information**

PART	TEMP RANGE	PIN-PACKAGE
MAX803_EXR-T	-40°C to +125°C	3 SC70
MAX803_EXR+T	-40°C to +125°C	3 SC70
MAX803_EXR/V+	-40°C to +125°C	3 SC70
MAX803_EXR/V+T	-40°C to +125°C	3 SC70
MAX803LEXR/V+	-40°C to +125°C	3 SC70
MAX803MEXR/V+T	-40°C to +125°C	3 SC70
MAX803SEXR/V+T	-40°C to +125°C	3 SC70
MAX803TEXR/V+T	-40°C to +125°C	3 SC70
MAX809_EXR-T	-40°C to +125°C	3 SC70
MAX809_EXR+T	-40°C to +125°C	3 SC70
MAX809_EUR-T	-40°C to +105°C	3 SOT23
MAX809_EUR+T	-40°C to +105°C	3 SOT23
MAX810_EXR+T	-40°C to +125°C	3 SC70
MAX810_EUR+T	-40°C to +105°C	3 SOT23

Note: These parts are offered in 2.5k reels, and must be ordered in 2.5k increments. Insert the desired suffix letter from the Selector Guide into the blank to complete the part number. All versions of these products may not be available at the time of announcement. Contact factory for availability.

Some devices are available in both leaded and lead-free packaging.

+Denotes a lead(Pb)-free/RoHS-compliant package.

/V denotes an automotive qualified part.

T = Tape and reel.

### **Chip Information**

TRANSISTOR COUNT: 275 (SOT23) 380 (SC70)

### **Package Information**

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
3 SC70	X3+2, X3-2	21-0075
3 SOT23	U3+1, U3-1	21-0051

# MAX803/MAX809/ **MAX810**

# 3-Pin Microprocessor Reset Circuits

### **Revision History**

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	12/94	Initial release.	_
7	2/10	Updated <i>Ordering Information</i> , added lead-free note, and added soldering temperature in the <i>Absolute Maximum Ratings</i>	1, 2
8	12/12	Added MAX803_EXR/V+ to Ordering Information	1
9	6/15	Added MAX803_EXR/V+ to Ordering Information, moved Ordering Information, Pin Configuration, and Selector Guide to end of data sheet, and rebranded data sheet	1–8
10	12/15	Added lead-free package code information and removed top mark information from Selector Guide	7
11	10/17	Added AEC qualification statement to <i>Benefits and Features</i> section and updated <i>Ordering Information</i> table	1, 7

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 $\frac{\text{MAX803TEXR+T}}{\text{MAX803MEXR/V+T}} \; \frac{\text{MAX803TEXR+V+T}}{\text{MAX803MEXR/V+T}} \; \frac{\text{MAX803SEXR/V+T}}{\text{MAX803MEXR/V+T}} \; \frac{\text{MAX803TEXR+V+T}}{\text{MAX803MEXR/V+T}}$