

SLC NAND - PRODUCT LIST

Density	Part Number	Techn.	Page Size	Vcc	ECC	Temperature	Package
1Gb	TC58NVG0S3HTA00	24nm	(2048+128)x8 bit	3.3V	8bit/512B	0°C to 70°C	48TSOP 12x20
	TC58NYG0S3HBAI4	24nm	(2048+128)x8 bit	1.8V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TC58NVG0S3HTAI0	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	48TSOP 12x20
	TC58NVG0S3HBAI4	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TC58NYG0S3HBAI6	24nm	(2048+128)x8 bit	1.8V	8bit/512B	-40°C to 85°C	67BGA 6.5x8
	TC58NVG0S3HBAI6	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	67BGA 6.5x8
2Gb	TC58NVG1S3HTA00	24nm	(2048+128)x8 bit	3.3V	8bit/512B	0°C to 70°C	48TSOP 12x20
	TC58NYG1S3HBAI4	24nm	(2048+128)x8 bit	1.8V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TC58NVG1S3HTAI0	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	48TSOP 12x20
	TC58NVG1S3HBAI4	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TC58NYG1S3HBAI6	24nm	(2048+128)x8 bit	1.8V	8bit/512B	-40°C to 85°C	67BGA 6.5x8
	TC58NVG1S3HBAI6	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	67BGA 6.5x8
4Gb	TH58NVG2S3HTA00	24nm	(2048+128)x8 bit	3.3V	8bit/512B	0°C to 70°C	48TSOP 12x20
	TC58NVG2S0HTA00	24nm	(4096+256)x8 bit	3.3V	8bit/512B	0°C to 70°C	48TSOP 12x20
	TC58NVG2S0HTAI0	24nm	(4096+256)x8 bit	3.3V	8bit/512B	-40°C to 85°C	48TSOP 12x20
	TH58NVG2S3HTAI0	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	48TSOP 12x20
	TH58NVG2S3HBAI4	24nm	(2048+128)x8 bit	3.3V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TH58NYG2S3HBAI4	24nm	(2048+128)x8 bit	1.8V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TC58NVG2S0HBAI4	24nm	(4096+256)x8 bit	3.3V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TC58NYG2S0HBAI4	24nm	(4096+256)x8 bit	1.8V	8bit/512B	-40°C to 85°C	63BGA 9x11
	TC58NVG2S0HBAI6	24nm	(4096+256)x8 bit	3.3V	8bit/512B	-40°C to 85°C	67BGA 6.5x8
	TC58NYG2S0HBAI6	24nm	(4096+256)x8 bit	1.8V	8bit/512B	-40°C to 85°C	67BGA 6.5x8
	TH58NVG3S0HTA00	24nm	(4096+256)x8 bit	3.3V	8bit/512B	0°C to 70°C	48TSOP 12x20
	TH58NVG3S0HBAI4	24nm	(4096+256)x8 bit	3.3V	8bit/512B	-40°C to 85°C	63BGA 9x11
TH58NYG3S0HBAI4	24nm	(4096+256)x8 bit	1.8V	8bit/512B	-40°C to 85°C	63BGA 9x11	
TH58NVG3S0HTAI0	24nm	(4096+256)x8 bit	3.3V	8bit/512B	-40°C to 85°C	48TSOP 12x20	
TH58NVG3S0HBAI6	24nm	(4096+256)x8 bit	3.3V	8bit/512B	-40°C to 85°C	67BGA 6.5x8	
TH58NYG3S0HBAI6	24nm	(4096+256)x8 bit	1.8V	8bit/512B	-40°C to 85°C	67BGA 6.5x8	
16Gb	TH58NVG4S0FTA20	32nm	(4096+256)x8 bit	3.3V	4bit/512B	0°C to 70°C	48TSOP 12x20
	TH58NVG4S0FTA00	32nm	(4096+256)x8 bit	3.3V	4bit/512B	-40°C to 85°C	48TSOP 12x20
	TH58NVG4S0FBAID	32nm	(4096+256)x8 bit	3.3V	4bit/512B	-40°C to 85°C	63BGA 10x11
	TH58NVG4S0HTA20	24nm	(4096+256)x8 bit	3.3V	8bit/512B	0°C to 70°C	48TSOP 12x20
	TH58NVG4S0HTAK0	24nm	(4096+256)x8 bit	3.3V	8bit/512B	-40°C to 85°C	48TSOP 12x20
32Gb	TC58NVG5H2HTA00	24nm	(8192+1024)x8 bit	3.3V	24bit/1024B	0°C to 70°C	48TSOP 12x20
	TC58NVG5H2HTAI0	24nm	(8192+1024)x8 bit	3.3V	24bit/1024B	-40°C to 85°C	48TSOP 12x20
64Gb	TH58NVG6H2HTAK0	24nm	(8192+1024)x8 bit	3.3V	24bit/1024B	-40°C to 85°C	48TSOP 12x20
128Gb	TH58NVG7H2HTA20	24nm	(8192+1024)x8 bit	3.3V	24bit/1024B	0°C to 70°C	48TSOP 12x20

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Density	Part Number	Techn.	Page Size	Vcc	ECC	Temperature	Package
1Gb	TC58BVG0S3HTA00	24nm	(2048+64)x8 bit	3.3V	internal ECC	0°C to 70°C	48TSOP 12x20
	TC58BYG0S3HBAI4	24nm	(2048+64)x8 bit	1.8V	internal ECC	-40°C to 85°C	63BGA 9x11
	TC58BVG0S3HTAI0	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	48TSOP 12x20
	TC58BVG0S3HBAI4	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	63BGA 9x11
	TC58BYG0S3HBAI6	24nm	(2048+64)x8 bit	1.8V	internal ECC	-40°C to 85°C	67BGA 6.5x8
	TC58BVG0S3HBAI6	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	67BGA 6.5x8
2Gb	TC58BVG1S3HTA00	24nm	(2048+64)x8 bit	3.3V	internal ECC	0°C to 70°C	48TSOP 12x20
	TC58BYG1S3HBAI4	24nm	(2048+64)x8 bit	1.8V	internal ECC	-40°C to 85°C	63BGA 9x11
	TC58BVG1S3HTAI0	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	48TSOP 12x20
	TC58BVG1S3HBAI4	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	63BGA 9x11
	TC58BYG1S3HBAI6	24nm	(2048+64)x8 bit	1.8V	internal ECC	-40°C to 85°C	67BGA 6.5x8
	TC58BVG1S3HBAI6	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	67BGA 6.5x8
4Gb	TH58BVG2S3HTA00	24nm	(2048+64)x8 bit	3.3V	internal ECC	0°C to 70°C	48TSOP 12x20
	TC58BVG2S0HTA00	24nm	(4096+128)x8 bit	3.3V	internal ECC	0°C to 70°C	48TSOP 12x20
	TC58BVG2S0HTAI0	24nm	(4096+128)x8 bit	3.3V	internal ECC	-40°C to 85°C	48TSOP 12x20
	TH58BVG2S3HTAI0	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	48TSOP 12x20
	TH58BVG2S3HBAI4	24nm	(2048+64)x8 bit	3.3V	internal ECC	-40°C to 85°C	63BGA 9x11
	TH58BYG2S3HBAI4	24nm	(2048+64)x8 bit	1.8V	internal ECC	-40°C to 85°C	63BGA 9x11
	TC58BVG2S0HBAI4	24nm	(4096+128)x8 bit	3.3V	internal ECC	-40°C to 85°C	63BGA 9x11
	TC58BYG2S0HBAI4	24nm	(4096+128)x8 bit	1.8V	internal ECC	-40°C to 85°C	63BGA 9x11
	TC58BVG2S0HBAI6	24nm	(4096+128)x8 bit	3.3V	internal ECC	-40°C to 85°C	67BGA 6.5x8
	TC58BYG2S0HBAI6	24nm	(4096+128)x8 bit	1.8V	internal ECC	-40°C to 85°C	67BGA 6.5x8
	TH58BYG2S3HBAI6	24nm	(2048+64)x8 bit	1.8V	internal ECC	-40°C to 85°C	67BGA 6.5x8
	8Gb	TH58BVG3S0HTA00	24nm	(4096+128)x8 bit	3.3V	internal ECC	0°C to 70°C
TH58BYG3S0HBAI4		24nm	(4096+128)x8 bit	1.8V	internal ECC	-40°C to 85°C	63BGA 9x11
TH58BVG3S0HTAI0		24nm	(4096+128)x8 bit	3.3V	internal ECC	-40°C to 85°C	48TSOP 12x20
TH58BVG3S0HBAI4		24nm	(4096+128)x8 bit	3.3V	internal ECC	-40°C to 85°C	63BGA 9x11
TH58BVG3S0HBAI6		24nm	(4096+128)x8 bit	3.3V	internal ECC	-40°C to 85°C	67BGA 6.5x8
TH58BYG3S0HBAI6		24nm	(4096+128)x8 bit	1.8V	internal ECC	-40°C to 85°C	67BGA 6.5x8

BENAND™ is the trademark of Toshiba Corporation.

Product density is identified based on the maximum density of memory chip(s) within the Product, not the amount of memory capacity available for data storage by the end user. Consumer-usable capacity will be less due to overhead data areas, formatting, bad blocks, and other constraints, and may also vary based on the host device and application.

Maximum read and write speed may vary depending on the host device, read and write conditions, and file size.

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