FUNCTIONAL DESCRIPTION

The DS21S07A consists of a bandgap reference, buffer amplifier, and nine termination resistors (Figure 1). The bandgap reference circuit produces a precise 2.55V level that is fed to a buffer amplifier. The buffer produces a 2.85V level and can source at least 24mA into each of the termination resistors when the signal line is low (active). When the driver for a given signal line turns off, the terminator pulls the signal line to 2.85V (quiescent state). To handle actively negated SCSI signals, the buffer can sink at least 200mA, and VREF will move less than 60mV. When all lines settle in the quiescent state, the regulator consumes about 2.5mA. When the DS21S07A is put into power-down mode by bringing $\overline{PD}$ low, the power-down circuitry turns off the transistors on each signal line. This isolates the DS21S07A from the signal lines and effectively removes it from the circuit. The power-down pin ($\overline{PD}$) has an internal 50kΩ pullup resistor. To place the DS21S07A into an active state, the $\overline{PD}$ pin should be left open circuited. When installed on disk drives or RAID system components, the DS21S07A will not affect the SCSI bus during a hot plug operation.

To ensure proper operation, both the TERMPWR1 and TERMPWR2 pins must be connected to the SCSI bus TERMPWR line and both the VREF1 and VREF2 pins must be tied together externally. Each DS21S07A requires a 4.7µF capacitor connected between the VREF pins and ground. Figure 2 details a typical SCSI bus configuration. In an 8-bit-wide SCSI bus arrangement (“A” Cable), two DS21S07A’s would be needed at each end of the SCSI cable to terminate the 18 active signal lines. In a 16-bit-wide SCSI bus arrangement (“P” Cable), three DS21S07A’s would be needed at each end of the SCSI cable to terminate the 27 active signal lines.

Figure 1. DS21S087A Block Diagram

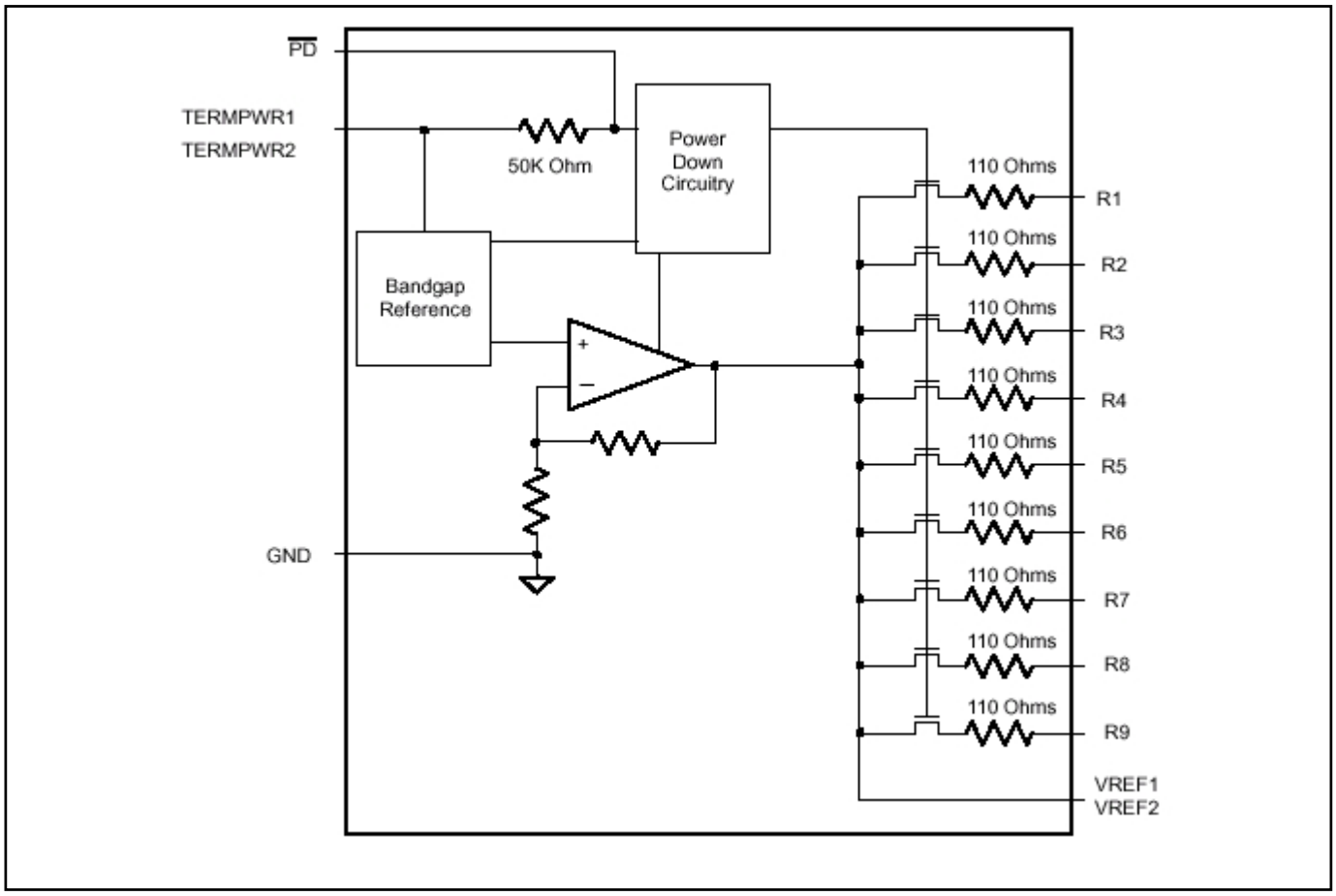
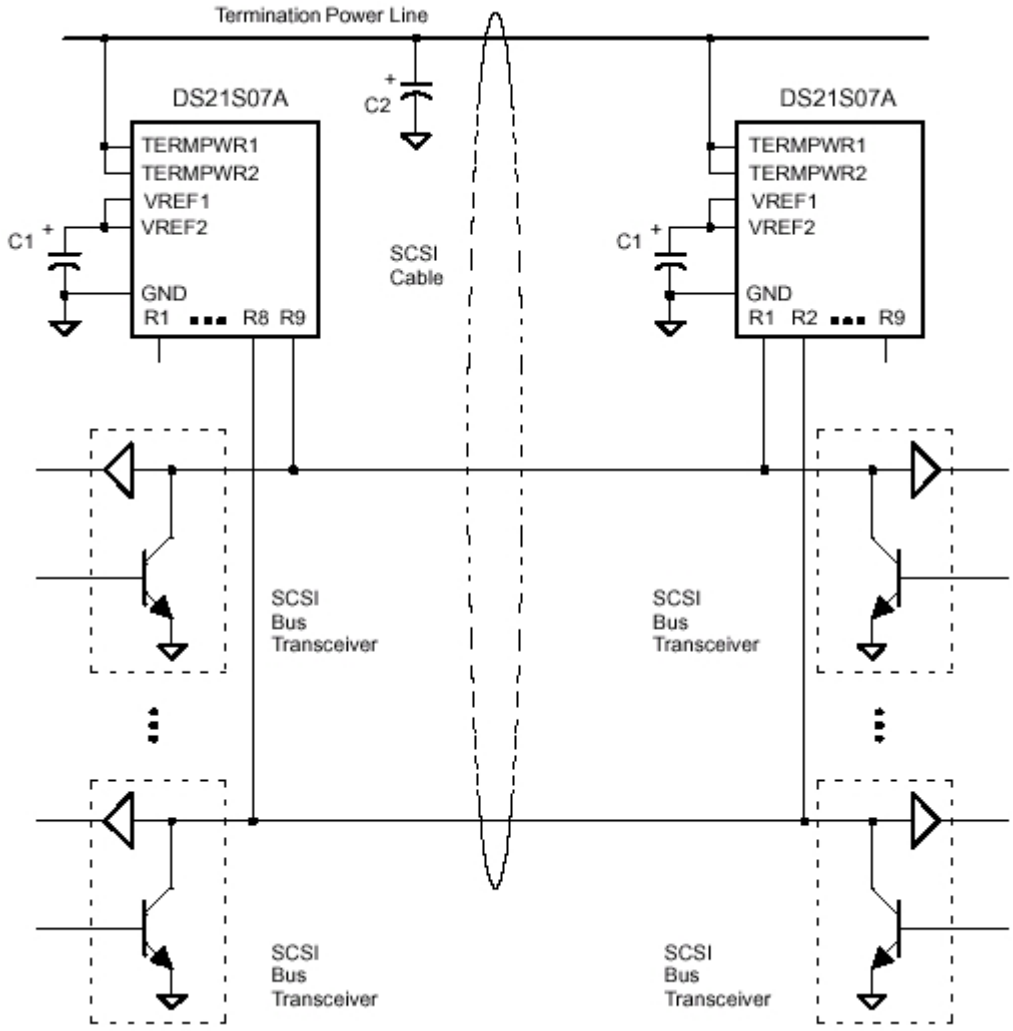


Figure 2. Typical Differential SCSI Bus Configuration



NOTE 1: C1 = 4.7μF TANTALUM CAPACITOR; C2 = 2.2μF TANTALUM OR 4.7μF ALUMINUM CAPACITOR.
NOTE 2: IF THE DS21S07A IS TO BE EMBEDDED INTO A PERIPHERAL THAT WILL ACT AS A TARGET ON A SCSI BUS, IT IS RECOMMENDED THAT TERMPWR BE DERIVED FROM THE SCSI CABLE, NOT GENERATED LOCALLY. IN THIS CONFIGURATION, IF A POWER FAILURE OCCURS IN THE PERIPHERAL, IT WILL NOT AFFECT THE BUS.
NOTE 3: A HIGH-FREQUENCY BYPASS CAPACITOR (0.1μF RECOMMENDED) CAN BE ADDED IN PARALLEL TO C1 FOR APPLICATIONS USING FAST RISE/FALL TIME DRIVERS.

Table 1. Pin Description

PIN		NAME	FUNCTION
TSSOP	SO		
1	1	TERMPWR1	**Termination Power 1.** Should be connected to the SCSI TERMPWR line. Must be decoupled with either a 2.2µF capacitor or 4.7µF capacitor. See Figure 2.
2, 8, 12, 18	—	HS-GND	**Heat Sink Ground.** Internally connected to the mounting pad. Should be either grounded or electrically isolated from other circuitry.
3–7, 13–16	2–6, 10–13	R1–R9	**Signal Termination 1–9.** 110Ω termination.
9	7	VREF1	**Reference Voltage 1.** Must be externally connected directly to the VREF2 pin. Must be decoupled with a 4.7µF capacitor as shown in Figure 2.
10	8	GND	**Ground.** 0V, signal ground.
11	9	TERMPWR2	**Termination Power 2.** Should be connected to the SCSI TERMPWR line. Must be decoupled with either a 2.2µF capacitor or 4.7µF capacitor. See Figure 2.
17	14	NC	**No Connection.** Do not connect any signal to this pin.
19	15	VREF2	**Reference Voltage 2.** Must be externally connected directly to the VREF1 pin. Must be decoupled with a 4.7µF capacitor as shown in Figure 2.
20	16	$\overline{PD}$	**Active-Low Power-Down.** When tied low, the DS21S07A enters a power-down mode. Contains an internal 50kΩ pullup resistor. Strap low to deactivate the DS21S07A; leave open circuited to activate the DS21S07A

ABSOLUTE MAXIMUM RATINGS

Voltage Range on Any Pin Relative to Ground……..-1.0V to +7.0V
Operating Temperature Range……..0°C to +70°C
Storage Temperature Range……-55°C to +125°C
Soldering Temperature…….See IPC/JEDEC J-STD-020 Specification

This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

(T_A = 0°C to +70°C)

PARAMETER	SYM	MIN	TYP	MAX	UNITS	NOTES
TERMPWR Voltage	V_{TP}	4.00		5.50	V	
$\overline{PD}$ Active	V_{PDA}	-0.3		+0.8	V	
$\overline{PD}$ Inactive	V_{PDI}	2.0		V_{TP} + 0.3	V	

DC CHARACTERISTICS

(T_A = 0°C to +70°C)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
TERMPWR Current	I_{TP}			250	mA	1, 3
			2.5	5	mA	1, 4
Power-Down Current	I_{PD}		100	150	µA	1, 2, 5
Termination Resistance	R_{TERM}	108	110	112	Ω	1, 2
Die Thermal Shutdown	T_{SD}	150			ºC	1, 6
Power-Down Termination Capacitance	C_{PD}		3.0	5.0	pF	1, 2, 5, 6
Input Leakage High	I_{IH}	-1.0			µA	1, 8
Input Leakage Low	I_{IL}			1.0	µA	1, 7

REGULATOR CHARACTERISTICS

(T_A = 0°C to +70°C)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Output Voltage	V_{REF}	2.79	2.85	2.93	V	1, 2
Drop-Out Voltage	V_{DROP}		0.50	0.75	V	3, 6
Line Regulation	LI_{REG}		1.0	2.0	%	1, 4
Load Regulation	LO_{REG}		1.3	3.0	%	1, 3
Current Limit	I_{LIM}		350		mA	1
Sink Current	I_{SINK}	200			mA	1

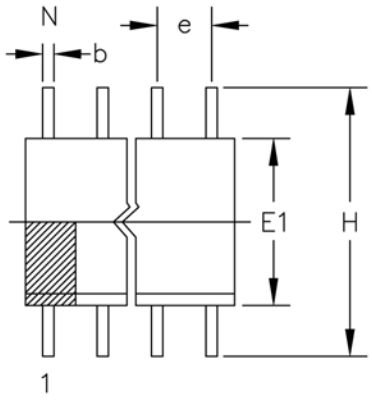
NOTES:

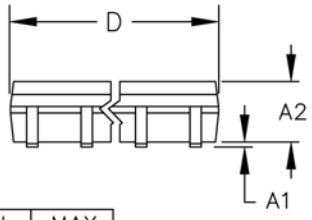
1) 4.00V < TERMPWR < 5.50V.
2) 0.0V < signal lines < TERMPWR.
3) All signal lines = 0V.
4) All signal lines open.
5) $\overline{PD}$ = 0V.
6) Guaranteed by design; not production tested.
7) R1 to R9 only.
8) R1 to R9 and $\overline{PD}$.

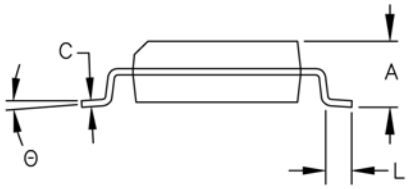
PACKAGE INFORMATION

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/DallasPackInfo**.)

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
A	NEW DRAWING	5/18	J.W.
B	INC. ECN NO. 8680		







LTR		MIN	MAX
A	IN.	0.094	0.105
	MM	2.39	2.67
A1	IN.	0.004	0.012
	MM	0.102	0.30
A2	IN.	0.089	0.095
	MM	2.26	2.41
b	IN.	0.013	0.020
	MM	0.33	0.51
C	IN.	0.009	0.013
	MM	0.229	0.33
e	IN.	.050 BSC	
	MM	1.27 BSC	
E1	IN.	0.290	0.300
	MM	7.37	7.62
H	IN.	0.398	0.416
	MM	10.11	10.57
L	IN.	0.016	0.040
	MM	0.40	1.02
Θ		0°	8°

		16 PIN		18 PIN		20 PIN		24 PIN		28 PIN	
		MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX
D	IN.	0.398	0.412	0.448	0.462	0.498	0.511	0.598	0.612	0.698	0.712
	MM	10.11	10.46	11.38	11.73	12.65	12.99	15.19	15.54	17.73	18.08

THE CHAMFER ON THE BODY IS OPTIONAL.
IF IT IS NOT PRESENT, A TERMINAL 1 IDENTIFIER
MUST BE POSITIONED SO THAT 1/2 OR MORE OF
IT'S AREA IS CONTAINED IN THE HATCHED ZONE.

SIGNATURE		DATE
DOC. CONTROL:	J.WILKINS	5/94
ENGR. MGR:	B.W.MCARTY	5/94
MFG. ENGR:	C.M.SELLS	5/94
CHECKED BY:	C.M.SELLS	5/94
DRAWN BY:	M.W.C.	5/94
DO NOT SCALE DWG.		

DALLAS SEMICONDUCTOR MAXIM

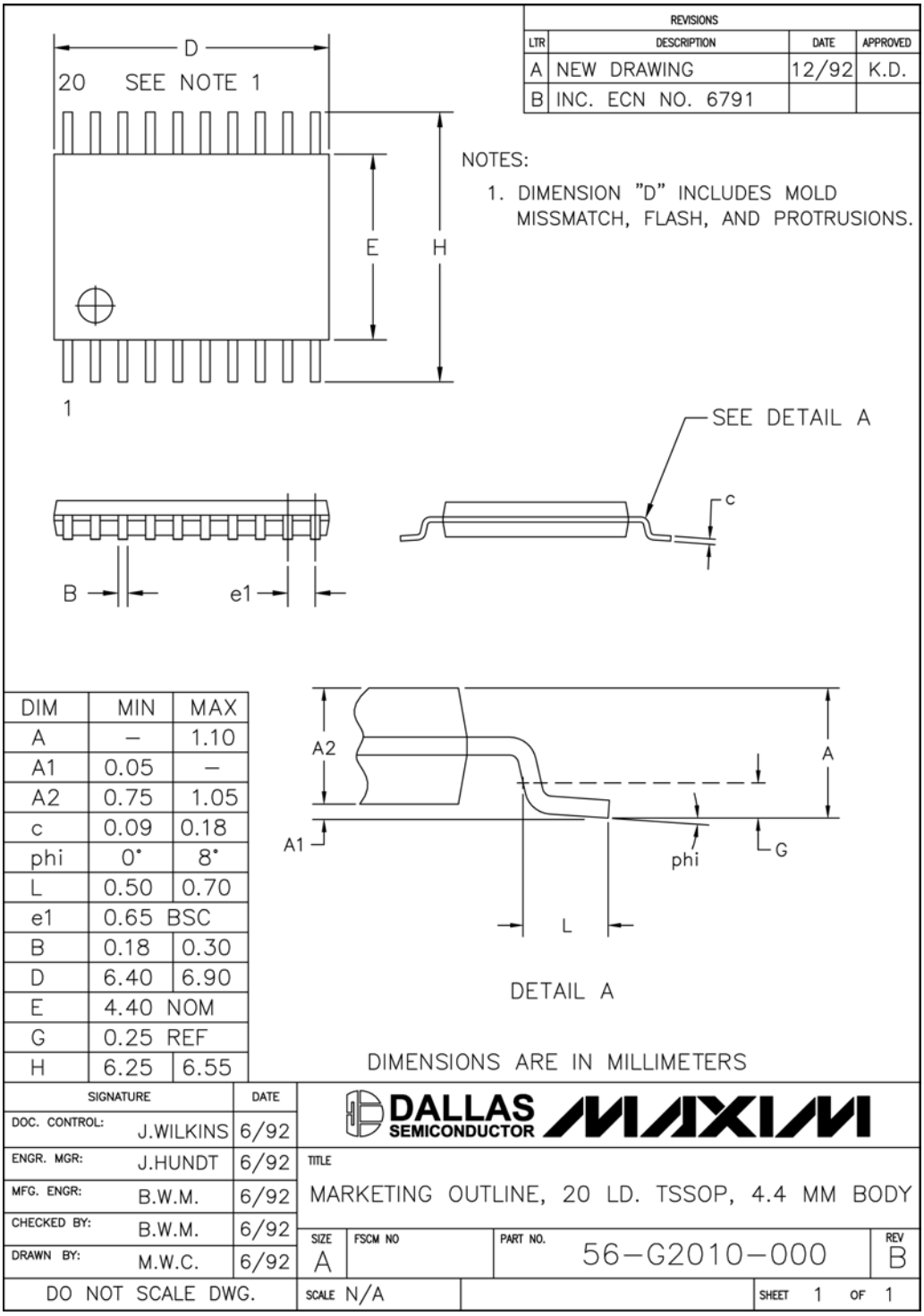
TITLE: PACKAGE OUTLINE .300" SOIC 16,18,20,24&28 LD.

SIZE	FSCM NO	PART NO.	REV
A		56-G4009-001	B

SCALE N/A | SHEET 1 OF 1

PACKAGE INFORMATION (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/DallasPackInfo**.)



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