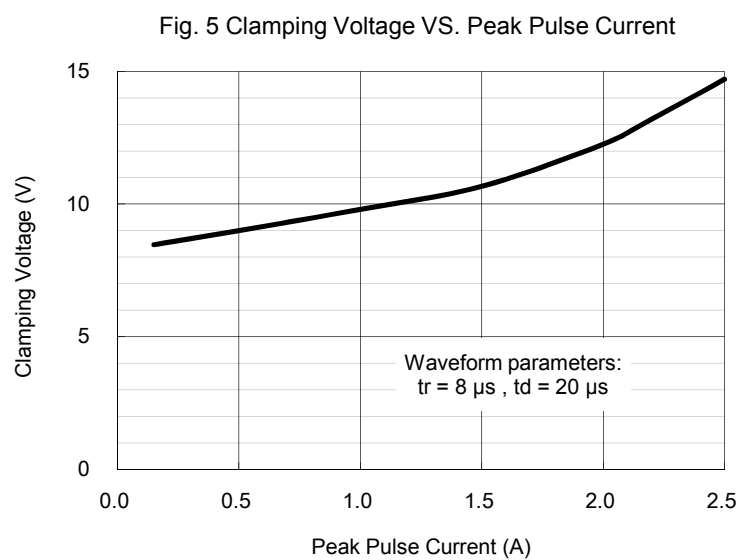
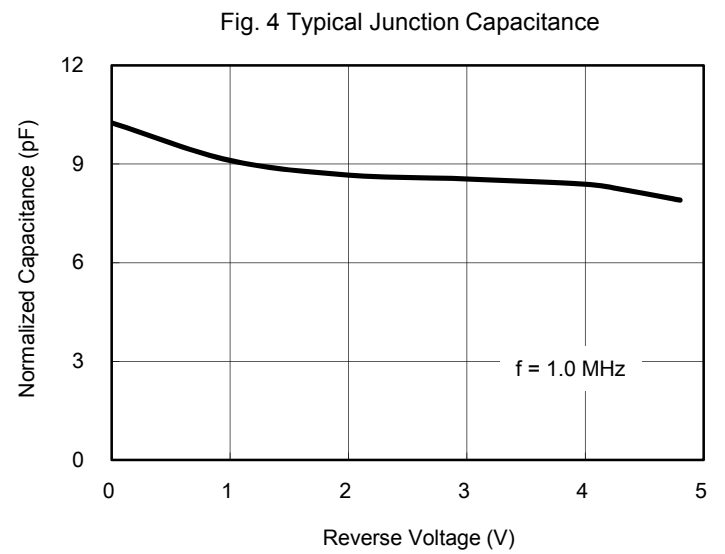
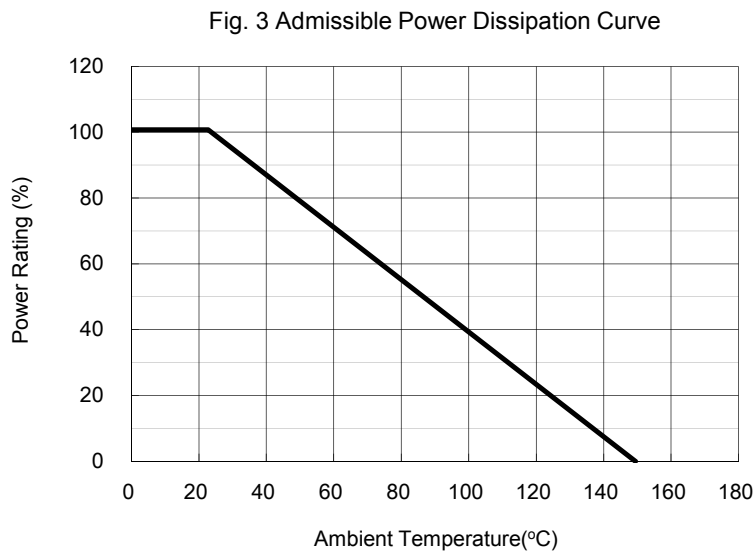
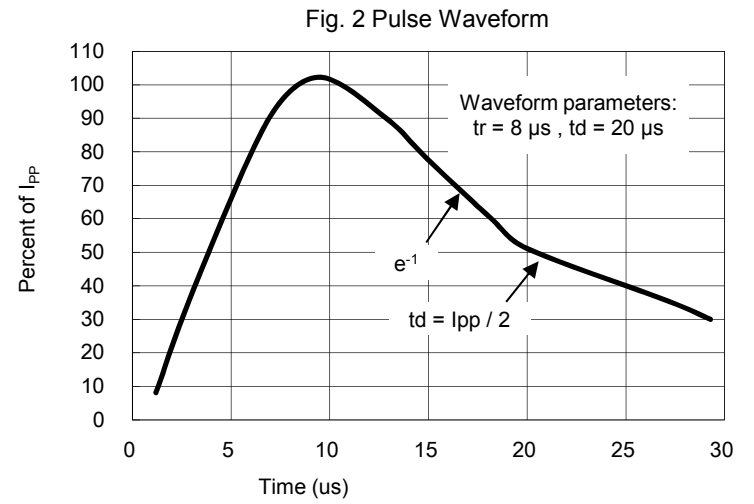
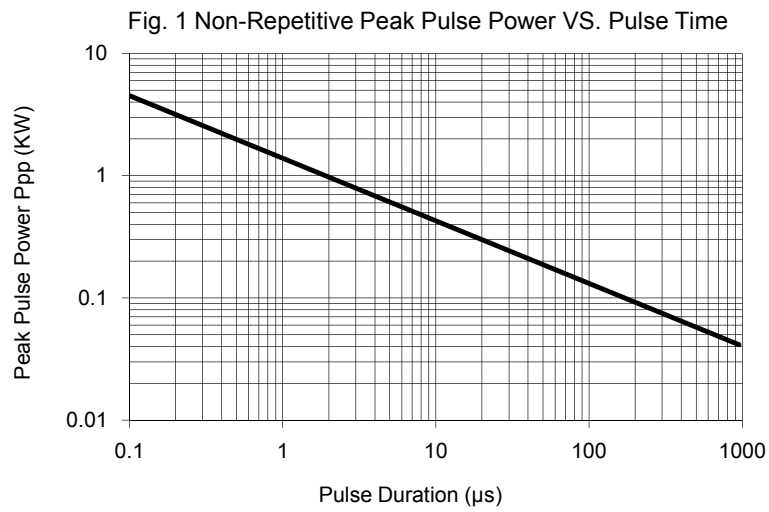


Small Signal Product

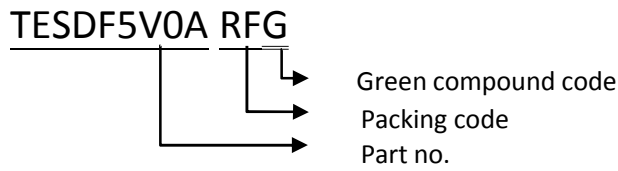
RATINGS AND CHARACTERISTICS CURVES

($T_A=25^\circ\text{C}$ unless otherwise noted)



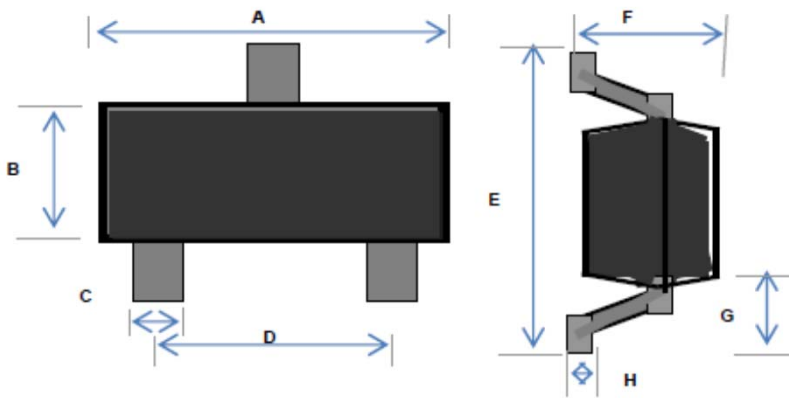
Small Signal Product

ORDER INFORMATION (EXAMPLE)



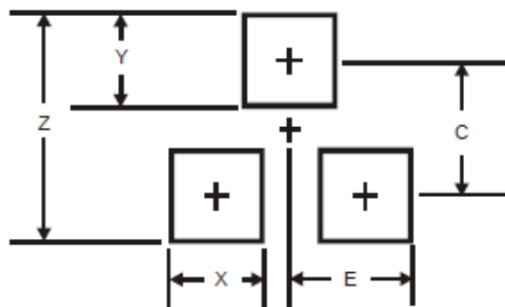
PACKAGE OUTLINE DIMENSIONS

SOT-23



DIM.	Unit (mm)		Unit (inch)	
	Min	Max	Min	Max
A	2.70	3.10	0.106	0.122
B	1.10	1.50	0.043	0.059
C	0.30	0.51	0.012	0.020
D	1.78	2.04	0.070	0.080
E	2.10	2.64	0.083	0.104
F	0.89	1.30	0.035	0.051
G	0.55 REF		0.022 REF	
H	0.10 REF		0.004 REF	

SUGGEST PAD LAYOUT



DIM.	Unit (mm)	Unit (inch)
	Typ.	Typ.
Z	2.8	0.110
X	0.7	0.028
Y	0.9	0.035
C	1.9	0.075
E	1.0	0.039

Note: 1. The suggested land pattern dimensions have been provided for reference only, as actual pad layouts may vary depending on application.

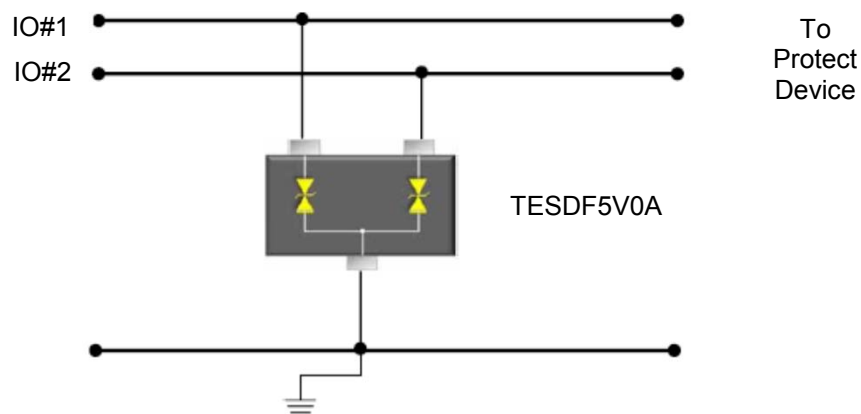
Small Signal Product

APPLICATIONS INFORMATION

- ◇ Designed for the bi-directional protection of 2 lines from the damage caused by Electro Static Discharge (ESD) and surge pulses
- ◇ Be used on lines where the signal polarities are above and below ground
- ◇ Provides a surge capability of 100 Watts peak Ppp per line for an 8/20 ms waveform

CIRCUIT BOARD LAYOUT RECOMMENDATIONS

- ◇ Place the ESD Protection array as close to the input terminal or connector as possible
- ◇ Keep parallel signal paths to a minimum
- ◇ Minimize all printed-circuit board conductive loops including power and group loops
- ◇ Avoid using shared transient return paths to a common ground point
- ◇ Ground planes should be used. For multilayer printed-circuit boards, use ground vias
- ◇ Below picture is the typical application for bi-directional protection of two lines



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