

**electrical characteristics at 25°C case temperature (unless otherwise noted) (continued)**

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
V <sub>GT</sub>	Gate trigger voltage	V <sub>supply</sub> = +12 V†	R <sub>L</sub> = 10 Ω	t <sub>p(g)</sub> > 20 μs			2.2	V
		V <sub>supply</sub> = +12 V†	R <sub>L</sub> = 10 Ω	t <sub>p(g)</sub> > 20 μs			-2.2	
		V <sub>supply</sub> = -12 V†	R <sub>L</sub> = 10 Ω	t <sub>p(g)</sub> > 20 μs			-2.2	
		V <sub>supply</sub> = -12 V†	R <sub>L</sub> = 10 Ω	t <sub>p(g)</sub> > 20 μs			3	
V <sub>T</sub>	On-state voltage	I <sub>T</sub> = ±8.4 A	I <sub>G</sub> = 50 mA	(see Note 5)			±1.7	V
I <sub>H</sub>	Holding current	V <sub>supply</sub> = +12 V†	I <sub>G</sub> = 0	Init' I <sub>TM</sub> = 100 mA			30	mA
		V <sub>supply</sub> = -12 V†	I <sub>G</sub> = 0	Init' I <sub>TM</sub> = -100 mA			-30	
I <sub>L</sub>	Latching current	V <sub>supply</sub> = +12 V†	(see Note 6)			4		mA
		V <sub>supply</sub> = -12 V†				-2		
dv/dt	Critical rate of rise of off-state voltage	V <sub>DRM</sub> = Rated V <sub>DRM</sub>	I <sub>G</sub> = 0	T <sub>C</sub> = 110°C		±20		V/μs
dv/dt <sub>(c)</sub>	Critical rise of commutation voltage	V <sub>DRM</sub> = Rated V <sub>DRM</sub>	I <sub>TRM</sub> = ±8.4 A	T <sub>C</sub> = 70°C	±2	±5		V/μs

† All voltages are with respect to Main Terminal 1.

NOTES: 5. This parameter must be measured using pulse techniques, t<sub>p</sub> = ≤ 1 ms, duty cycle ≤ 2 %. Voltage-sensing contacts separate from the current carrying contacts are located within 3.2 mm from the device body.

6. The triacs are triggered by a 15-V (open-circuit amplitude) pulse supplied by a generator with the following characteristics:  
R<sub>G</sub> = 100 Ω, t<sub>p(g)</sub> = 20 μs, t<sub>r</sub> = ≤ 15 ns, f = 1 kHz.

**thermal characteristics**

PARAMETER		MIN	TYP	MAX	UNIT
R <sub>θJC</sub>	Junction to case thermal resistance			2.5	°C/W
R <sub>θJA</sub>	Junction to free air thermal resistance			62.5	°C/W

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Bourns:

[TIC216D-S](#) [TIC216S-S](#) [TIC216M-S](#)