

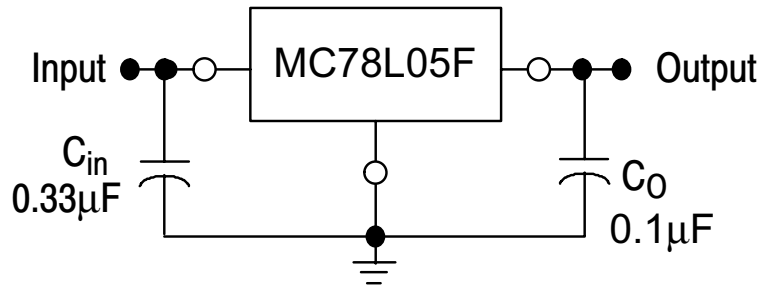
**Electrical Characteristics**

 ( $V_i=10V$ ,  $I_o=40mA$ ,  $0^\circ C < T_j < 120^\circ C$ ,  $C_i=0.33\mu F$ ,  $C_o=0.1\mu F$ , Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	$V_o$	$T_j=25^\circ C$	4.8	5.0	5.2	V
		$7V \leq V_1 \leq 20V$ , $I_o=1mA-40mA$	4.7	-	5.25	V
			5.0			V
		$7V \leq V_1 \leq V_{MAX}$ $I_o=1mA-700mA$ (Note2)	4.7	-	5.25	V
5.0	V					
Load Regulation	$\Delta V_o$	$I_o=1mA-100mA, T_j=25^\circ C$	-	11	60	mV
		$I_o=1mA-40mA, T_j=25^\circ C$	-	5.0	30	mV
Line Regulation	$\Delta V_o$	$7V \leq V_1 \leq 20V, T_j=25^\circ C$	-	8	150	mV
		$8V \leq V_1 \leq 20V, T_j=25^\circ C$	-	6	100	mV
Quiescent Current	$I_q$		-	2.0	5.5	mA
Quiescent Current Change	$\Delta I_q$	$8V \leq V_1 \leq 20V$	-	-	1.5	mA
		$1mA \leq I_o \leq 40mA$	-	-	0.1	mA
Output Noise Voltage	$V_N$	$10Hz \leq f \leq 100KHz$	-	40	-	$\mu V$
Ripple Rejection	RR	$8V \leq V_1 \leq 20V, f=120Hz, T_j=25^\circ C$	41	80	-	dB
Dropout Voltage	$V_d$	$T_j=25^\circ C$	-	1.7	-	V

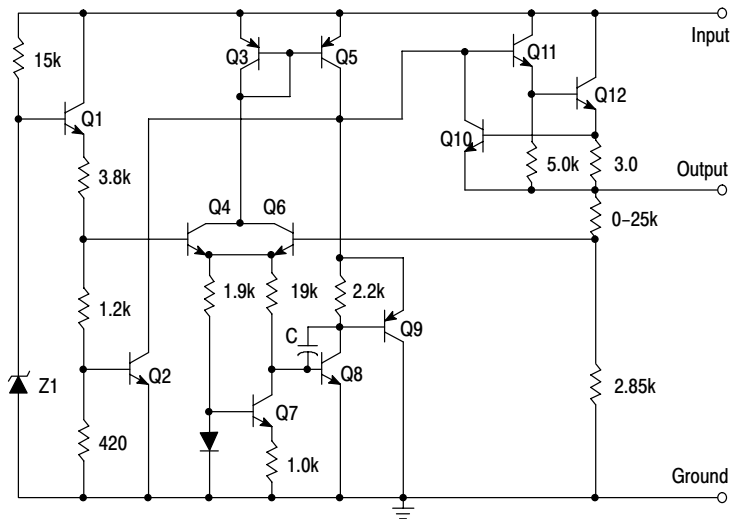
Note: 2. Bypass Capacitors are Recommended for Optimum Stability and Transient Response and should be Located as Close as Possible to The Regulators

**Typical Application**



**Curve Characteristics**

Figure 1. Representative Schematic Diagram



Device	Packing
Part Number-TP	Tape&Reel:1Kpcs/Reel

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