



BCM5720 Block Diagram

The host interface supports a separate PCI Express function for each LAN interface. The BCM5720 includes I/O Virtualization features such as 17 receive and 16 transmit queues, 17 MSI-X vectors with flexible vector-to-queue association. These IOV features enable the BCM5720 to support the VMWare NetQueue and Microsoft VMQ technologies.

The BCM5720 includes a comprehensive set of hardware features that the system may use to implement IEEE 1588- or IEEE

802.1AS-based time synchronization. These hardware features include a high-precision clock, timestamp registers for receive/transmit packets, and programmable trigger inputs and watchdog outputs.

The BCM5720 is fabricated in a low-voltage 65 nm CMOS process and integrates an efficient switching voltage regulator controller for core power supplies. Advanced power management features built into the BCM5720 enable minimiz-

ing the power consumption of the network controller subsystem by lowering the controller's power consumption under lighter network traffic without compromising performance. The BCM5720 implements Energy Efficient Ethernet (EEE) compliance with the IEEE Std 802.3az-2010, which further reduces the power consumption with idle 100 Mbit or 1 Gbit GHY links. These features provide a low power consumption Ethernet controller solution.

Ordering Information

Package	Ambient Temperature	Part Number
169-ball FBGA (14 mm x 14 mm, lead-free)	0°C to 55°C	BCM5720A0KFBG

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