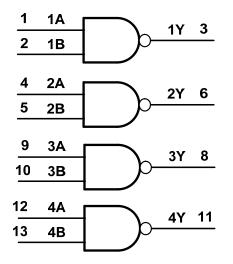


74HC00

Pin Descriptions

Pin Number	Pin Name	Function
1	1A	Data Input
2	1B	Data Input
3	1Y	Data Output
4	2A	Data Input
5	2B	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3B	Data Input
11	4Y	Data Output
12	4A	Data Input
13	4B	Data Input
14	V _{CC}	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
Α	В	Y
L	L	Н
L	н	Н
Н	L	Н
н	Н	L



Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current VI < -0.5V or Vi > V _{CC} +0.5V	±20	mA
I _{OK}	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} + 0.5V$	±20	mA
lo	Continuous output current $-0.5V < V_0 V_{CC} + 0.5V$	+/- 25	mA
lcc	Continuous current through Vcc	50	mA
I _{GND}	Continuous current through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
Ртот	Total Power Dissipation	500	mW

Notes: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values. V_{CC} to the extent the maximum clamp current is exceeded.

5. Input Voltage cannot exceed Vcc to the extent the Maximum clamp current is exceeded.

Recommended Operating Conditions (Note 6) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Мах	Unit
Vcc	Supply Voltage		2.0	6.0	V
VI	Input Voltage		0	V _{CC}	V
Vo	Output Voltage		0	V _{CC}	V
		V _{CC} = 2.0V		625	
Δt/ΔV	Input transition rise or fall rate	V _{CC} = 4.5V		140	ns/V
		V _{CC} = 6.0V		85	
TA	Operating Free-Air Temperature		-40	+125	°C

Note: 6. Unused inputs should be held at V_{CC} or Ground.



Cumb al	Parameter	Test Conditions	V	$T_A = -40^{\circ}C tc$		T _A = -40°0	C to 125°C	Unit
Symbol	Symbol Parameter	Test Conditions	Vcc	Min	Max	Min	Max	Unit
	LiBele Jacob Jacob		2.0V	1.5		1.5		v
VIH	High-level Input Voltage		4.5V	3.15		3.15		
	Volidge		6.0V	4.2		4.2		
	Low lovel input		2.0V		0.5		0.5	
VIL	Low-level input voltage		4.5V 1.35		1.35	V		
voltage		6.0V		1.8		1.8		
	V _{OH} High-level Output Voltage	I _{OH} = -20μA	2.0V	1.9		1.9		v
		I _{OH} = -20μA	4.5V	4.4		4.4		
V _{OH}		I _{OH} = -20μA	6.0V	5.9		5.9		
	Voltage	I _{OH} = -4.0mA	4.5V	3.84		3.7		
		I _{OH} = -5.2mA	6.0V	5.34		5.2		
		I _{OL} = 20μA	2.0V		0.1		0.1	
		I _{OL} = 20μΑ	4.5V		0.1		0.1	
V _{OL}	Low-level Output Voltage	I _{OL} = 20μA	6.0V		0.1		0.1	V
	Voltage	I _{OL} = 4mA	4.5V		0.33		0.44	1
		I _{OL} = 5.2mA	6.0V		0.33		0.44	
li –	Input Current	V _I =GND to 5.5V	6.0V		± 1		± 1	μA
lcc	Supply Current	$V_{I} = GND \text{ or } V_{CC}, I_{O} = 0$	6.0V		20		40	μA

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Switching Characteristics

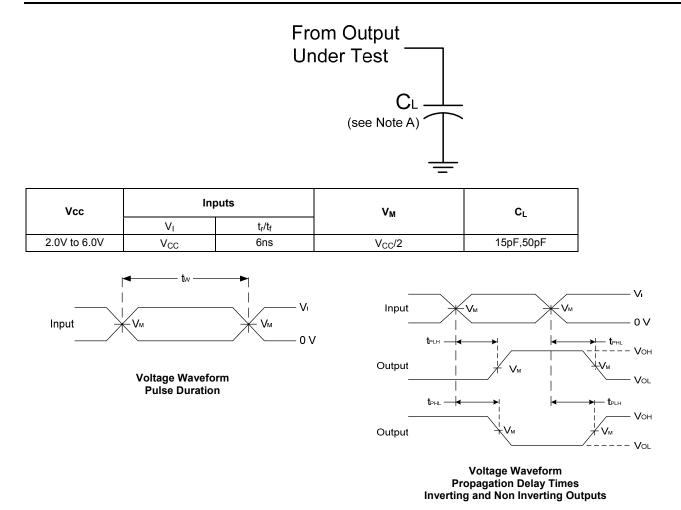
Symbol Devemotor	Parameter C	Parameter	Baramatar	Devementer	Devementer	Test	Test	st v		T _A = +25°0	2	-40°C to +85°C	-40°C to +125°C	Unit
Symbol		Conditions	Vcc	Min	Тур.	Max	Max	Max	Unit					
	Dreneration	F i A	2.0V	—	25	90	115	135						
t _{PD}	$\begin{array}{c} \text{Propagation} \\ \text{PD} \end{array} \begin{array}{c} \text{Propagation} \\ \text{Delay } A_{\text{N}} \text{ to } Y_{\text{N}} \end{array} \begin{array}{c} \text{Figure 1} \\ \text{C}_{\text{L}} = 50 \text{p} \end{array}$	-	4.5V	_	9	18	23	27	ns					
			6.0V	_	7	15	20	23						
		me Figure 1 $C_{L} = 50pF$	2.0V	_	19	75	95	110						
tt	tt Transition Time		4.5V	_	7	15	19	22	ns					
		CL - Sobi	6.0V	—	6	13	16	19						

Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

Parameter		Test Conditions	V _{CC} = 6V Typ	Unit
C _{pd}	Power Dissipation Capacitance per Gate	f = 1 MHz	22	pF
Cı	Input Capacitance	$V_1 = V_{CC} - or GND$	4	pF



Parameter Measurement Information



Notes: A . Includes test lead and test apparatus capacitance.

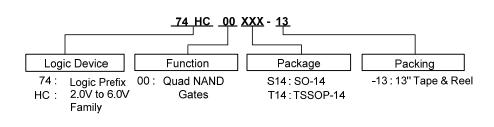
- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz
 C. Inputs are measured separately one transition per measurement
- D. t_{PLH} and t_{PHL} are the same as t_{PD}

Figure 1 Load Circuit and Voltage Waveforms



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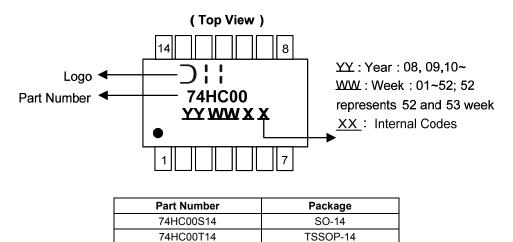
Ordering Information



	Device	Package Code	Packaging	7" Tape a	and Reel
		Fackage Coue	Fackaging	Quantity	Part Number Suffix
Lead-free Green	74HC00S14-13	S14	SO-14	2500/Tape & Reel	-13
Lead-free Green	74HC00T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14

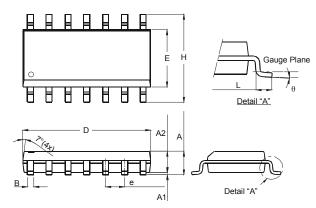




Package Outline Dimensions (All dimensions in mm.)

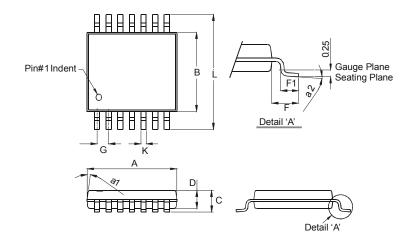
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14		
Dim	Min	Max	
Α	1.47	1.73	
A1	0.10	0.25	
A2	1.45 Typ		
В	0.33	0.51	
D	8.53	8.74	
ш	3.80	3.99	
e	1.27	Тур	
Н	5.80	6.20	
L	0.38	1.27	
θ	0°	8°	
All Di	mensions	s in mm	

Package Type: TSSOP-14

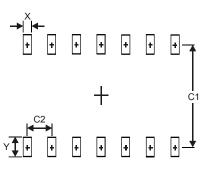


	TSSOP-1	4		
Dim	Min	Max		
a1	7° (4X)		
a2	0°	8°		
Α	4.9	5.10		
В	4.30	4.50		
С		1.2		
D	0.8	1.05		
F	1.00	Тур		
F1	0.45	0.75		
G	0.65	Тур		
Κ	0.19	0.30		
L	L 6.40 Typ			
All Dir	nensions	s in mm		

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

Package Type: SO-14



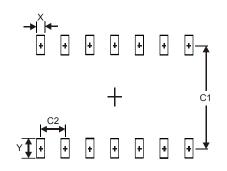
Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27



74HC00

Suggested Pad Layout (cont.)

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65

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