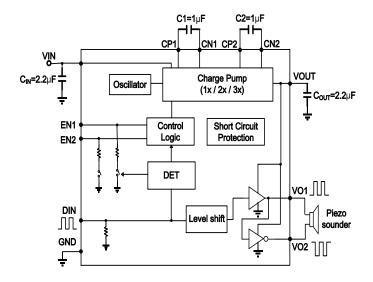
March 2020

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Typical Applications Circuit



Pin Descriptions

Pin N	lumber			
U-QFN3030-16 (Type B)	U-QFN3030-12 (Type A)	Pin Name	I/O/P	Function
1	1	EN1	I	Charge pump mode select 1
2	2	EN2	I	Charge pump mode select 2
3	3	DIN	I	Signal Input
4	_	NC	_	No Connection
5	4	CN1	I	Capacitor 1 Negative Terminal
6	5	GND	Р	Ground
7	6	VO2	0	Positive Output
8	_	NC	_	No Connection
9	7	VO1	0	Negative Output
10	8	CN2	I	Capacitor 2 Negative Terminal
11	9	CP1	I	Capacitor 1 Positive Terminal
12	_	NC	_	No Connection
13	10	VOUT	0	Boost Output
14	11	CP2	I	Capacitor 2 Positive Terminal
15	12	VIN	Р	Power Supply
16	_	NC	_	No Connection



Absolute Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Symbol	Characteristics	Value	Unit
Vin	Supply Voltage	-0.3 to +6.0	V
Vouт	Output Voltage	15	V
VEN1, VEN2	EN1, EN2 Voltage	GND -0.3 to V _{IN} +0.3	V
TA	Operating Free-Air Temperature Range	-40 to +85	°C
TJ	Operating Junction Temperature Range	-40 to +150	°C
Tstg	Storage Temperature Range	-65 to +150	°C

Recommended Operating Conditions (@ T_A = +25°C, unless otherwise specified.)

Symbol	Characteristics		Min	Max	Unit
Vin	Supply Voltage	_	2.3	5.5	V
V _{IH}	High-Level Input Voltage	EN1, EN2	1.2 to V	n +0.3	V
V _{IL}	Low-Level Input Voltage	EN1, EN2	-0.3	+0.4	V
T _A	Operating Free-Air Tempera	Operating Free-Air Temperature		+85	°C

Thermal Information

Parameter	Symbol	Package	Maximum	Unit
Thermal Resistance (Junction to Ambient)	θ JA	U-QFN3030-16 (Type B) U-QFN3030-12 (Type A)	35	°C/W
Thermal Resistance (Junction to Case)	θЈС	U-QFN3030-16 (Type B) U-QFN3030-12 (Type A)	14	°C/W

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$\textbf{Electrical Characteristics} \ (@T_A = +25^{\circ}\text{C}, \ V_{\text{IN}} = 3.0\text{V}, \ C_{\text{PIEZO}} = 15\text{nF}, \ f_{\text{DIN}} = 4\text{kHz}, \ unless \ otherwise \ specified.})$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Output Voltage Range	Vout	(Note 4)	2.1	_	12	V
	Vout1	1x Mode	2.8	_	3	V
Output Voltage	Vout2	2x Mode	5.2	_	6	V
	Vоитз	3x Mode (Note 5)	7.2	_	9	V
	I _{DD11}	1x Mode, CPIEZO = No Load	_	50	_	μΑ
Operating Current 1	I _{DD12}	2x Mode, CPIEZO = No Load	_	720	_	μΑ
	I _{DD13}	3x Mode, CPIEZO = No Load	_	1,700	_	μΑ
	I _{DD21}	1x Mode, Single-ended application	_	0.3	_	mA
Operating Current 2	I _{DD22}	2x Mode, Single-ended application	_	1.4	_	mA
	I _{DD23}	3x Mode, Single-ended application	_	3.9	_	mA
	I _{DD31}	1x Mode, Differential application	_	0.9	_	mA
Operating Current 3	I _{DD32}	2x Mode, Differential application	_	3.6	_	mA
	I _{DD33}	3x Mode, Differential application	_	7.9	_	mA
Shutdown Current	I _{SD}	DIN = 0V	_	_	1	μΑ
Input Frequency	fin	Rectangular pulse	_	4	_	kHz
Oscillating Frequency	fosc	_	_	1	_	MHz
	t _{ON1}	1x Mode, From DIN signal High to 90% Vout steady state	_	270		μs
VOUT Start Delay Time	ton2	2x Mode, From DIN signal High to 90% V _{OUT} steady state	_	320	_	μs
	t _{ON3}	3x Mode From DIN signal High to 90% Vout steady state	_	350	_	μs
Shutdown Delay Time	toff	DIN = H- > L	_	42	_	ms
Output Short-Circuit Current	Isc	_	_	40	_	mA
Control Terminal Voltage H	ViH	EN1, EN2, DIN pins	0.8*VIN	-	Vin	V
Control Terminal Voltage L	VIL	EN1, EN2, DIN pins	0	-	0.2*V _{IN}	V
Control Terminal Current 1	I _{IH1}	DIN = 3V	_	_	1	μΑ
Control Terminal Current 2	I _{IH2}	V _{EN1} , V _{EN2} = 3V, DIN = 3V	_		1	μΑ
Control Terminal Current 3	Інз	V _{EN1} , V _{EN2} = 3V, DIN = 0V	_	_	1	μΑ

Notes:

^{4.} It is possible to drive VOUT, VO1 and VO2 to 3x V_{DD}. A supply voltage of 4V of more should not be used in 3x mode as this will exceed the maximum output voltage rating.

5. When designed under 3x mode, it should be carefully noted that the V_{OUT} absolute maximum value should not exceed 15V.



Application Information

Charge Pump Mode Setting

The Charge Pump Mode (CPM) pins EN1 and EN2 are used to set the charge pump into mode 1x V_{DD}, 2x V_{DD}, 3x V_{DD} or they can be used to put the PAM8904 in to a forced low current shutdown mode.

DIN	EN1	EN2	MODE
0	_	_	Shutdown Mode
1	0	0	Shutdown Mode
1	0	1	1x Mode
1	1	0	2x Mode
1	1	1	3x Mode

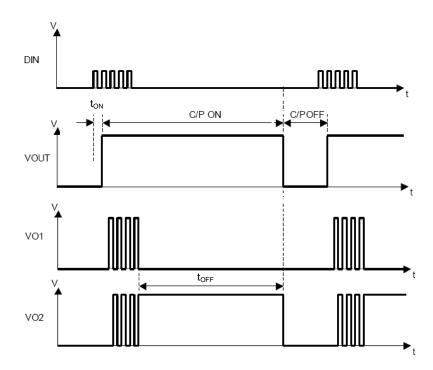
Care must be taken when using the 3x mode with a V_{DD} supply of 5V or more, as this will force the V_{OUT} to exceed its Absolute Maximum specification (15V).

V _{DD} Range	Mode
2.3V to 5.5V	1x, 2x and 3x

Timing Chart and Device Operation

When one or both of the EN pins are pulled high, the device enters normal operation mode, refer to the above table for the mode selection. Once the PAM8904 senses a valid signal on the DIN pin, the charge pump will start and provide the desired voltage on the VOUT pin and the output drive VO1 and VO2 start to function after time ton which is typically between 270µs and 350µs depending on the mode chosen. Once the input signal on DIN is removed, the PAM8904 senses this and waits typically 42ms to ensure the signal has been removed. If there is no further valid signal within the time period toFF, the PAM8904 enters into a low current standby mode.

Timing Chart





Application Information (continued)

Output Configuration

The PAM8904 is able to be configured either in a differential or a single ended configuration.

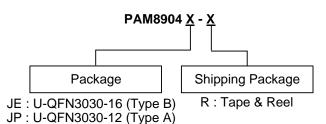
Short-Circuit Protection

The PAM8904 has short circuit protection circuitry on the outputs to prevent damage. Once a short circuit is detected on the outputs the chip will limit the total current to protect the output device. This is not a latched fault; once the short is removed the normal operation is restored.

Thermal Protection

If the junction temperature of the PAM8904 exceeds +150°C the device will enter overtemperature shutdown. The outputs and the charge pump will be switched off. Once the junction temperature cools down to its normal operating condition, the IC will re-start automatically.

Ordering Information



Part Number	Package Type	Shipping
PAM8904JER	U-QFN3030-16 (Type B)	3,000/Tape & Reel
PAM8904JPR	U-QFN3030-12 (Type A)	3,000/Tape & Reel

Marking Information

U-QFN3030-16 (Type B) / U-QFN3030-12 (Type A)

(Top View)



P8904: Product Code XXX: Internal Code Y: Year 0-9

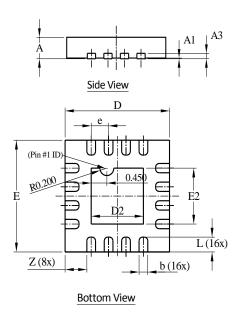
W: Week: A~Z: 1~26 weeks; a~z: 27~52 weeks; z represents 52 and 53 weeks



Package Outline Dimensions

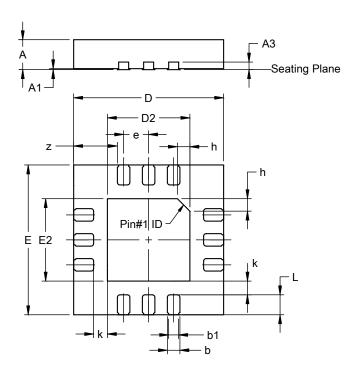
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: U-QFN3030-16 (Type B)



U-QFN3030-16						
	Type B					
Dim	Min	Max	Тур			
Α	0.55	0.65	0.60			
A1	0	0.05	0.02			
A3	-	-	0.15			
b	0.18	0.28	0.23			
D	2.95	3.05	3.00			
D2	1.40	1.60	1.50			
Е	2.95	3.05	3.00			
E2	1.40	1.60	1.50			
е	-	-	0.50			
L	0.35	0.45	0.40			
Z	-	ı	0.625			
All Dimensions in mm						

(2) Package Type: U-QFN3030-12 (Type A)



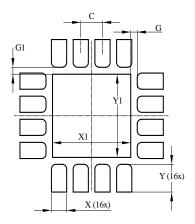
U-QFN3030-12						
	(Type A)					
Dim	Min	Max	Тур			
Α	0.55	0.65	0.60			
A1	0.00	0.05	0.02			
A3	1	1	0.152			
b	0.20	0.35	0.25			
b1	0.15	0.25	0.20			
D	2.95	3.05	3.00			
D2	1.55	1.75	1.65			
E	2.95	3.05	3.00			
E2	1.55	1.75	1.65			
е			0.50			
h	-	-	0.25			
L	0.35	0.45	0.40			
k			0.275			
Z			0.875			
All Dimensions in mm						



Suggested Pad Layout

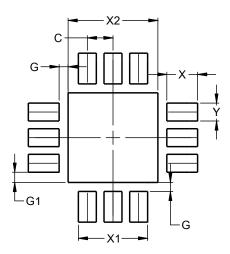
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: U-QFN3030-16 (Type B)



Dimensions	Value
	(in mm)
С	0.500
G	0.150
G1	0.150
Х	0.350
X1	1.800
Υ	0.600
Y1	1.800

(2) Package Type: U-QFN3030-12 (Type A)



Dimensions	Value (in mm)
С	0.500
G	0.175
G1	0.200
Х	0.600
X1	1.350
X2	1.750
Y	0.350
Y1	1 750



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