



<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		$V_R$	5	V
Forward current		$I_F$	100	mA
Peak forward current	$t_p/T = 0.5$ , $t_p = 100\text{ }\mu\text{s}$	$I_{FM}$	200	mA
Surge forward current	$t_p = 100\text{ }\mu\text{s}$	$I_{FSM}$	1.5	A
Power dissipation		$P_V$	170	mW
Junction temperature		$T_j$	100	$^{\circ}\text{C}$
Operating temperature range		$T_{amb}$	-40 to +85	$^{\circ}\text{C}$
Storage temperature range		$T_{stg}$	-40 to +100	$^{\circ}\text{C}$
Soldering temperature	According to Fig. 11, J-STD-020	$T_{sd}$	260	$^{\circ}\text{C}$
Thermal resistance junction-to-ambient	J-STD-051, soldered on PCB	$R_{thJA}$	250	K/W

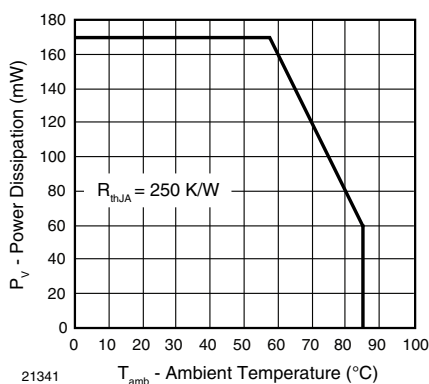


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

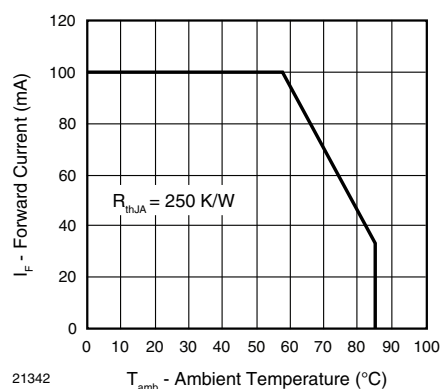


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100\text{ mA}$ , $t_p = 20\text{ ms}$	$V_F$	-	1.3	1.7	V
	$I_F = 1\text{ A}$ , $t_p = 100\text{ }\mu\text{s}$	$V_F$	-	1.8	-	V
Temperature coefficient of $V_F$	$I_F = 100\text{ mA}$	$TK_{V_F}$	-	-1.3	-	mV/K
Reverse current	$V_R = 5\text{ V}$	$I_R$	-	-	100	$\mu\text{A}$
Junction capacitance	$V_R = 0\text{ V}$ , $f = 1\text{ MHz}$ , $E = 0$	$C_j$	-	30	-	pF
Radiant intensity	$I_F = 100\text{ mA}$ , $t_p = 20\text{ ms}$	$I_e$	1.6	4.5	8	mW/sr
	$I_F = 1.5\text{ A}$ , $t_p = 100\text{ }\mu\text{s}$	$I_e$	-	35	-	mW/sr
Radiant power	$I_F = 100\text{ mA}$ , $t_p = 20\text{ ms}$	$\phi_e$	-	15	-	mW
Temperature coefficient of $\phi_e$	$I_F = 100\text{ mA}$	$TK_{\phi_e}$	-	-0.8	-	%/K
Angle of half intensity		$\phi$	-	$\pm 60$	-	$^{\circ}$
Peak wavelength	$I_F = 100\text{ mA}$	$\lambda_p$	-	950	-	nm
Spectral bandwidth	$I_F = 100\text{ mA}$	$\Delta\lambda$	-	50	-	nm
Temperature coefficient of $\lambda_p$	$I_F = 100\text{ mA}$	$TK_{\lambda_p}$	-	0.2	-	nm/K
Rise time	$I_F = 20\text{ mA}$	$t_r$	-	800	-	ns
	$I_F = 1\text{ A}$	$t_r$	-	400	-	ns
Fall time	$I_F = 20\text{ mA}$	$t_f$	-	800	-	ns
	$I_F = 1\text{ A}$	$t_f$	-	400	-	ns
Virtual source diameter	EN 60825-1	$d$	-	0.5	-	mm



**BASIC CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

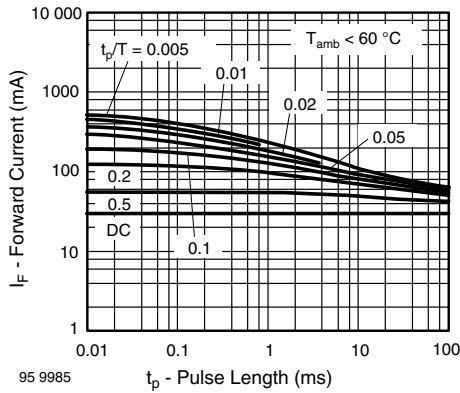


Fig. 3 - Pulse Forward Current vs. Pulse Duration

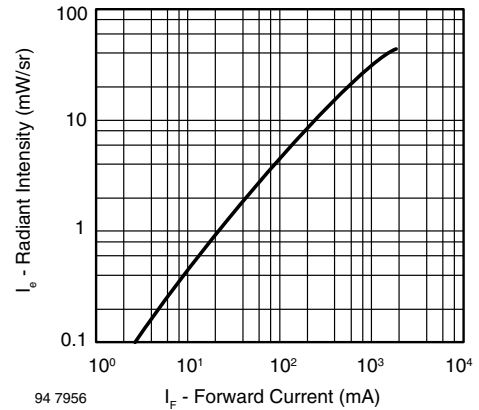


Fig. 6 - Radiant Intensity vs. Forward Current

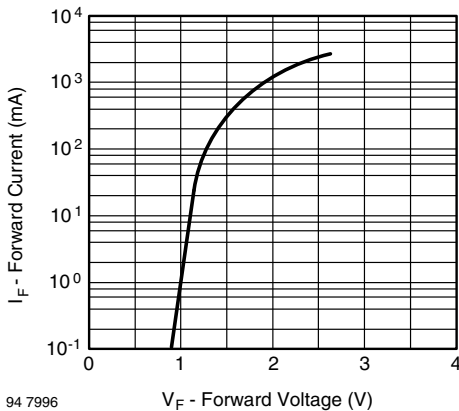


Fig. 4 - Forward Current vs. Forward Voltage

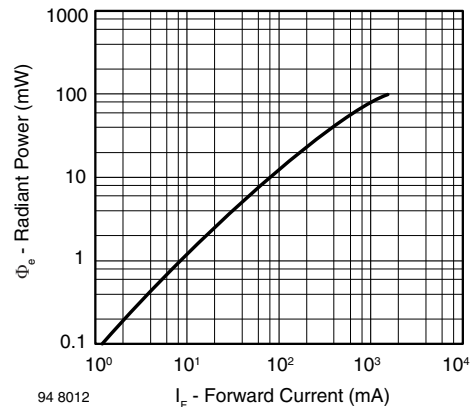


Fig. 7 - Radiant Power vs. Forward Current

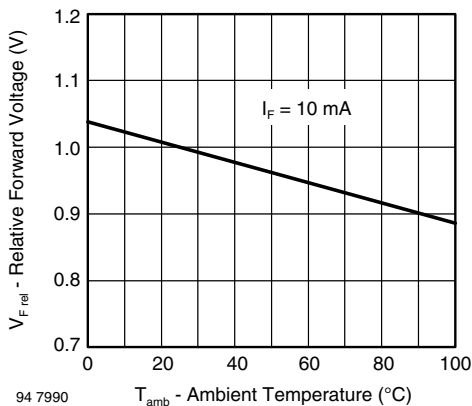


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

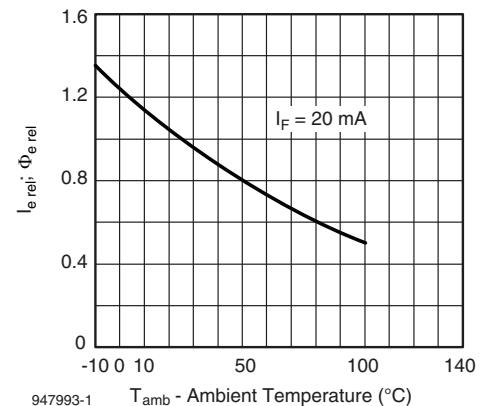


Fig. 8 - Relative Radiant Intensity/Power vs. Ambient Temperature

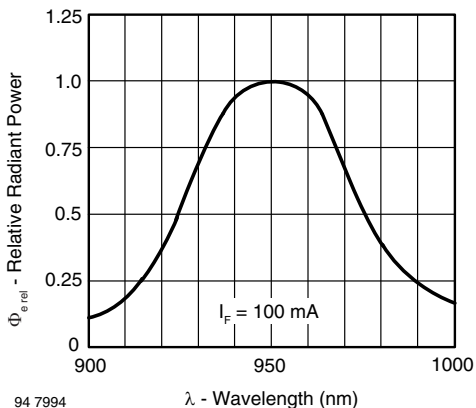


Fig. 9 - Relative Radiant Power vs. Wavelength

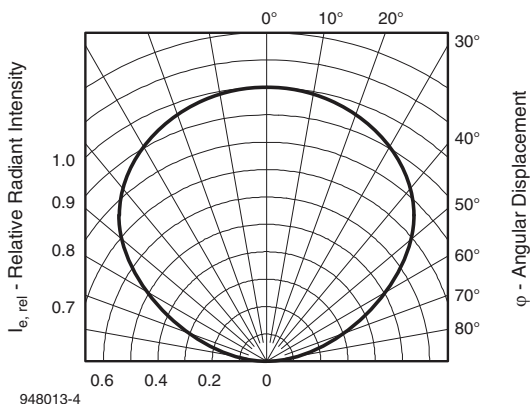
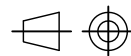
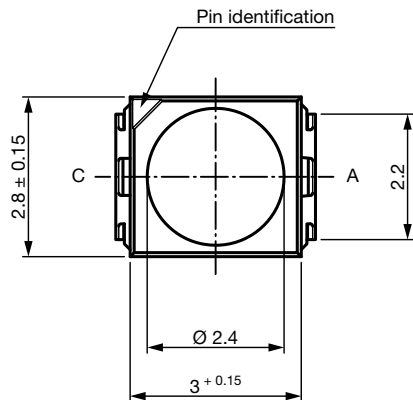
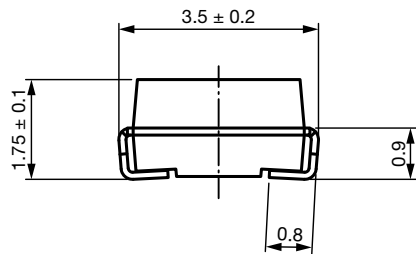


Fig. 10 - Relative Radiant Intensity vs. Angular Displacement

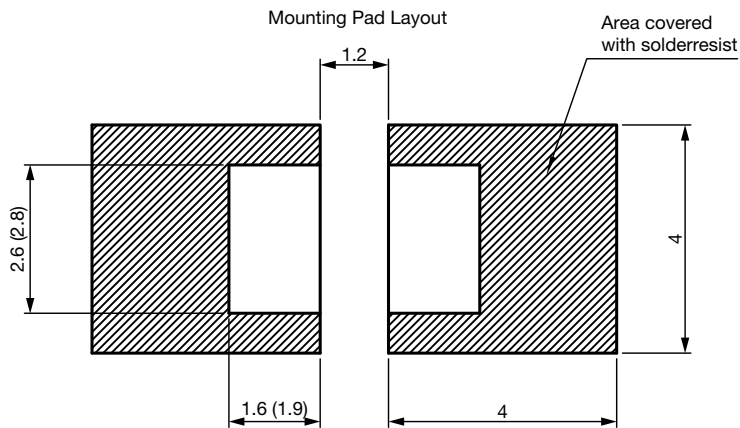


**PACKAGE DIMENSIONS** in millimeters



technical drawings  
according to DIN  
specifications

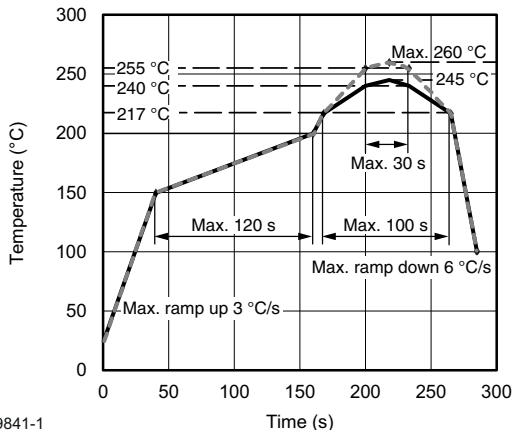
Drawing-No.: 6.541-5067.01-4  
Issue: 7; 12.03.14



Dimensions: reflow and vapor phase (wave soldering)



**SOLDER PROFILE**



19841-1

Fig. 11 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

**DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

**FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions:  $T_{amb} < 30\text{ }^{\circ}\text{C}$ ,  $\text{RH} < 60\%$

Moisture sensitivity level 3, acc. to J-STD-020

**DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at  $40\text{ }^{\circ}\text{C}$  (+  $5\text{ }^{\circ}\text{C}$ ),  $\text{RH} < 5\%$ .

**TAPE AND REEL**

PLCC-2 components are packed in antistatic blister tape (DIN IEC (CO) 564) for automatic component insertion. Cavities of blister tape are covered with adhesive tape.

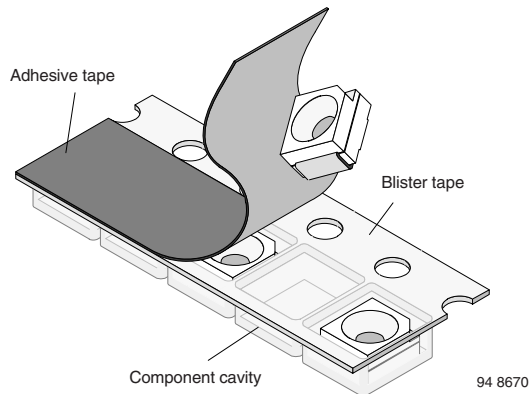
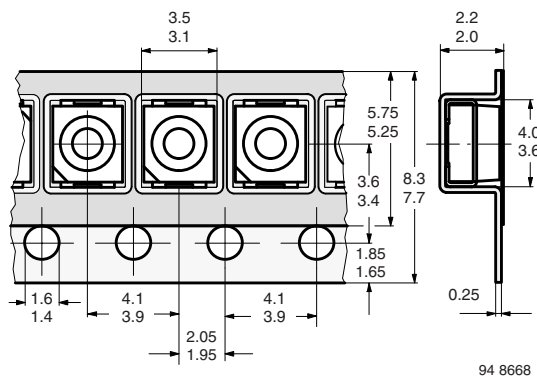


Fig. 12 - Blister Tape



94 8668

Fig. 13 - Tape Dimensions in mm for PLCC-2

**MISSING DEVICES**

A maximum of 0.5 % of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

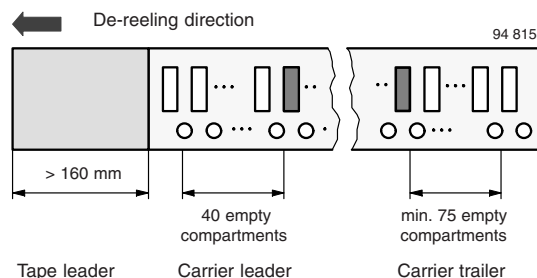


Fig. 14 - Beginning and End of Reel



The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartments. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape. The least component is followed by a carrier tape trailer with a least 75 empty compartments and sealed with cover tape.

**COVER TAPE REMOVAL FORCE**

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s. In order to prevent components from popping out of the blisters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.

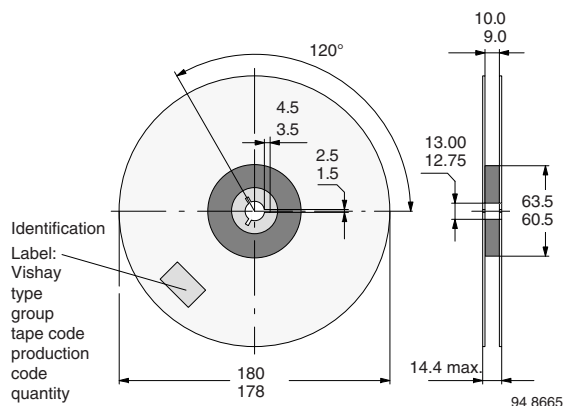


Fig. 15 - Dimensions of Reel-GS08

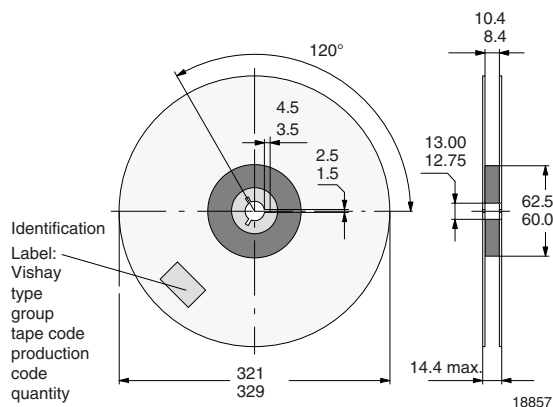


Fig. 16 - Dimensions of Reel-GS18



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