Vishay Semiconductors

Silicon PIN Photodiode



BASIC CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 50 mA	V _F		1		V
Breakdown voltage	I _R = 100 μA, E = 0	V _(BR)	32			V
Reverse dark current	V _R = 10 V, E = 0	I _{ro}		1	10	nA
Diode capacitance	V _R = 0 V, f = 1 MHz, E = 0	CD		4		pF
	$V_{R} = 5 V, f = 1 MHz, E = 0$	CD		1.3		pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	TK _{Vo}		- 2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	l _k		11		μA
Temperature coefficient of I_k	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	ΤK _{Ik}		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}, \\ V_R = 5 \text{ V}$	I _{ra}	5	12		μA
Angle of half sensitivity		φ		± 15		deg
Wavelength of peak sensitivity		λρ		940		nm
Range of spectral bandwidth		λ _{0.5}		750 to 1050		nm
Rise time	$V_{R} = 10 \text{ V}, \text{R}_{L} = 1 \text{k}\Omega, \\ \lambda = 820 \text{ nm}$	t _r		100		ns
Fall time	$V_{R} = 10 \text{ V}, \text{R}_{L} = 1 \text{k}\Omega,$ $\lambda = 820 \text{ nm}$	t _f		100		ns

Note

T_{amb} = 25 °C, unless otherwise specified

BASIC CHARACTERISTICS

 $T_{amb} = 25 \ ^{\circ}C$, unless otherwise specified

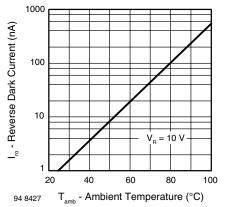


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

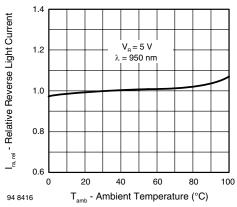


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature



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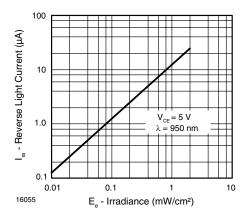


Fig. 3 - Reverse Light Current vs. Irradiance

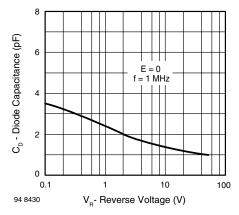


Fig. 4 - Diode Capacitance vs. Reverse Voltage

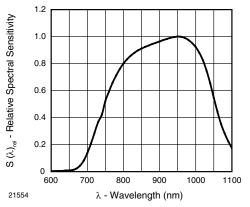


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

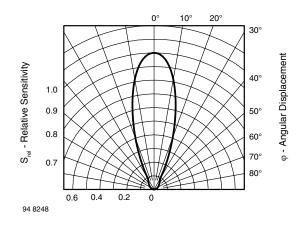


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

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REFLOW SOLDER PROFILE

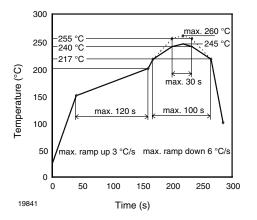


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020D

PACKAGE DIMENSIONS in millimeters: VEMD2000

DRYPACK

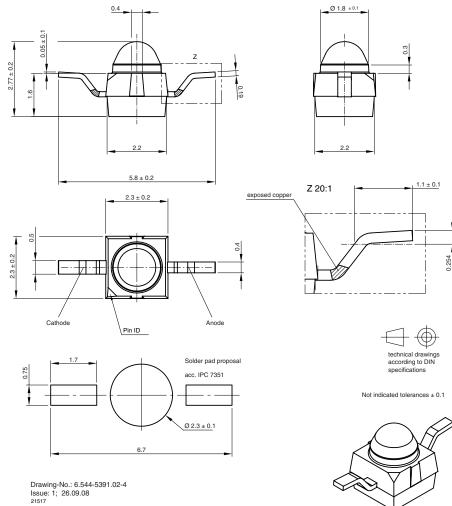
Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks Conditions: $T_{amb} < 30$ °C, RH < 60 % Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

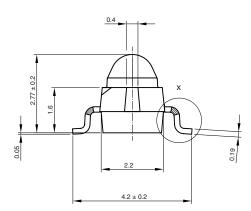


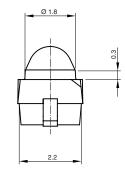


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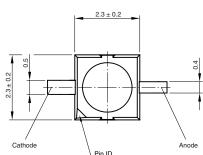
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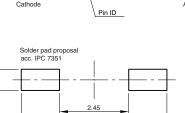
PACKAGE DIMENSIONS in millimeters: VEMD2020





X 20:1

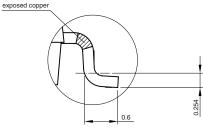




5.15

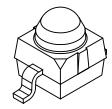
Drawing-No.: 6.544-5383.02-4 Issue: 3; 26.09.08 21488

0.75





Not indicated tolerances ± 0.1

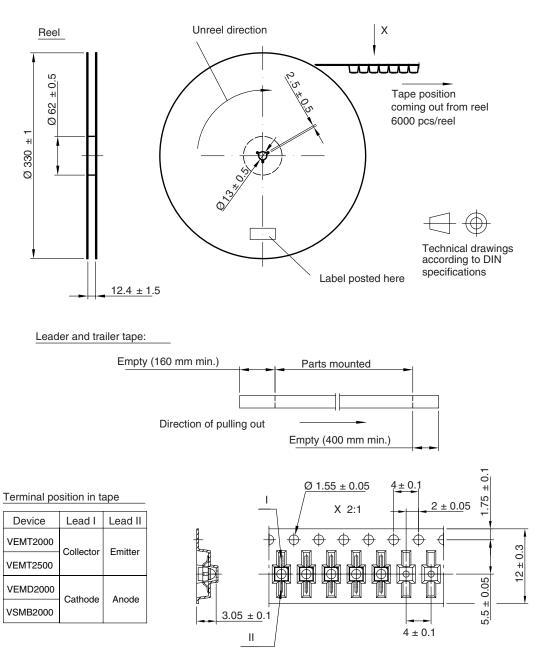


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TAPING AND REEL DIMENSIONS in millimeters: VEMD2000



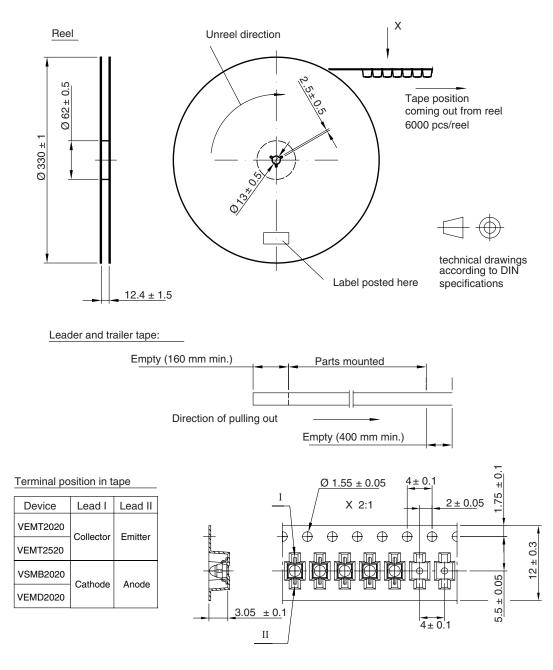
Drawing-No.: 9.800-5100.01-4 Issue: X; 29.04.09 21572



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TAPING AND REEL DIMENSIONS in millimeters: VEMD2020



Drawing-No.: 9.800-5091.01-4 Issue: X; 29.04.09 21571



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