Quad PIN diode attenuator

2 Pinning information

Table 1. Discrete pinning

Pin	Description	Simplified outline	Graphic symbol			
1	RF in	D	_			
2	series bias	5	5 4			
3	RF out					
4	shunt 1 bias	1 1 2 13	1 2 3			
5	shunt 2 bias		sym142			

3 Ordering information

Table 2. Ordering information

Type number	Package				
	Name	Description	Version		
BAP70Q	SC-74A	plastic surface-mounted package; 5 leads	SOT753		

4 Marking code

Table 3. Marking

Type number	Marking code
BAP70Q	A2

5 Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_R	reverse voltage		[1]	-	50	V
I _F	forward current		[1]	-	100	mA
P _{tot}	total power dissipation	T _{sp} ≤ 90 °C	[1]	-	125	mW
T _{stg}	storage temperature			-65	+150	°C
Tj	junction temperature			-65	+150	°C

^[1] single diode.

6 Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		350	K/W

BAP70Q

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Quad PIN diode attenuator

7 Characteristics

Table 6. Characteristics

 T_i = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
V _F	forward voltage	I _F = 50 mA	-	0.95	1.1	٧
I _R	reverse current	V _R = 50 V	-	-	100	nA
C _d diode capacitance f = 1 MHz (see <u>Figure 1</u>)						
		V _R = 0 V	-	600	-	fF
		V _R = 1 V	-	430	-	fF
		V _R = 20 V	-	250	300	fF
r _D	diode forward resistance	f = 100 MHz (see <u>Figure 2</u>)				
		I _F = 0.5 mA	-	77	100	Ω
		I _F = 1 mA	-	40	50	Ω
		I _F = 10 mA	-	5.4	7	Ω
		I _F = 100 mA	-	1.4	1.9	Ω
τι	charge carrier life time	when switched from I_F = 10 mA to I_R = 6 mA; R_L = 100 Ω ; measured at I_R = 3 mA	-	1.25	-	μs

Quad PIN diode attenuator

8 Graphical data

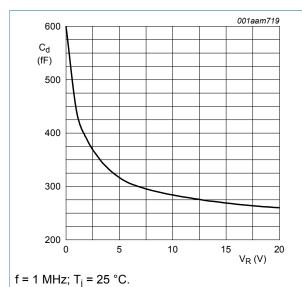
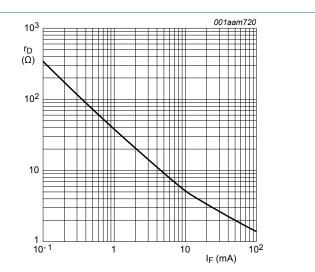


Figure 1. Diode capacitance as a function of reverse

voltage (typical values)



f = 100 MHz; $T_i = 25 \,^{\circ}\text{C}$.

Figure 2. Diode forward resistance as a function of forward current (typical values)

Quad PIN diode attenuator

9 Application information

9.1 Application circuit

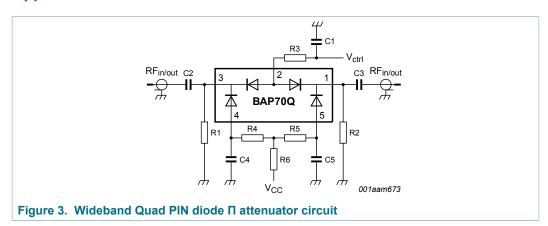


Table 7. List of components used for the typical application

Component	Description	Conditions	Value
C1; C2; C3; C4; C5	chip capacitor	V _{CC} = 3.7 V	47 nF
		V _{CC} = 5 V	47 nF
R1; R2	chip resistor	V _{CC} = 3.7 V	560 Ω
		V _{CC} = 5 V	910 Ω
R3	chip resistor	V _{CC} = 3.7 V	330 Ω
		V _{CC} = 5 V	1000 Ω
R4; R5	chip resistor	V _{CC} = 3.7 V	1500 Ω
		V _{CC} = 5 V	2000 Ω
R6	chip resistor	V _{CC} = 3.7 V	680 Ω
		V _{CC} = 5 V	1000 Ω

Quad PIN diode attenuator

9.2 Quad PIN pi attenuator characteristics

Table 8. Typical performance for BAP70Q quad PIN diode Π attenuator

 V_{CC} = 3.7 V; T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Test Conditions	Тур	Units	
L _{ins}	insertion loss	V _C = 10 V; f = 1 GHz	3	dB	
RLin	input return loss	V _C = 0 V; f = 1 GHz	24	dB	
а	attenuation	V _C = 0 V; f = 1 GHz	44	dB	
IP3 _i	input third-order intercept point	f = 0.1 GHz		,	
		V _{ctrl} = 2 V	38	dBm	
		V _{ctrl} = 10 V	45	dBm	
		f = 0.9 GHz			
		V _{ctrl} = 2 V	45	dBm	
		V _{ctrl} = 10 V	45	dBm	
		f = 1.8 GHz			
		V _{ctrl} = 2 V	45	dBm	
		V _{ctrl} = 10 V	45	dBm	
		f = 2.1 GHz			
		V _{ctrl} = 2 V	44	dBm	
		V _{ctrl} = 10 V	44	dBm	

Quad PIN diode attenuator

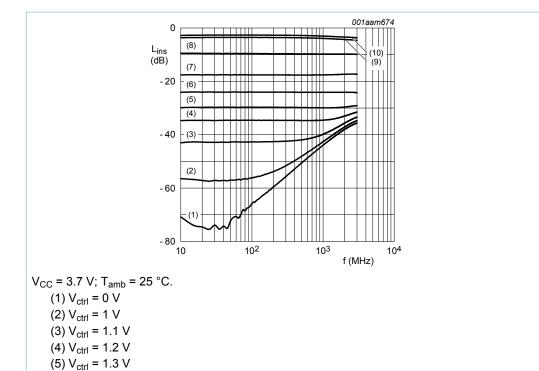
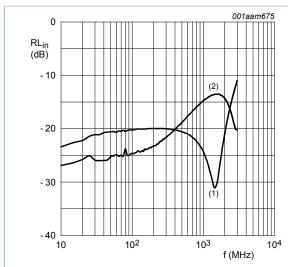


Figure 4. Insertion loss as function of frequency (typical values)

(6) $V_{ctrl} = 1.5 \text{ V}$ (7) $V_{ctrl} = 2 \text{ V}$ (8) $V_{ctrl} = 4 \text{ V}$ (9) $V_{ctrl} = 7.5 \text{ V}$ (10) $V_{ctrl} = 10 \text{ V}$

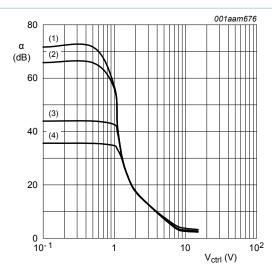
Quad PIN diode attenuator



 V_{CC} = 3.7 V; T_{amb} = 25 °C.

(1)
$$V_{ctrl} = 0 V$$

(2)
$$V_{ctrl} = 15 \text{ V}$$



 V_{CC} = 3.7 V; T_{amb} = 25 °C.

(1) f = 10 MHz

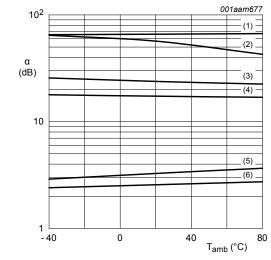
(2) f = 100 MHz

(3) f = 1000 MHz

(4) f = 3000 MHz

Figure 5. Return loss as function of frequency (typical values)





 $V_{CC} = 3.7 \text{ V}$; f = 100 MHz.

(1)
$$V_{ctrl} = 0 V$$

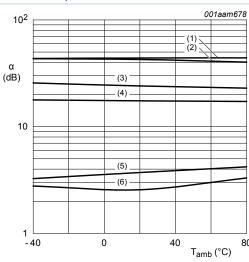
(2)
$$V_{ctrl} = 1 V$$

(3)
$$V_{ctrl} = 1.5 V$$

(4)
$$V_{ctrl} = 2 V$$

(5)
$$V_{ctrl} = 7.5 V$$

(6)
$$V_{ctrl} = 10 \text{ V}$$



 $V_{CC} = 3.7 \text{ V}$; f = 1000 MHz.

(1)
$$V_{ctrl} = 0 V$$

(2)
$$V_{ctrl} = 1 V$$

(3)
$$V_{ctrl} = 1.5 \text{ V}$$

(4)
$$V_{ctrl} = 2 V$$

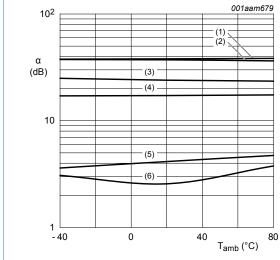
(5)
$$V_{ctrl} = 7.5 V$$

(6)
$$V_{ctrl} = 10 \text{ V}$$

Figure 7. Attenuation as function of temperature (typical Figure 8. Attenuation as function of temperature (typical values)

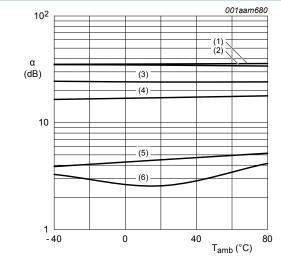
values)

Quad PIN diode attenuator



 $V_{CC} = 3.7 \text{ V}$; f = 2000 MHz.

- (1) $V_{ctrl} = 0 V$
- (2) $V_{ctrl} = 1 V$
- (3) $V_{ctrl} = 1.5 V$
- (4) $V_{ctrl} = 2 V$
- $(5) V_{ctrl} = 7.5 V$
- (6) $V_{ctrl} = 10 \text{ V}$

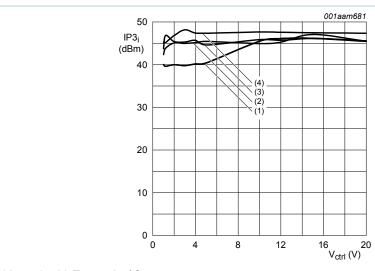


 $V_{CC} = 3.7 \text{ V}$; f = 3000 MHz.

- (1) $V_{ctrl} = 0 V$
- (2) $V_{ctrl} = 1 V$
- (3) $V_{ctrl} = 1.5 V$
- (4) $V_{ctrl} = 2 V$
- $(5) V_{ctrl} = 7.5 V$
- (6) $V_{ctrl} = 10 \text{ V}$

Figure 9. Attenuation as function of temperature (typical Figure 10. Attenuation as function of temperature values)

(typical values)

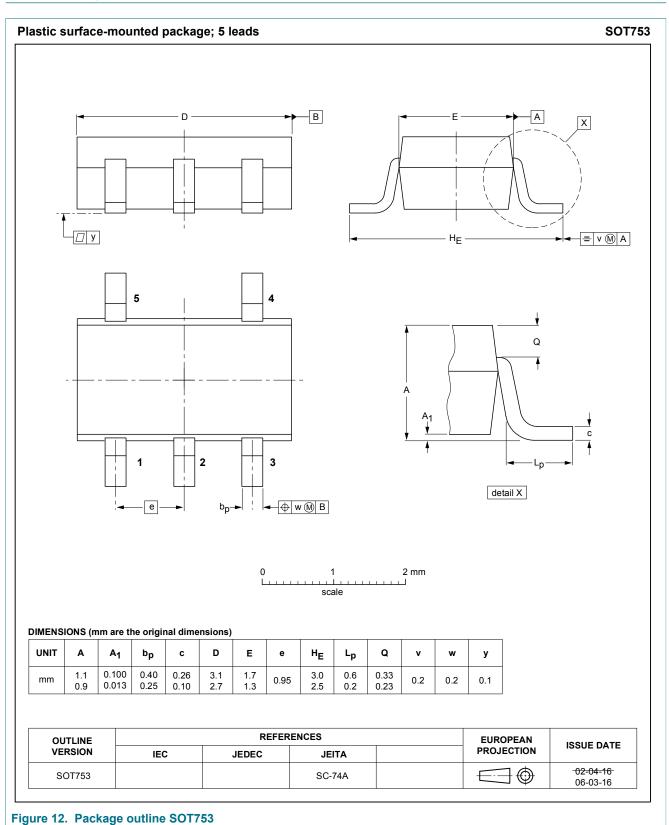


 V_{CC} = 3.7 V; T_{amb} = 25 °C.

- (1) f = 100 MHz
- (2) f = 900 MHz
- (3) f = 1800 MHz
- (4) f = 2100 MHz

Figure 11. Input third-order intercept point as control voltage (typical values)

10 Package outline



Quad PIN diode attenuator

11 Abbreviations

Table 9. Abbreviations

Acronym	Description
PIN	P-type, intrinsic, N-type
RF	radio frequency

Quad PIN diode attenuator

12 Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
BAP70Q v.3	20181211	Product data sheet	-	BAP70Q v.2	
Modifications:	 Section 1.2 "Features and benefits" has been updated. Changed to non automotive legal information The "Legal information" pages have been updated. 				
BAP70Q v.2	20120306	Product data sheet	-	BAP70Q v.1	
BAP70Q v.1	20101006	Product data sheet	-	-	

Quad PIN diode attenuator

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Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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Quad PIN diode attenuator

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