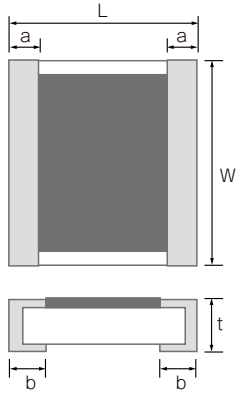


◆Dimensions



Type	Size (inch)	W	L	a	b	t
PRG2010	0804	2.00±0.20	1.00±0.20	0.20±0.10	0.25±0.10	0.35±0.10
PRG3216	1206	3.20+0.40/-0.20	1.60±0.20	0.30±0.20	0.35±0.20	0.45+0.15/-0.10
PRG5025	2010	5.00±0.20	2.50±0.20	0.55±0.20	0.60±0.20	0.45+0.15/-0.10
PRG6432	2512	6.40+0.20/-0.40	3.20±0.20	0.40±0.20	0.55±0.20	0.45+0.15/-0.10

(unit : mm)

Thin film surface mount resistors

◆Reliability specification

Test items	Condition (test methods (JIS C5201-1))	Standard	
		≤47Ω	≥47Ω
Life (biased)	70°C, rated voltage, ^{*1} 90min on 30min off, 1000hours	±(0.25%+0.05Ω)	±(0.1%+0.01Ω)
High temperature high humidity	85°C, 85%RH, 1/10 of rated power, 90min on 30min off, 1000hours	±(0.25%+0.05Ω)	±(0.1%+0.01Ω)
Temperature shock	-55°C (30min) ~ 125°C (30min) 1000cycles	±(0.25%+0.05Ω)	±(0.1%+0.01Ω)
High temperature exposure	155°C, no bias, 1000hours	±(0.25%+0.05Ω)	±(0.1%+0.01Ω)
Resistance to soldering heat	260±5°C, 10 seconds (reflow)	±(0.1%+0.01Ω)	±(0.05%+0.01Ω)

*1 Rated voltage is given by $E = \sqrt{R \times P}$
 E= rated voltage (V), R=nominal resistance value(Ω), P=rated power(W)
 If rated voltage exceeds maximum voltage /element, maximum voltage/element is the rated voltage.

PRG series

High power thin film chip resistors (long side terminal)

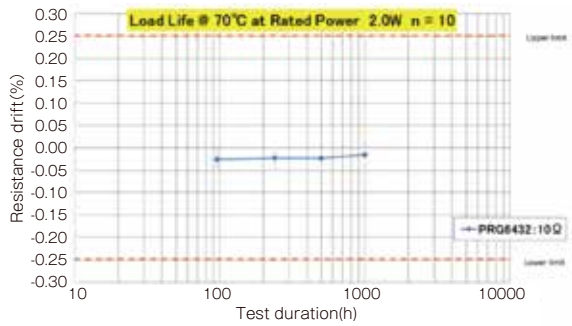
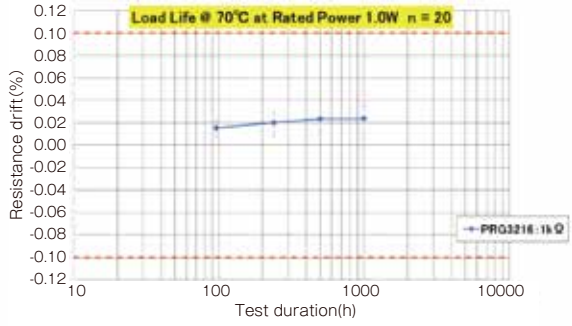
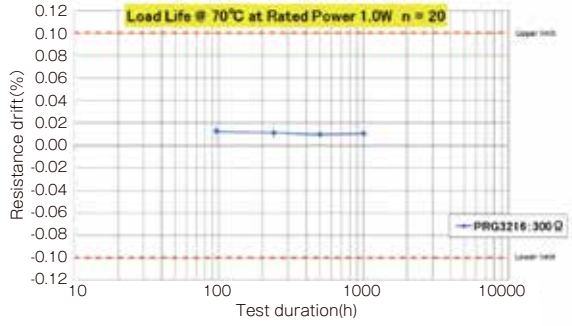
PRG series

Reliability test data

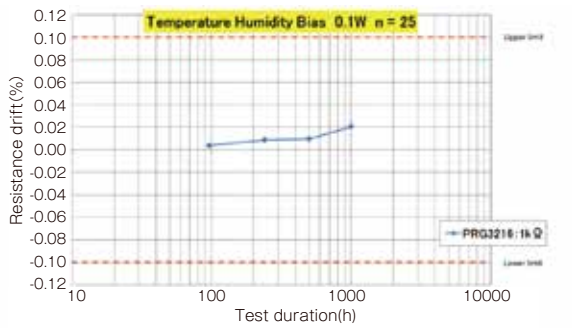
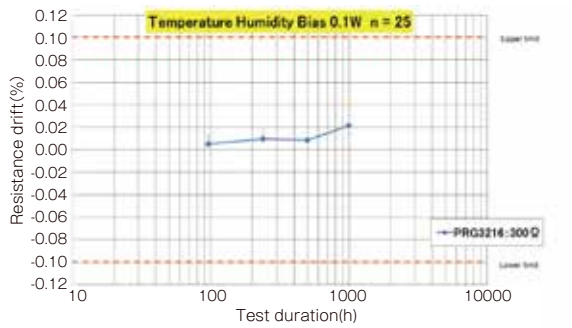
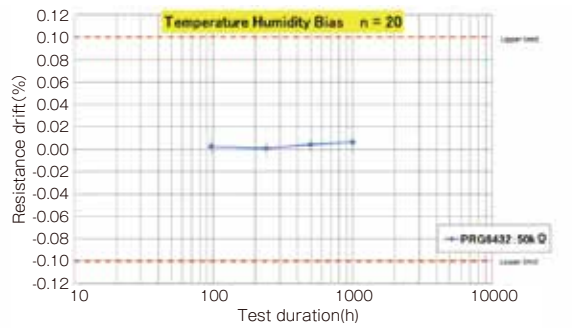
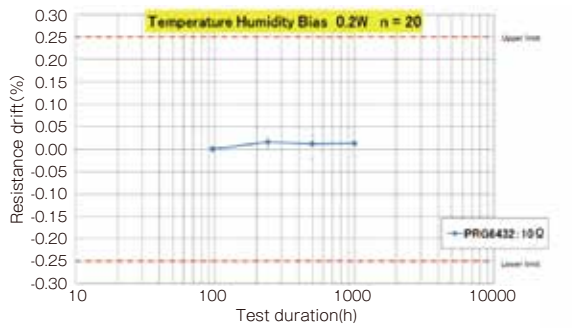
Biased life test

Thin film surface mount resistors

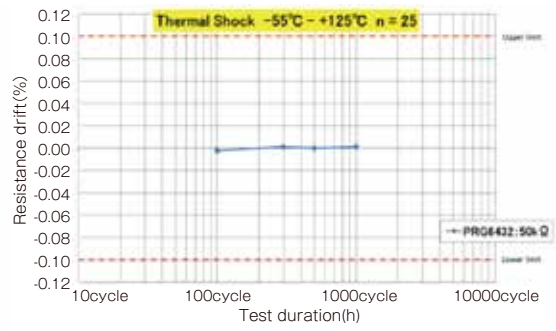
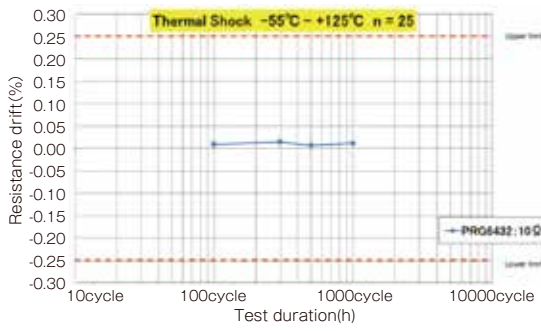
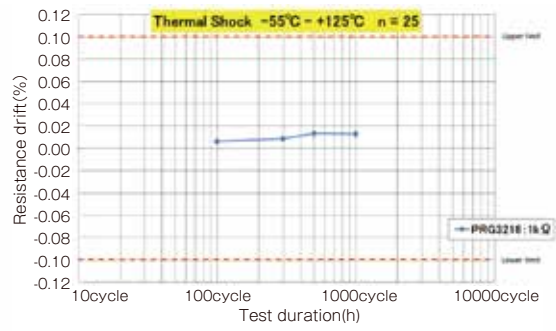
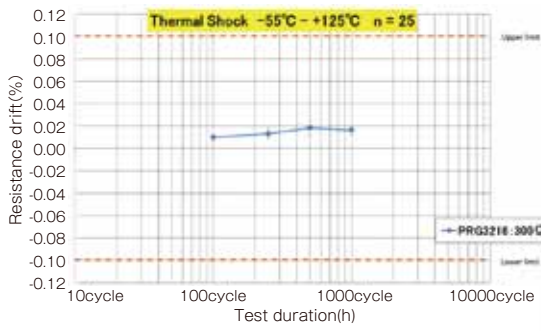
PRG series



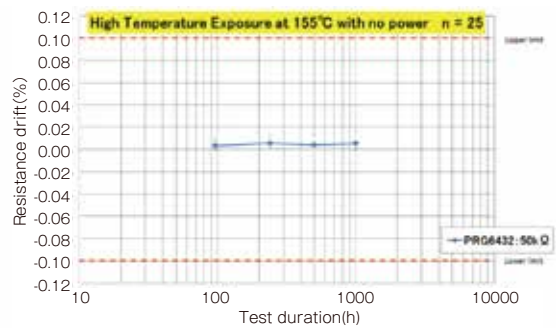
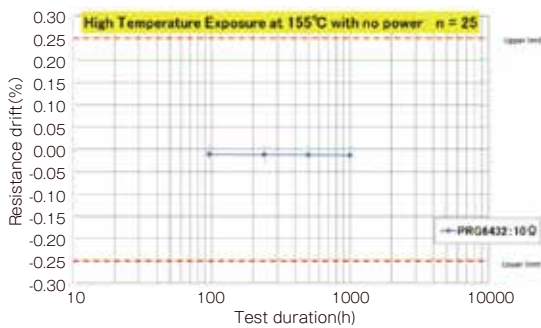
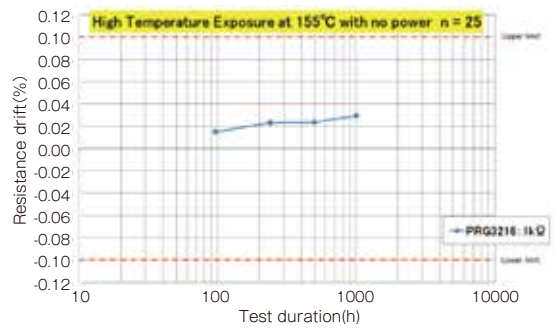
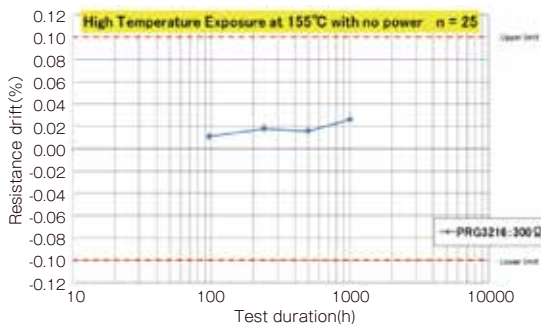
High temperature high humidity (biased)



○ Temperature shock

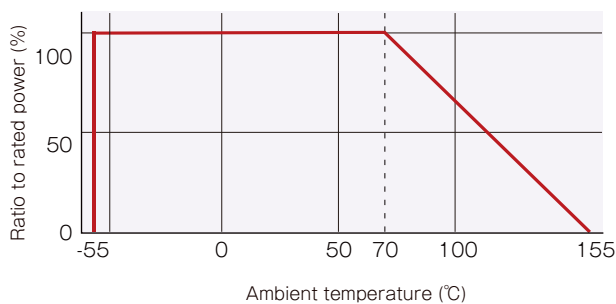


○ High temperature exposure

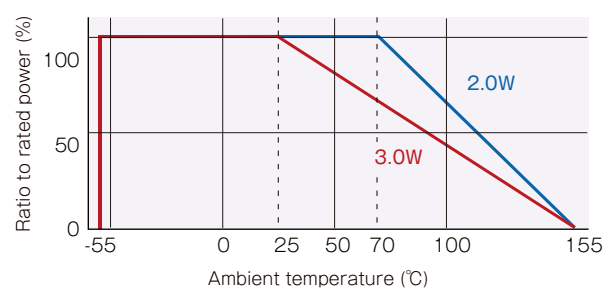


◆ Derating Curve

○ PRG3216



○ PRG6432



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