

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	16	A
Drain peak current	I _{D(pulse)} ^{Note1}	128	A
Body-drain diode reverse drain current	I _{DR}	16	A
Avalanche current	I _{AP} ^{Note 2}	16	A
Avalanche energy	E _{AR} ^{Note 2}	25.6	mJ
Channel dissipation	P _{ch} ^{Note3}	2.5	W
Channel to ambient thermal impedance	θ _{ch-a} ^{Note3}	50	°C/W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

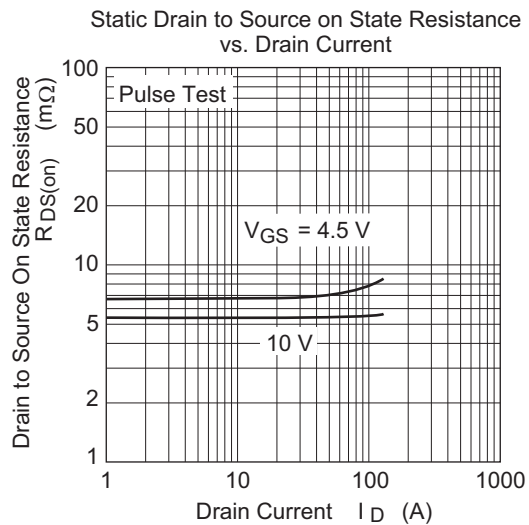
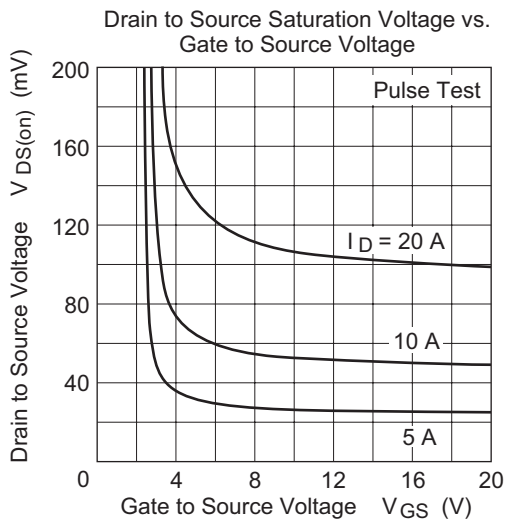
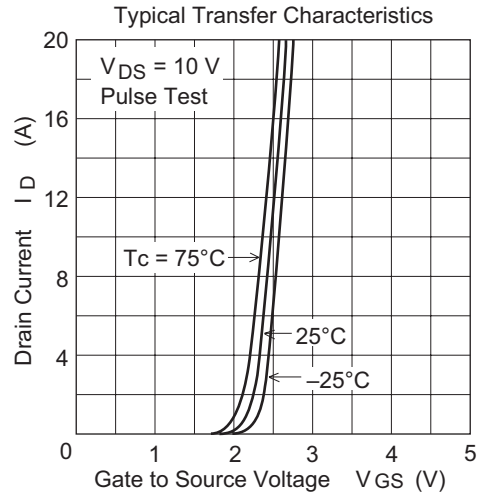
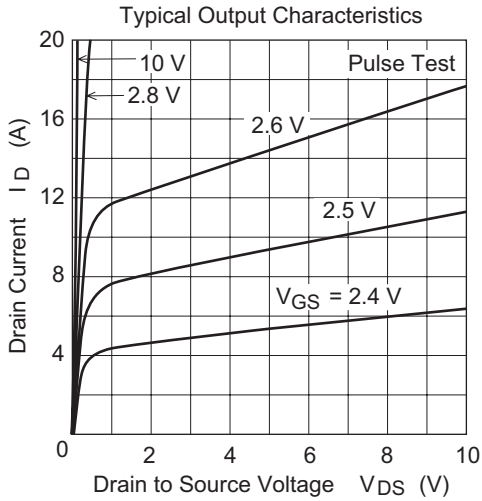
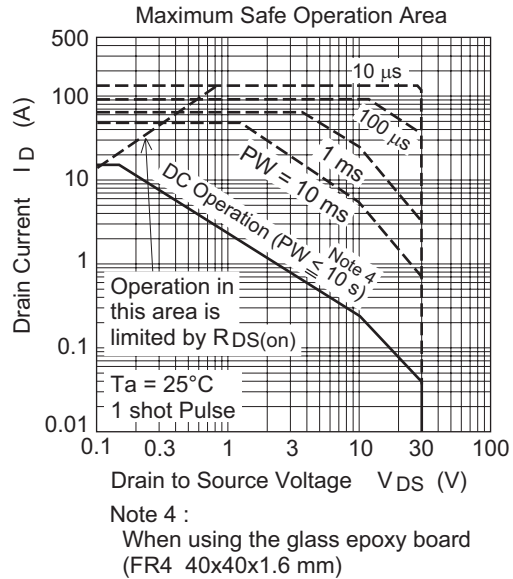
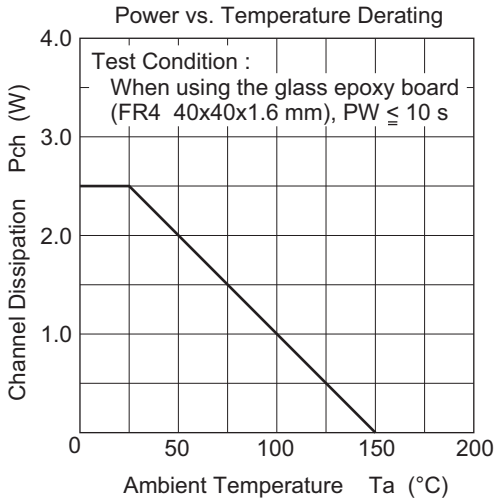
- Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
 2. Value at T_{ch} = 25°C, R_g ≥ 50 Ω
 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW ≤ 10s

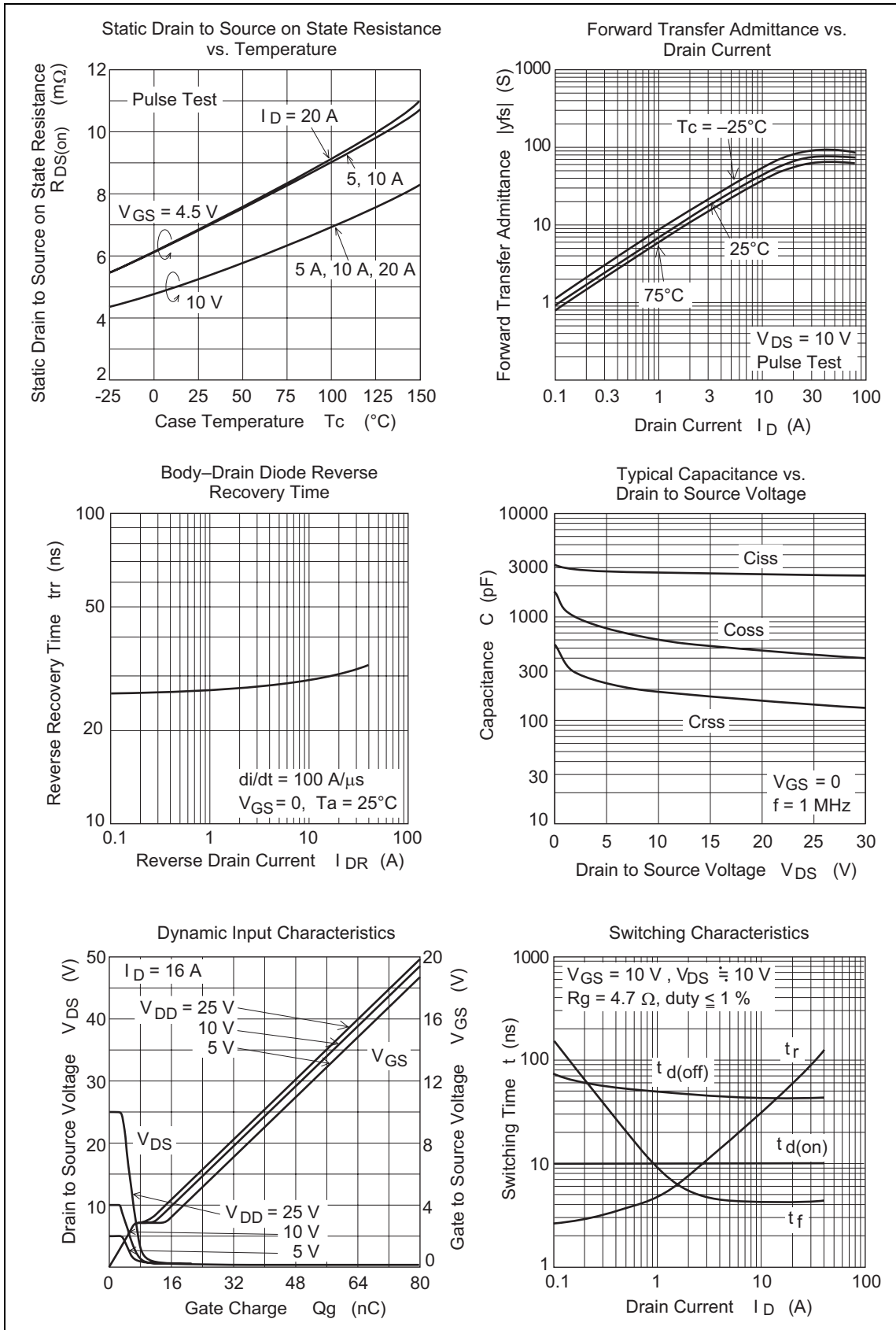
Electrical Characteristics

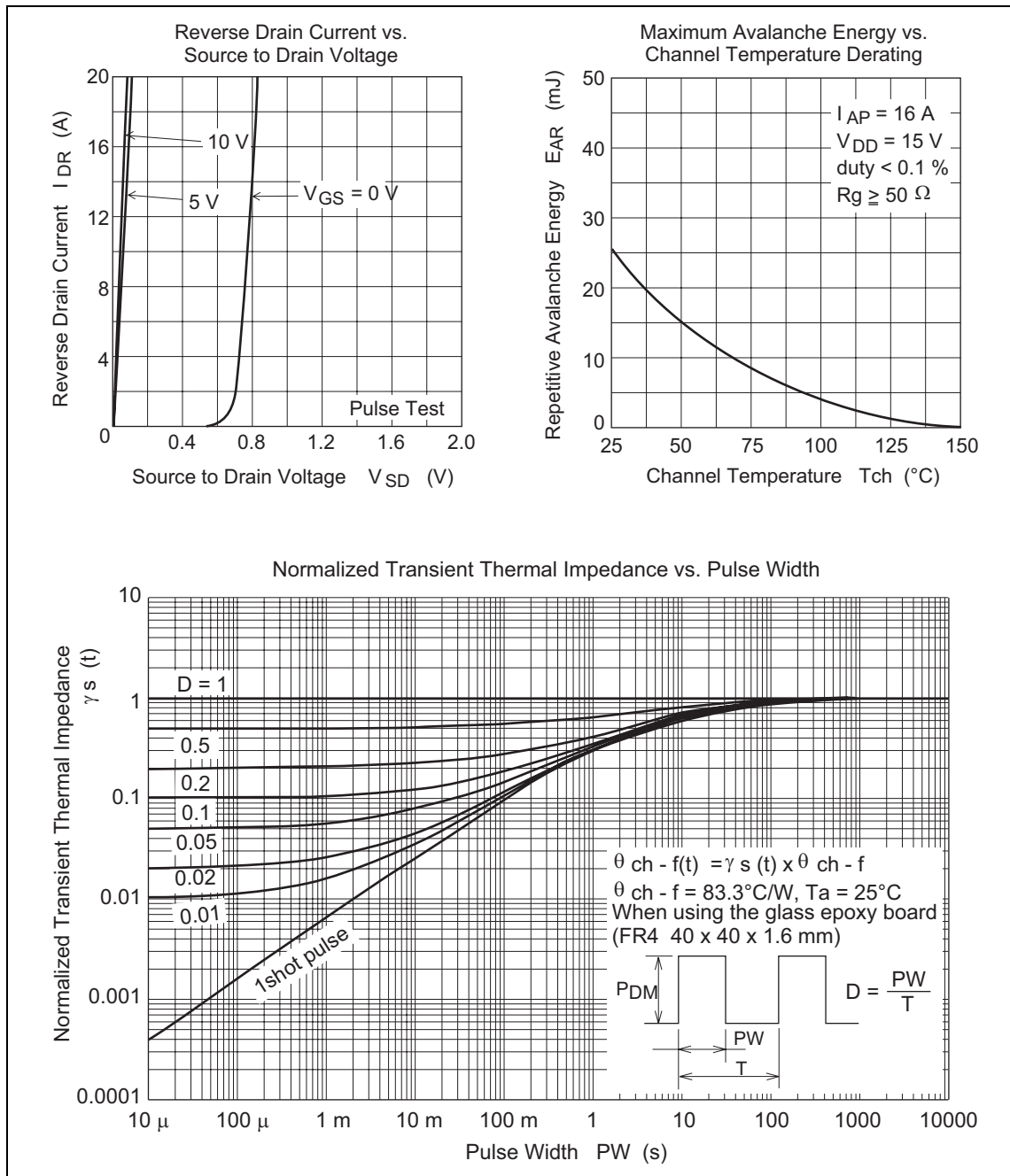
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	30	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source leak current	I _{GSS}	—	—	± 0.1	μA	V _{GS} = ±20 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	V _{DS} = 30 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.0	—	2.5	V	V _{DS} = 10 V, I _D = 1 mA
Static drain to source on state resistance	R _{DS(on)}	—	5.3	6.7	mΩ	I _D = 8 A, V _{GS} = 10 V ^{Note4}
	R _{DS(on)}	—	6.8	9.9	mΩ	I _D = 8 A, V _{GS} = 4.5 V ^{Note4}
Forward transfer admittance	y _{fs}	22	38	—	S	I _D = 8 A, V _{DS} = 10 V ^{Note4}
Input capacitance	C _{iss}	—	2650	—	pF	V _{DS} = 10 V
Output capacitance	C _{oss}	—	610	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	190	—	pF	f = 1 MHz
Gate Resistance	R _g	—	1.2	—	Ω	
Total gate charge	Q _g	—	18	—	nC	V _{DD} = 10 V
Gate to source charge	Q _{gs}	—	7.5	—	nC	V _{GS} = 4.5 V
Gate to drain charge	Q _{gd}	—	4.2	—	nC	I _D = 16 A
Turn-on delay time	t _{d(on)}	—	10	—	ns	V _{GS} = 10 V, I _D = 8 A
Rise time	t _r	—	25	—	ns	V _{DD} ≅ 10 V
Turn-off delay time	t _{d(off)}	—	45	—	ns	R _L = 1.25 Ω
Fall time	t _f	—	4.2	—	ns	R _g = 4.7 Ω
Body-drain diode forward voltage	V _{DF}	—	0.80	1.04	V	I _F = 16 A, V _{GS} = 0 ^{Note4}
Body-drain diode reverse recovery time	t _{rr}	—	30	—	ns	I _F = 16 A, V _{GS} = 0 diF/ dt = 100 A/μs

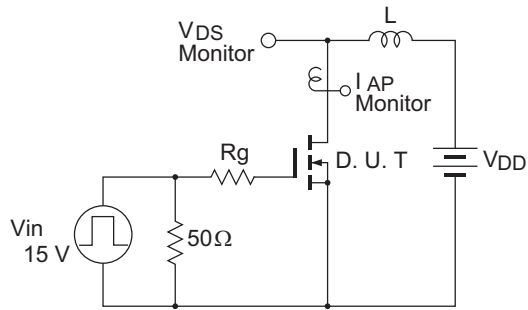
- Notes: 4. Pulse test





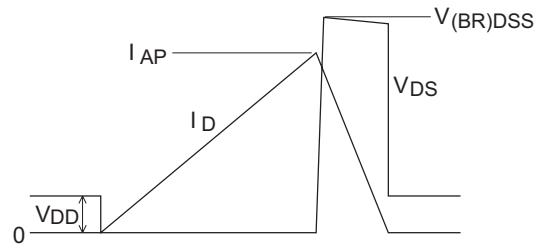


Avalanche Test Circuit

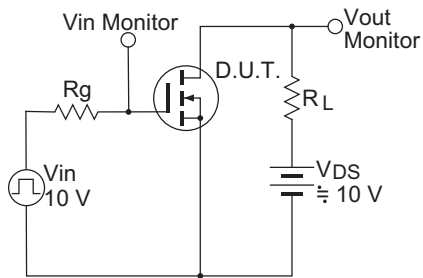


Avalanche Waveform

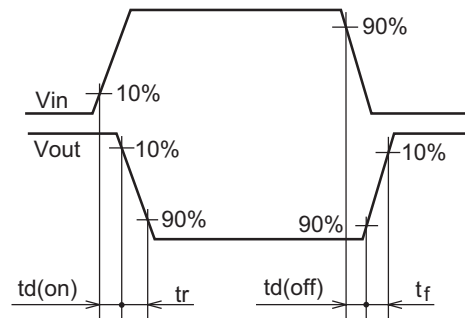
$$E_{AR} = \frac{1}{2} L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$



Switching Time Test Circuit

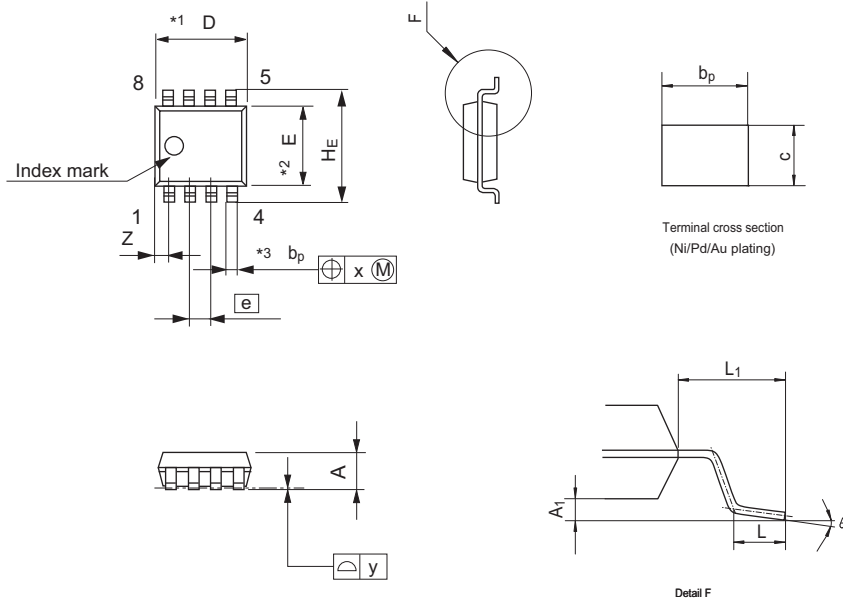


Switching Time Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
SOP-8	P-SOP8-3.95 × 4.9-1.27	PRSP0008DD-D	FP-8DAV	0.085g



NOTE)
 1. DIMENSIONS **1(Nom)** AND **2** DO NOT INCLUDE MOLD FLASH.
 2. DIMENSION **3* DOES NOT INCLUDE TRIM OFFSET.

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	—	4.90	5.3
E	—	3.95	—
A ₂	—	—	—
A ₁	0.10	0.14	0.25
A	—	—	1.75
b _p	0.34	0.40	0.46
b ₁	—	—	—
c	0.15	0.20	0.25
c ₁	—	—	—
θ	0°	—	8°
H _E	5.80	6.10	6.20
Ⓜ	—	1.27	—
x	—	—	0.25
y	—	—	0.1
Z	—	—	0.75
L	0.40	0.60	1.27
L ₁	—	1.08	—

Ordering Information

Orderable Part Number	Quantity	Shipping Container
HAT2197R-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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