## VBP104S, VBP104SR

Vishay Semiconductors



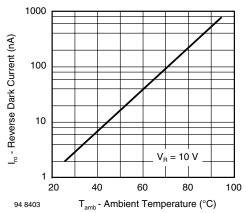
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1	1.3	V
Breakdown voltage	I <sub>R</sub> = 100 μA, E = 0	V <sub>(BR)</sub>	60			V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>		2	30	nA
Diode capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0$	CD		48		pF
	V <sub>R</sub> = 3 V, f = 1 MHz, E = 0	CD		17	40	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>Vo</sub>		- 2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	l <sub>k</sub>		32		μA
Temperature coefficient of Ik	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>Ik</sub>		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}, \\ V_R = 5 \text{ V}$	I <sub>ra</sub>	25	35		μΑ
Angle of half sensitivity		φ		± 65		deg
Wavelength of peak sensitivity		λρ		940		nm
Range of spectral bandwidth		λ <sub>0.1</sub>		430 to 1100		nm
Noise equivalent power	$V_{R} = 10 V, \lambda = 950 nm$	NEP		4 x 10 <sup>-14</sup>		W/√Hz
Rise time	$V_{R} = 10 \text{ V},  \text{R}_{L} = 1  \text{k}\Omega, \\ \lambda = 820 \text{ nm}$	t <sub>r</sub>		100		ns
Fall time	$V_R = 10 V, R_L = 1 k\Omega,$ $\lambda = 820 nm$	t <sub>f</sub>		100		ns

#### Note

 $T_{amb}$  = 25 °C, unless otherwise specified

### **BASIC CHARACTERISTICS**

 $T_{amb}$  = 25 °C, unless otherwise specified





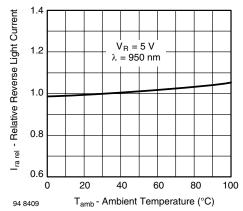


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature



## VBP104S, VBP104SR

## Silicon PIN Photodiode

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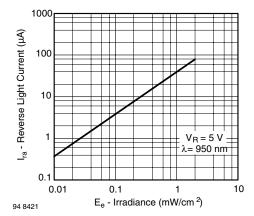


Fig. 3 - Reverse Light Current vs. Irradiance

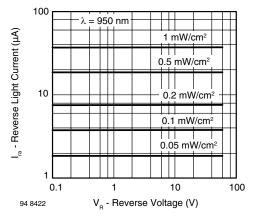


Fig. 4 - Reverse Light Current vs. Reverse Voltage

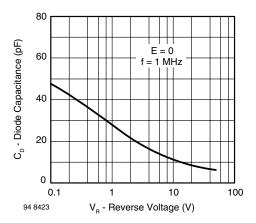


Fig. 5 - Diode Capacitance vs. Reverse Voltage

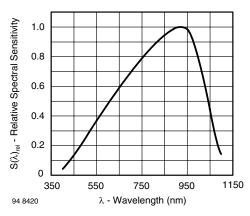


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

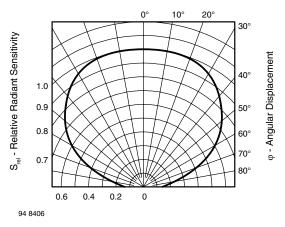


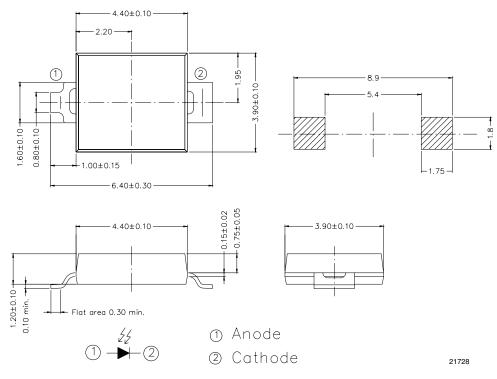
Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement

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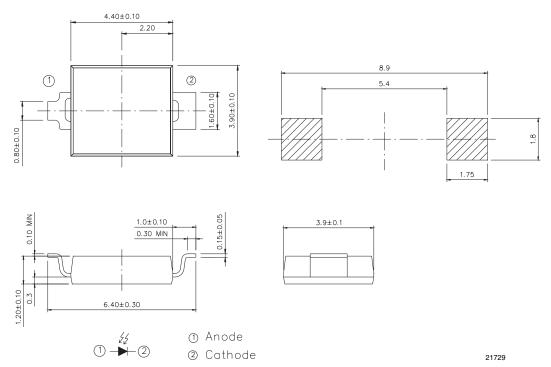
Silicon PIN Photodiode



## PACKAGE DIMENSIONS FOR VBP104S in millimeters



### PACKAGE DIMENSIONS FOR VBP104SR in millimeters



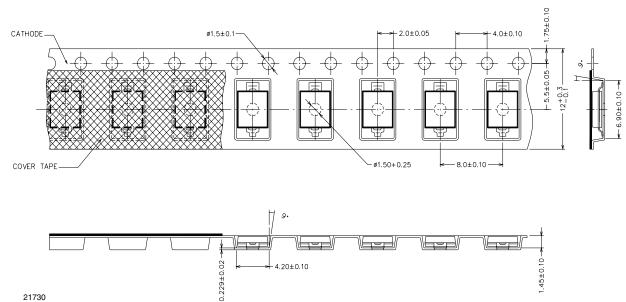


## VBP104S, VBP104SR

## Silicon PIN Photodiode

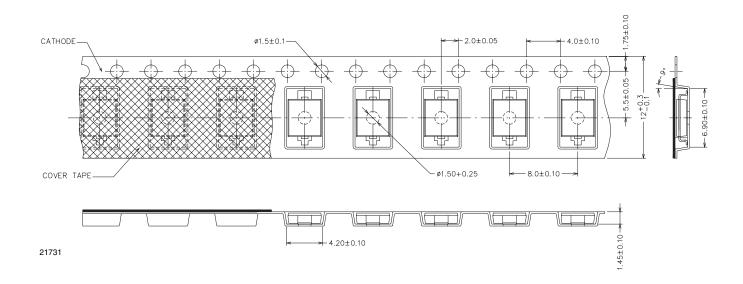
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## TAPING DIMENSIONS FOR VBP104S in millimeters



21730

### TAPING DIMENSIONS FOR VBP104SR in millimeters

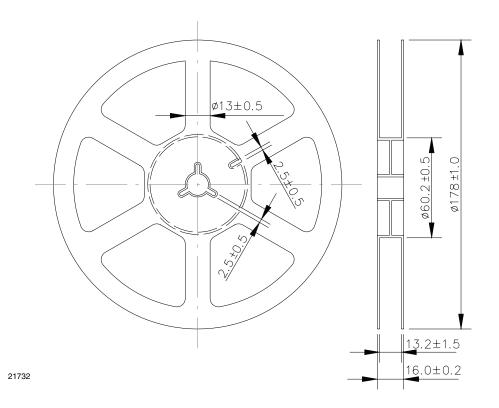


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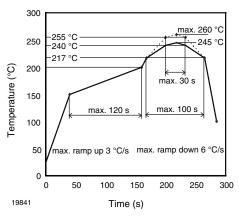
Silicon PIN Photodiode

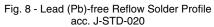


## REEL DIMENSIONS FOR VBP104S AND VBP104SR in millimeters



#### **SOLDER PROFILE**





### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020: Moisture sensitivity: level 3 Floor life: 168 h Conditions:  $T_{amb} < 30$  °C, RH < 60 %

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions: 192 h at 40 °C (+ 5 °C), RH < 5 % or 96 h at 60 °C (+ 5 °C), RH < 5 %.



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