

**POWER MANAGEMENT**
**Absolute Maximum Rating**

Exceeding the specifications below may result in permanent damage to the device or device malfunction. Operation outside of the parameters specified in the Electrical Characteristics section is not implied.

Parameter	Symbol	Maximum	Units
VCC, VCCIL, EN/NTC to GND		-0.3 to 7.0	V
VOUT, NOBAT, IPRGM, CHRGB, ITERM, RTIME to GND		-0.3 to +6.0	V
Power Dissipation MLP (Derate 20mW/ °C above 85°C)	$P_D$	2	W
Thermal Impedance, Junction to Case	$\theta_{JA}$	40 <sup>(1)</sup>	°C/W
Junction Temperature	$T_J$	150	°C
Operating Ambient Temperature Range	$T_A$	-40 to +85	°C
Peak IR Reflow Temperature, Soldering 20s-40s	$T_{PKG}$	260	°C
Storage Temperature Range	$T_{STG}$	-65 to 150	°C
$V_{OUT}$ Short to GND		Continuous	A
$I_{VCCIL} + I_{VOUT}$ Total Output Current	$I_{OUT\_TOT}$	1	A

Notes:

(1) Calculated from package in still air, mounted to 3" to 4.5", 4 layer FR4 PCB with thermal vias under the exposed pad per JESD51 standards.

**Electrical Characteristics**

Unless otherwise noted: VCC = 4.75V - 5.25V.

Parameter	Symbol	Condition	25°C			TA (-40°C to +85°C)		Units
			Min	Typ	Max	Min	Max	
Input Voltage				5		3.5	5.9	V
Operating Current	$ICC_{DIS}$	Shutdown Mode, EN = 0		0.1			1	µA
	$ICC_{CHG}$	Charging Mode CHRGB, ITERM, IPRG = 0µA		1.2			1.55	mA
Battery Leakage Current	$I_{leak\_BAT}$	VCC = 0V		0.1			1	µA
Regulated Voltage	VBAT	Vbat load = 75mA		4.20		4.16	4.24	V
Battery Pre-Charge Current	IPREQ	ITERM Resistor = 3.01k		50		44	54	mA
Battery Termination Current	ITERMQ			50		44.5	53.6	

**POWER MANAGEMENT**
**Electrical Characteristics (Cont.)**

Parameter	Symbol	Condition	25°C			TA (-40°C to +85°C)		Units
			Min	Typ	Max	Min	Max	
Battery Fast-Charge Current <sup>(1)</sup>	IFastQ <sub>1</sub>	IPRGM = 15k		100		91.5	111.5	mA
	IFastQ <sub>2</sub>	IPRGM = 1.87k VBATTERY = 3.8V Dropout Voltage = 500mV		787		-5	+5	%
Battery Fast-Charge Max Current Limit		2.8V < VBATTERY < VBAT					1.0	A
IProg Regulated Voltage	V <sub>IPRGM</sub>			1.5		1.42	1.58	V
ITerm Regulated Voltage	V <sub>ITERM</sub>			1.5		1.42	1.58	V
Vin OVP Rising Threshold	VT <sub>OVR</sub>			6.8		6.4	7.5	V
Vin OVP Falling Theshold	VT <sub>OVPF</sub>			6.4		5.9	7.15	V
Vin OVP Hysteresis	VT <sub>OVPH</sub>			375		150		mV
Vin UVLO Rising Threshold	VT <sub>UVLOR</sub>			2.8			3.5	V
Vin UVLO Falling Threshold	VT <sub>UVLOF</sub>			2.75		2.5	3.25	V
Vin UVLO Hysteresis	VT <sub>UVLOH</sub>			50		30		mV
Vbat Pre-Charge Threshold	VT <sub>PREQR</sub>			2.8		2.7	2.9	V
Vbat Recharge Threshold	VT <sub>REQ</sub>	VBAT - VBATTERY		100		60	140	mV
Pre-Charge Fault Time-Out	T <sub>PQFAULT</sub>	Rtime = 39.2k		38		30.7	44.7	min
Charge Complete Time-Out	T <sub>FASTQ</sub>	Rtime = 39.2k		2.5		2.0	3.0	hrs
Time Disable Threshold	VT <sub>TIMER</sub>	Rtime VIL Voltage		1.0		0.7		V
LDO-Mode Select Threshold	VT <sub>IINTTS</sub>	VCC-Rtime Voltage		1.1		0.7		V

**POWER MANAGEMENT**
**Electrical Characteristics (Cont.)**

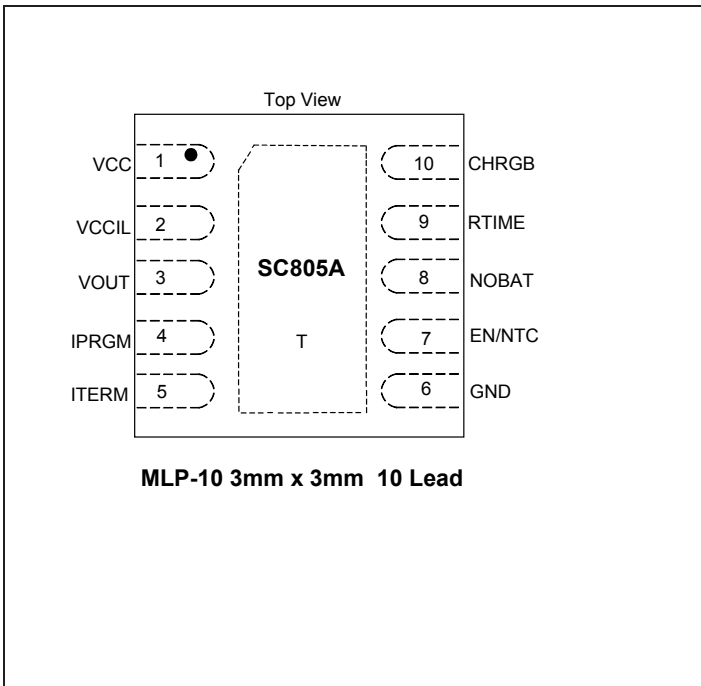
Parameter	Symbol	Condition	25°C			TA (-40°C to +85°C)		Units
			Min	Typ	Max	Min	Max	
En-NTC Thresholds	$V_{T_{DIS}}$	SC805A Disable		< 1.1		0.7		V
	$V_{T_{NTCH}}$	NTC Hot Vth Vcc = 5		$0.3V_{CC}$		$0.294V_{CC}$	$0.314V_{CC}$	V
	$V_{T_{NTCC}}$	NTC Cold Vth Vcc = 5		$0.75V_{CC}$		$0.736V_{CC}$	$0.756V_{CC}$	V
	$V_{T_{NOBAT}}$	No Battery Detect, Vcc = 5		$0.95V_{CC}$		$0.93V_{CC}$	$0.97V_{CC}$	V
	$NTC_{HYS}$	Hot/Cold Hysteresis		50				mV
Over-Temperature Shutdown	OT	Hysteresis = 10°C		150				°C
CHRGB LED Output	$VCB_{VOH}$	Charge Complete Isrc = 2mA		4.5		4.3	5.0	V
	$VCB_{VOL}$	Charging Isnk = 2mA		0.2		0.05	0.5	V
	$ICB_{OFF}$	Disable or LDO-mode Off-leakage		0.1			1.0	µA
NOBAT Output	$VNB_{VOH}$	EN = VCC, Isrc = 1mA		4.8		4.5		V
	$INB_{OFF}$	EN = VCC/2 Off-Leakage		0.1			1.0	µA
VCCIL Resistance	$VCCIL_R$	R VCC to VCCIL, I = 100mA		0.2				Ω
Reduced Fast-Charge Current	I <sub>fqRed</sub>	Vout = 3.8V, Rprog = 1.87k, VCCIL Load = 200mA		585		545	625	mA

Note:

(1) Charge current is reduced by VCCIL current. VCCIL current is not limited by the IC.

**POWER MANAGEMENT**

**Pin Configuration**



**Ordering Information**

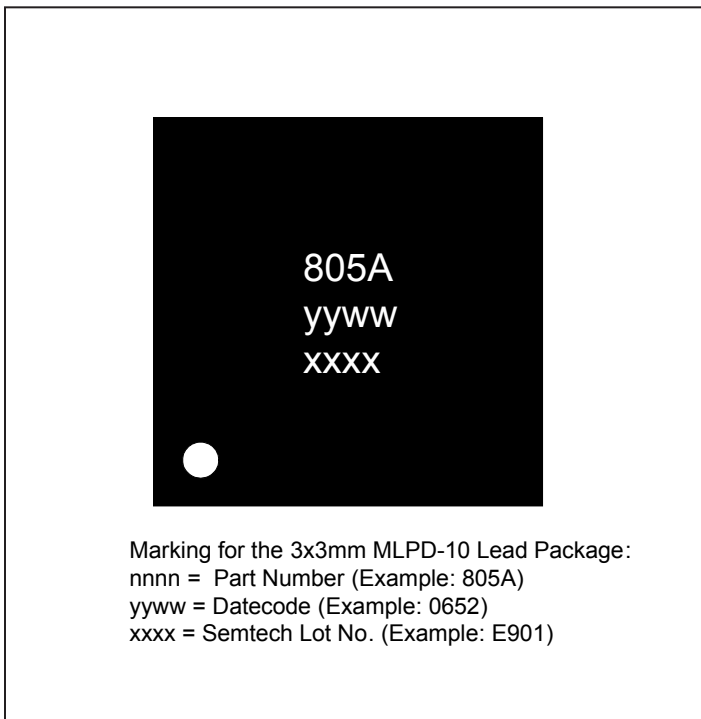
Device	Package <sup>(1)</sup>
SC805AMLTRT <sup>(2)</sup>	MLP-10
SC805AEVB	Evaluation Board

Notes:

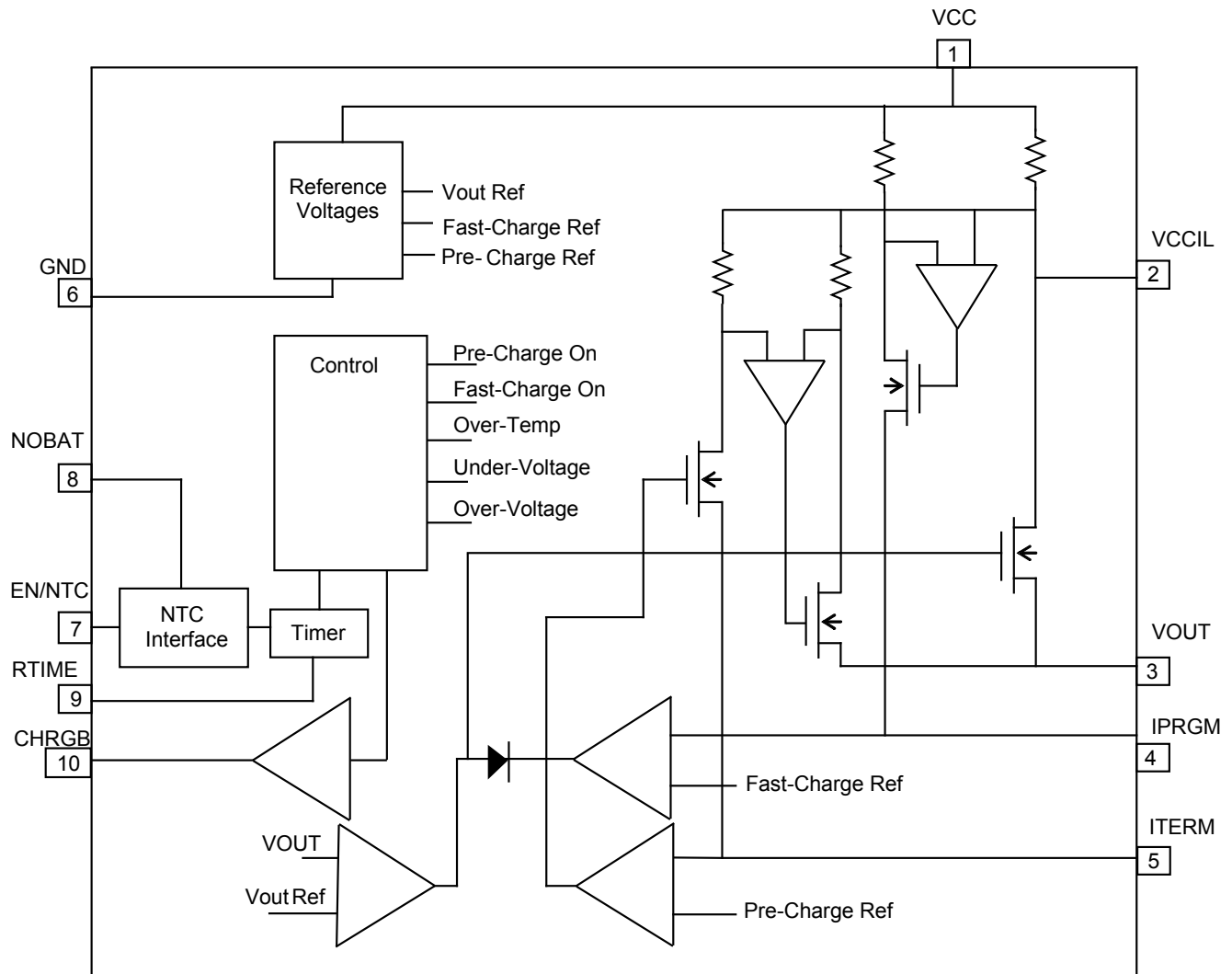
(1) Only available in tape and reel packaging. A reel contains 3000 devices.

(2) Lead-free product. This product is fully WEEE and RoHS compliant.

**Marking Information**



Block Diagram



**POWER MANAGEMENT**

**Pin Descriptions**

Pin	Pin Name	Pin Function
1	VCC	Supply pin – connect to adapter power
2	VCCIL	Adapter input voltage – this pin provides a bias voltage approximately equal to the adapter input voltage. When charging, the output current will be reduced by the load current on VCCIL.
3	VOOUT	Charger output – connect to battery
4	IPRGM	Current limit program pin for charge and LDO-mode – requires a resistor to ground to program fast-charge current.
5	ITERM	Selection for current termination and pre-charge current – requires a resistor to ground to program pre-charge and termination current.
6	GND	Ground
7	EN/NTC	Combined device enable/disable and NTC input pin – logic low disables the device. Analog voltages between 0.3VCC and 0.75VCC enable the NTC function. Voltages above 95% of VCC force the NOBAT output high.
8	NOBAT	Battery Present Indicator – this output goes high when the NTC voltage exceeds 95% of VCC indicating the battery and NTC have been removed.
9	RTIME	Charge time programming resistor – connect a resistor between RTIME and GND to program the charge time. Force the pin to GND to disable the timer. Force the pin to VCC to select LDO-mode.
10	CHRGB	Output status indicator designed to drive two LEDs. Connect cathode of the ‘charging’ LED to this pin to indicate charge in progress. Connect the anode of the ‘charge’ completed to this pin to indicate charge complete.
T	Thermal Pad	Pad is for heatsinking purposes – not connected internally. Connect exposed pad to ground plane with thermal vias directly under pad.

**Applications Information**
**General Operation**

The SC805A is configured based on the application for fast-charge and termination current, timing requirements, and operation with and without a battery (LDO mode). A typical charging cycle is described below. Details on alternate applications and output programmability are covered in the individual sections.

The charging cycle begins when the adapter is plugged in. The SC805A performs glitch filtering on the VCC input and initiates a charge cycle when  $VCC > UVLO$ . At this time, the CHRGB signal is active low turning on the charging LED. If the battery voltage is less than 2.8V, the SC805A will charge the output with the pre-charge current. When the battery voltage exceeds 2.8V, the SC805A enters fast-charge or constant current (CC) mode. When the battery voltage reaches its final value, the charger enters the constant voltage (CV) mode. In this mode the output current decreases as the battery continues to charge until the termination current level is reached.

The CHRGB pin goes high when VOUT output current is less than the programmed termination current, turning off the 'charging' LED and turning on the 'charge complete' LED. The SC805A continues to hold the battery in CV charge mode until the timer expires. At this point the charger enters the monitor mode where the output remains off until VOUT drops by 100mV and a new charge cycle is initiated.

**Pre-Charge Mode**

Pre-charge mode is automatically enabled whenever the battery voltage is below 2.8V. It is used to limit the power dissipation and precondition the battery for fast charging. The pre-charge current value is selected by the resistor on the ITERM pin. The pre-charge current is programmable from 10mA to 125mA. The equation to select the pre-charge current is given by:

$$PCI = \left( \frac{1.5}{R_{TERM}} \right) \cdot 100$$

If the charge timer is in use there is also a maximum allowed pre-charge duration. If the pre-charge time exceeds  $\frac{1}{4}$  of the total charge cycle, the charger will turn off due to a pre-charge fault. This fault is cleared when EN or VCC is toggled or the output voltage rises above 2.8V.

**Fast-Charge Mode (CC)**

The fast-charge or CC mode is active when the battery voltage is above 2.8V and less than the CV or final float charge voltage of the battery. The fast-charge current can be set to a maximum of 1.0A and is selected by the program resistor on the IPRGM pin. The voltage on this pin will represent the current through the battery enabling a microprocessor via an analog-to-digital converter (ADC) to monitor battery current by sensing the voltage on the IPRGM pin. The equation to set the fast-charge current is given by:

$$FCI = \left( \frac{1.5}{R_{PRGM}} \right) \cdot 1000$$

Note that for a given program resistor the current through the battery in CV mode can be determined by replacing 1.5 with the actual voltage on the IPRGM pin in the above equation.

The superior fast-charge current accuracy of the SC805A is obtained by use of US patent 6,836,095 polarity-switched (i.e., chopped) current sense amplifier to nullify current measurement offset errors.

**Termination Current**

Once the battery reaches the program voltage of 4.2V the device will transition from a constant current source to a constant voltage source and the current through the battery begins to decrease while the voltage remains constant. When the current falls below the programmed termination current set by the termination resistor on the ITERM pin, the SC805A will transition the CHRGB pin from low to high. If the timer is enabled the output will continue to float charge in CV mode until the timer expires. If the timer is disabled then the output will turn off as soon as the termination current level is reached. If the part is in LDO mode the SC805A will remain on indefinitely at the regulated CV voltage. The equation to set the termination current is given by:

$$ITERM = \left( \frac{1.5}{R_{TERM}} \right) \cdot 100$$

**Applications Information (Cont.)**
**Monitor Mode**

When a charge cycle is completed, the SC805A output turns off and the device enters monitor mode. If the voltage of the battery falls below the recharge threshold of CV-100mV the charger will clear the charge timer and re-initiate a charge cycle. The maximum current drain of the battery during monitor mode will be no more than 1µA over temperature. The status of the charger output as a function of RTIME, and VOUT current is shown below.

Output State	Rtime	Timer	VOUT current
ON	VCC	N/A	N/A
ON	R	T < Timer	N/A
OFF	R	T > Timer	N/A
ON	Low	Disabled	> termination
OFF	Low	Disabled	< termination

**Charge Timer**

The timer on the SC805A is used as protection in the case of a faulty battery and to maximize charging capacity. Connect a resistor from the RTIME pin to GND to select the desired charge time duration according to the following equation.

$$\text{CHARGE TIME} = \left( \frac{R_{\text{TIME}}}{39.2k} \right) \cdot 2.5\text{HRS}$$

The timer is programmable over the range of 1 to 10 hours. To disable the charge timer, connect RTIME to GND. Driving the RTIME pin to VCC will disable the timer and select LDO-mode operation for the SC805A. One implementation of this feature is discussed in the NOBAT operating section.

The SC805A will automatically turn off the output when the charge timer times out.

**NOBAT Output**

The SC805A detects the absence of the battery by monitoring the voltage on the NTC pin. When the NTC pin voltage exceeds 95% of VCC, the NOBAT output is driven high. This function utilizes the standard NTC configuration with the NTC component residing in the battery pack. When the battery is removed the NTC element in the resistor divider is missing and the NTC voltage is pulled up to VCC.

The NOBAT output can be used to signal the system that the battery has been removed, drive an external LED, or fed back to the SC805A to select LDO-mode.

Connect the NOBAT pin to the RTIME pin to select LDO mode when the battery is removed and re-enable the output of the SC805A.

**LDO Mode**

The SC805A is designed to work with or without a battery. If the battery is not in place the device can enter LDO mode. In this mode the SC805A will act like a low dropout regulator. The input pin RTIME is used to select LDO mode. If RTIME is high the device will be in LDO mode. When LDO mode is selected and the EN-NTC pin is greater than 90% of VCC the output will be on in LDO mode. During LDO mode the device will regulate the output voltage with a current limit set by the resistor tied to the IPRGM pin.

**EN-NTC Interface**

The enable pin on the SC805A provides three functions: device disable, battery detection, and NTC temperature protection. It can be used to turn off the device by driving the pin to GND. It can also be connected to an external resistor divider consisting of a resistor and an NTC Thermistor to disable the charge cycle when the temperature is out of range. If the pin is pulled to VCC the SC805A will signal a 'No Battery' condition on the NOBAT pin.

Note: the SC805A will be disabled due to the NTC function when the 'No Battery' condition is detected due to the NTC feature. The NTC temperature fault can be overridden to re-enable the output of the charger for no battery conditions as described in the NOBAT section of the datasheet.

The NTC external component configuration is shown in the typical application schematic on Page 1 of the datasheet. When the NTC voltage from the divider is above or below the cold and hot temperature threshold values, the SC805A will suspend the charge cycle by turning off the output, freezing the charge timer, and indicating a fault on the CHRGB LED. Hysteresis is included for both hot and cold thresholds to avoid chatter at the NTC trip points. When the temperature returns to the valid range, the SC805A will automatically resume the charge cycle. The charge timer will time-out when the SC805A output on-time exceeds the timer setting regardless of how long it has been disabled.



**Applications Information (Cont.)**

due to the NTC temperature. The internal NTC thresholds of 30% and 75% VCC were designed to work with Curve-2 type thermistors available from numerous vendors.

Design example based on the typical application schematic of Page 1 using a Curve-2, Vishay-Dale NTC.

Thot = 50°C  
Tcold = 0°C  
Find R2 and RT1

Step 1: Pick an RT1 value high enough to prevent self-heating.

$$RT1 \geq \left( \frac{V(RT1)}{DC} \right)^2$$

In general, lower values of RT1 provide more noise immunity for the NTC voltage at the expense of bias current from the input adapter. The dissipation constant DC is the power rating of the NTC resulting in a 1°C self-heating error.

The DC value for this NTC is 3mW and the voltage across the NTC is 2.5V for a 5V adapter. Therefore, RT1 > 2K,

Set RT1 = 10k

Step 2: Find R2 to set the correct hot level trip point,

$$R2 = 2.33 \cdot \left( R_{T25} \cdot \left( \frac{R_{T50}}{R_{T25}} \right) \right) = 9.52k$$

The closest standard resistor value is 9.53k. RT(temp)/RT(25°C) is often referred to as the ratio at the given temp. For this NTC at 50°C, the ratio is 0.4086.

Step 3: Calculate the Tcold value.

$$\left( \frac{RT_{COLD}}{RT_{25}} \right) = 3 \cdot \left( \frac{R2}{10K} \right) = 2.86$$

The ratio for the Curve-2 NTC at 0°C is 2.816. The difference represents less than 1/2°C (i.e., actual trip would be -0.5°C instead of 0°C.) For temperature trip values other than 0°C and 50°C, alternate curve-type NTC elements can be used or an additional resistor can be added to the Rntc divider to alter the temperature coefficient.

**CHRGB LED Flag**

The CHRGB LED driver on the SC805A can be used to drive two external LEDs: CHRGB=Low (charging) and CHRGB=High (charge complete). The CHRGB output will be high impedance when the VCC is in a UV condition. The table below defines the CHRGB LED output states.

LED	High	Low	High-Z	Blinking
CHRGB	Iout < Iterm	Iout > term	VCC < UV	Pre-charge OV or NTC Fault

The CHRGB output can be used for a VCC indicator. When the SC805A is enabled, the CHRGB output is either high or low providing the VCC input is above UVLO.

The CHRGB output signifies the charging status. When the output current is greater than termination current, CHRGB=low. CHRGB is high when Iout is < Itermination. The CHRGB output is latched high at the end of the charge cycle when the output current is less than Itermination. The CHRGB output will remain high until the timer expires and Vbatt enters a re-charge cycle, or if EN, or VCC are toggled. The CHRGB indicator operates the same way in both charge and LDO mode.

The CHRGB output can be used for a VCC indicator. When the SC805A is enabled the CHRGB output is either high or low providing the VCC input is above UVLO. Alternately an LED connected to VCCIL can be used for a VCC indicator as shown in the typical application schematic on Page 1.

There are five fault modes detected by the SC805A; (input under-voltage, input over-voltage, NTC temperature, max die temperature (or OT) and pre-charge timeout). The LED states for these fault modes are shown below.

Fault	CHRGB
VCC UVLO	High-Z
VCC OVP	Blinking
NTC Fault	Blinking
Pre-Charge Time-Out	Blinking
T <sub>j</sub> > 150°C	Blinking

## Applications Information (Cont.)

**Over-Current and Max Temperature Protection**

Over-current protection is inherent in all modes of operation. When the device is in charge-mode the output is current-limited to either the pre-charge current limit value or the fast-charge current limit value depending on the voltage at the output. When the device is in LDO mode, the output current is limited to the fast-charge current limit. Max die temperature protection is included on the SC805A. This feature allows the SC805A to operate with maximum power dissipation by disabling the output current when the die temperature reaches OT. The result is that the SC805A will operate as a pulse charger in extreme power dissipation applications delivering the maximum allowable output current while regulating the internal die temperature to a safe level.

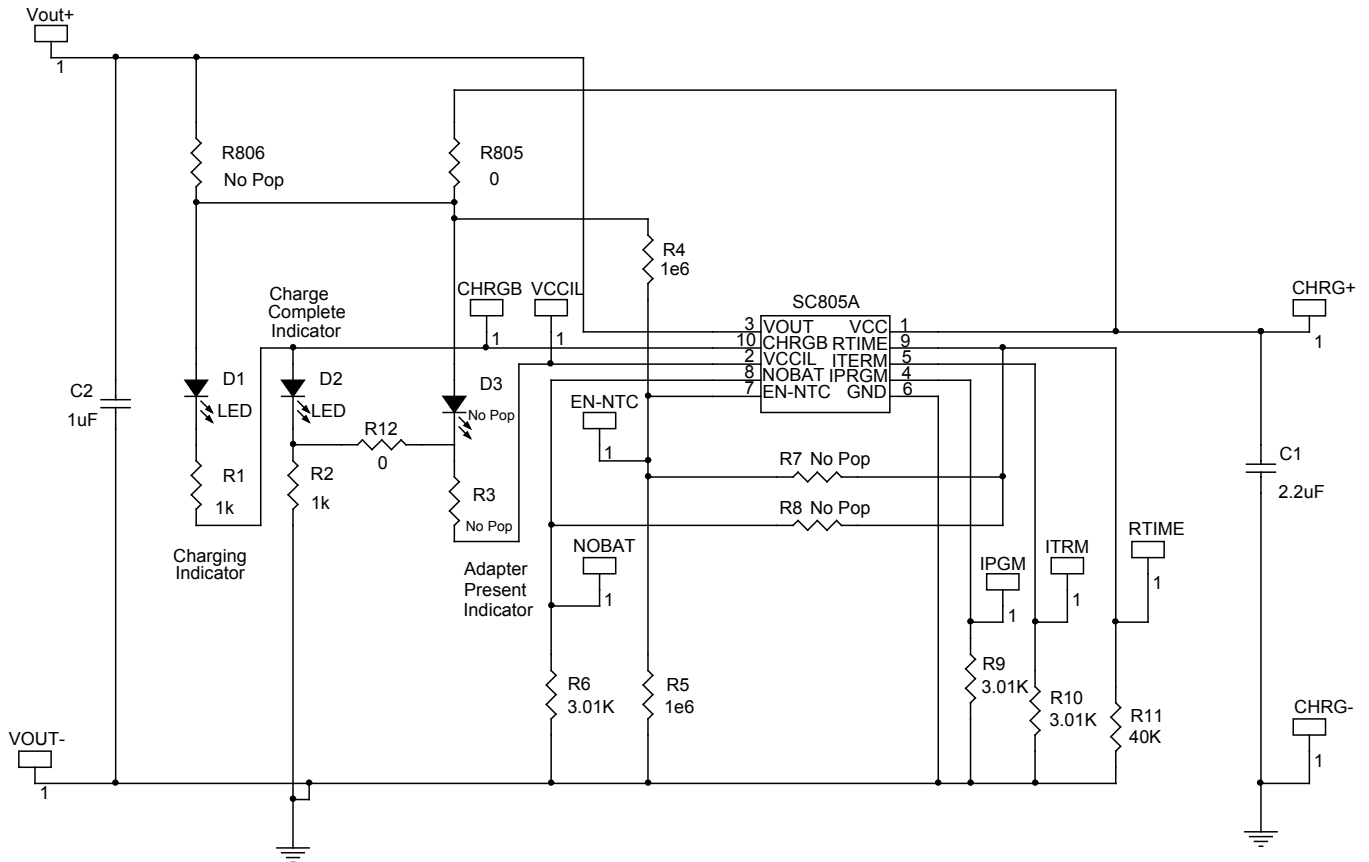
**Layout Guidelines**

The two most critical aspects of the PCB layout are the power path and thermal layout. The power path starts at the Adapter + input and runs to the VCC input of the SC805A, then from the VOUT pin of the SC805A to the Battery + terminal, and completes with the return trace from the battery - terminal to the adapter - input. All of these traces need to be designed to handle the required charging current. The trace from VOUT of the SC805A is most critical and should be made as wide as possible to minimize  $I \times R$  drops between the regulated voltage at the SC805A VOUT

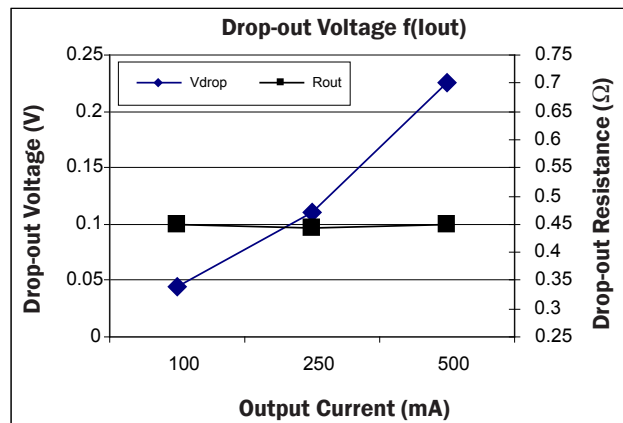
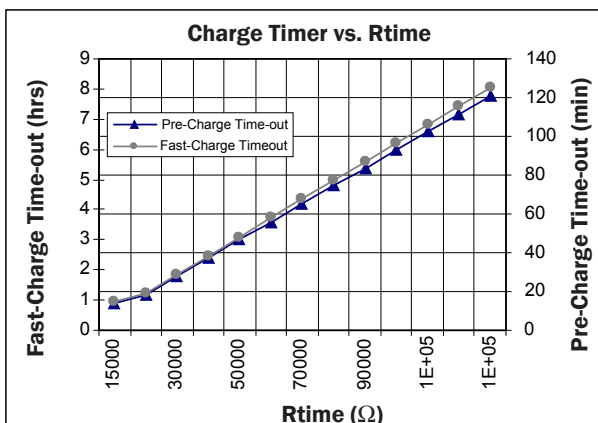
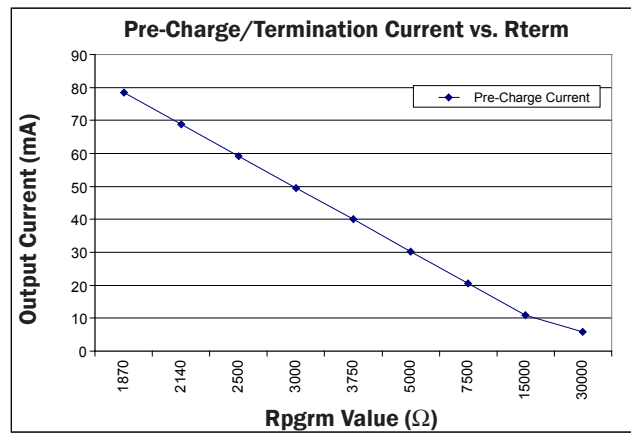
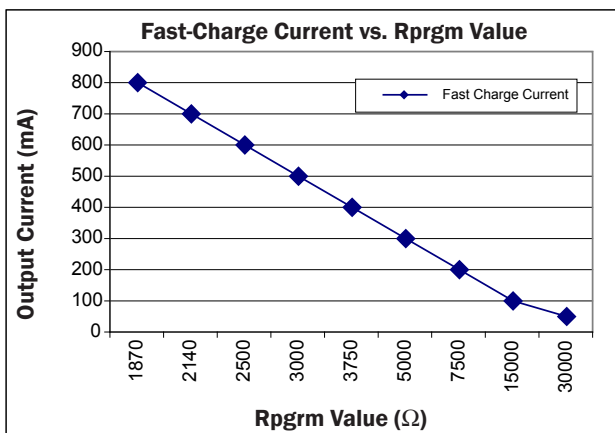
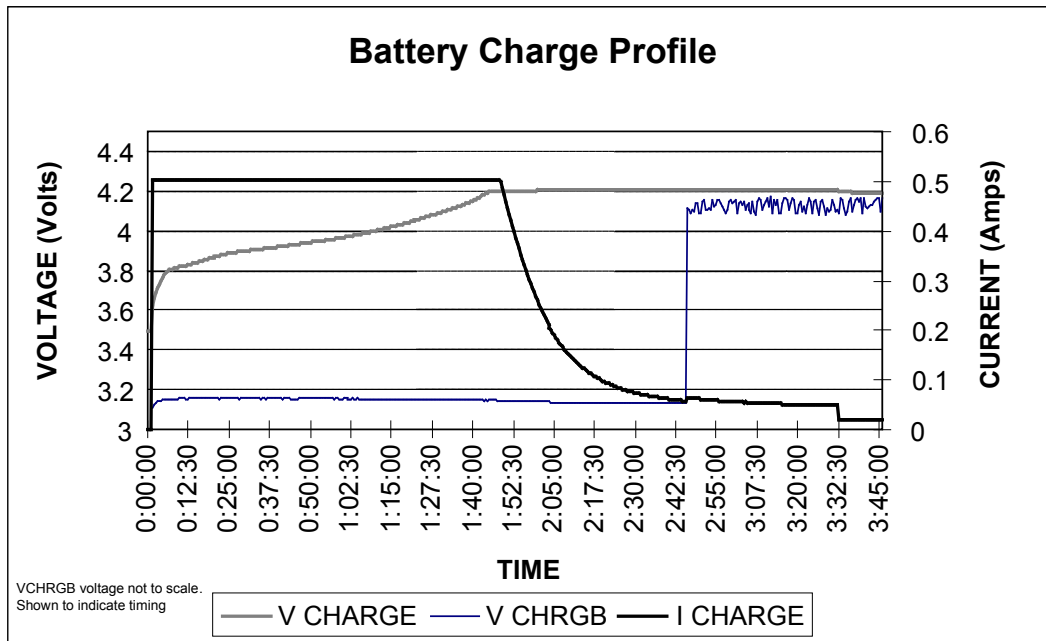
pin and the battery terminal. Keep the Adapter+ to SC805A VCC input trace wide to minimize voltage drops that will add to the dropout voltage of the SC805A. The GND pin of the SC805A should be connected in a Kelvin fashion at the battery-terminal to eliminate voltage drops in the return path which reduce the regulated battery voltage.

The thermal performance of the SC805A package requires a low impedance connection from the heat slug on the bottom of the package to an external ground plane. This is best accomplished by using a single large via under the device connected to a ground plane on the bottom exposed side of the PCB. The evaluation board uses 1 square inch of copper on the bottom of the PCB and is capable of 1A charging current.

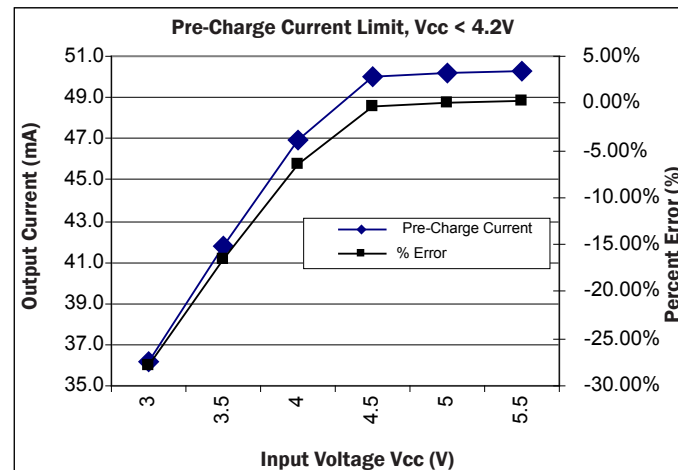
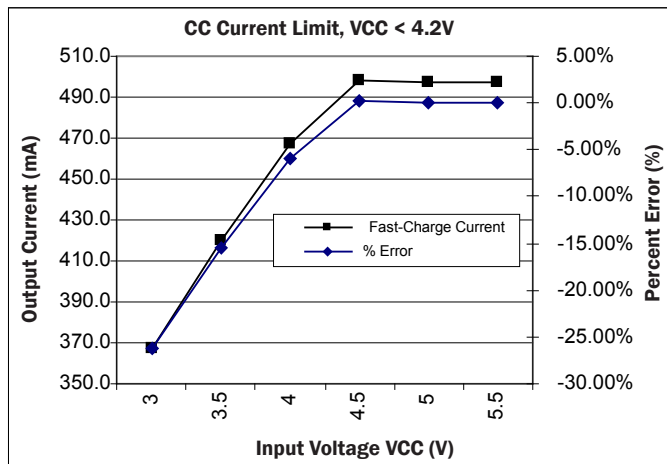
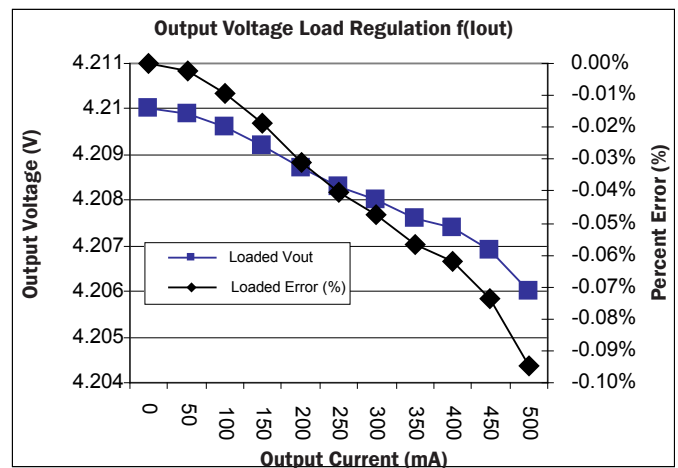
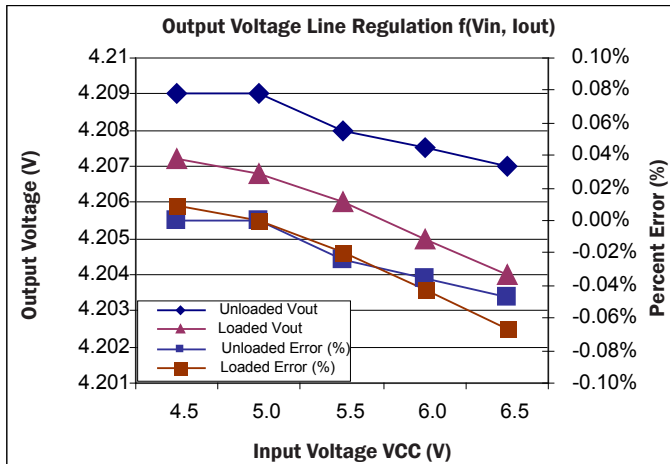
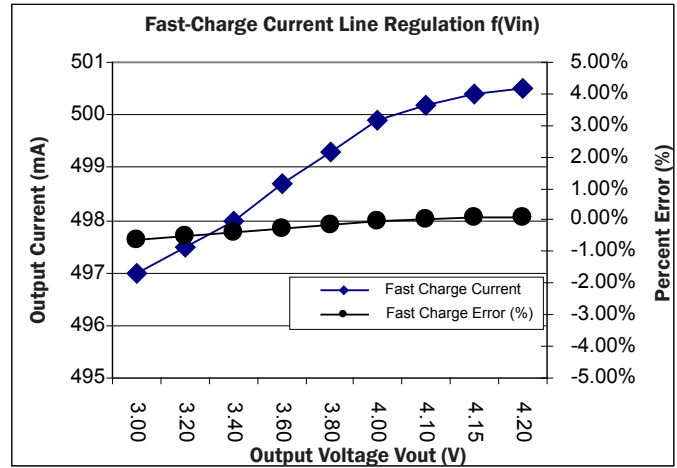
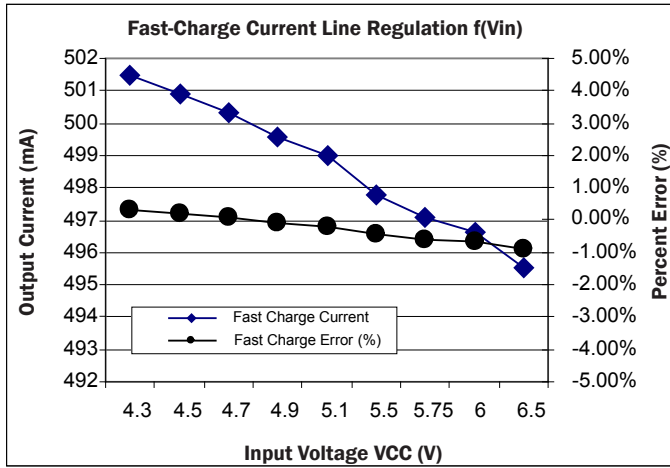
The input and output bulk decoupling capacitors for the SC805A should be placed near the external terminals for the adapter and battery. This short low impedance loop is for the high current spikes that result from input/output hot-plugging of the charger. To minimize these current spikes the value of the decoupling capacitor should be minimized. A typical application requires a 0.1 $\mu$ F input/output capacitor. If the distance from the external terminals to the SC805A is greater than 1 inch, 0.1 $\mu$ F local decoupling capacitors at the SC805A may be required.



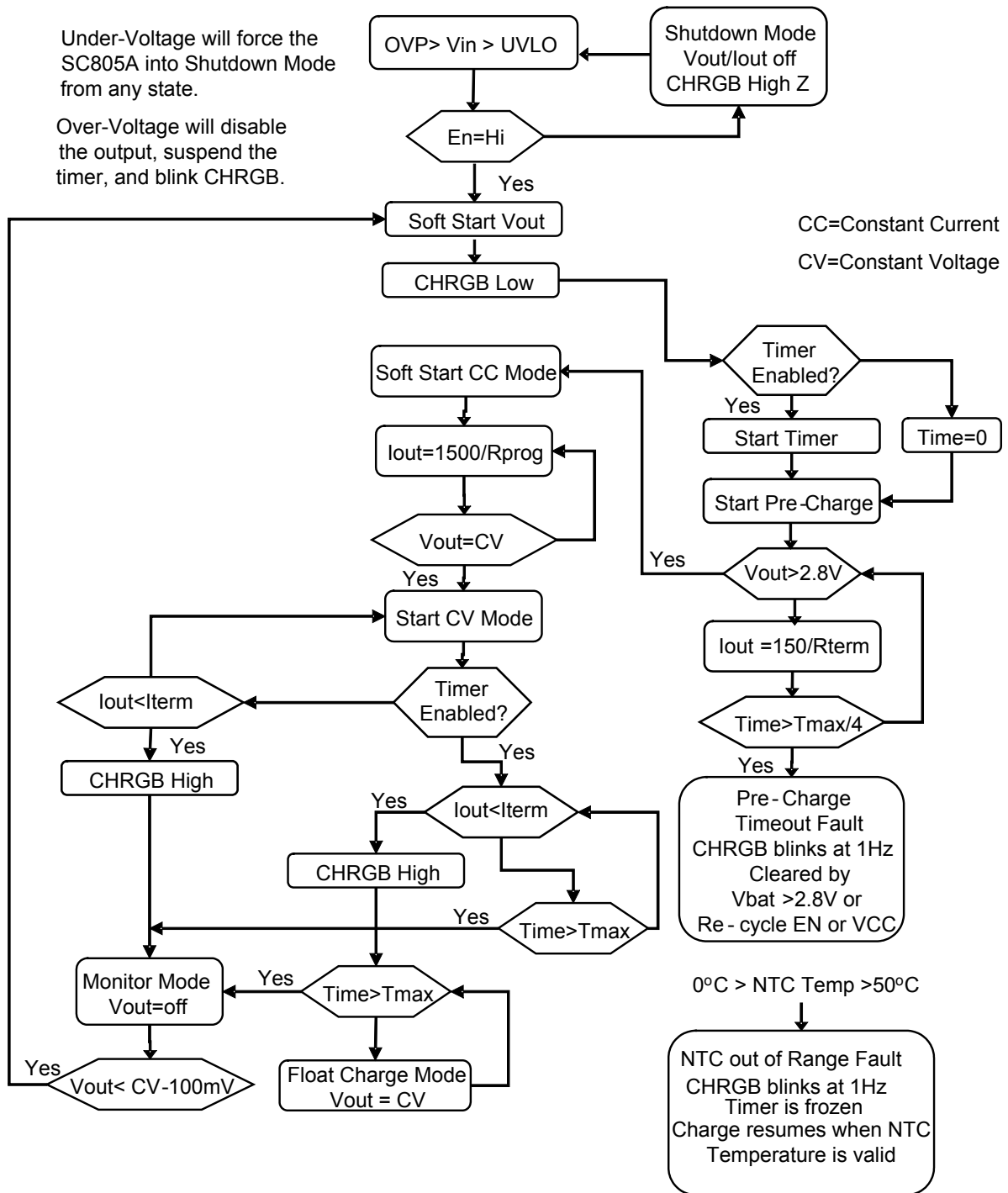
Typical Characteristics



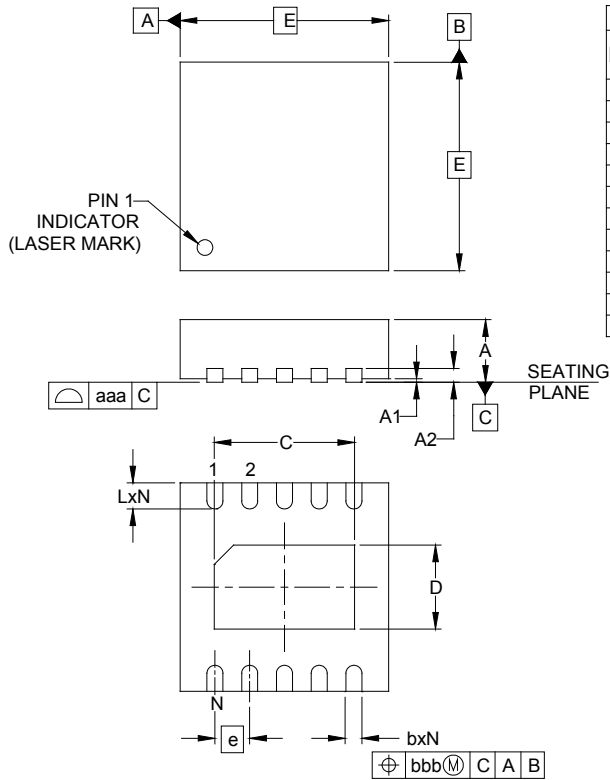
Typical Characteristics (Cont.)



State Diagram



Outline Drawing - MLPD-3x3-10



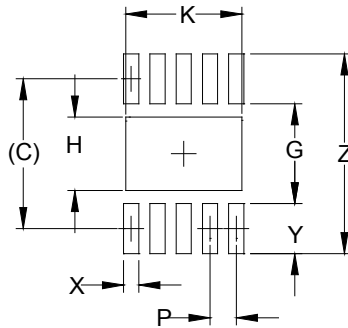
DIM	INCHES			MILLIMETERS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	.031	-	.039	0.80	-	1.00
A1	.000	-	.002	0.00	-	0.05
A2	-	(.008)	-	-	(0.20)	-
b	.007	.009	.011	0.18	0.23	0.30
C	.074	.079	.083	1.87	2.02	2.12
D	.042	.048	.052	1.06	1.21	1.31
E	.114	.118	.122	2.90	3.00	3.10
e	.020 BSC			0.50 BSC		
L	.012	.016	.020	0.30	0.40	0.50
N	10			10		
aaa	.003			0.08		
bbb	.004			0.10		

NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS TERMINALS.

POWER MANAGEMENT

Land Pattern - MLPD-3x3-10



DIMENSIONS		
DIM	INCHES	MILLIMETERS
C	(.112)	(2.85)
G	.075	1.90
H	.055	1.40
K	.087	2.20
P	.020	0.50
X	.012	0.30
Y	.037	0.95
Z	.150	3.80

NOTES:

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Contact Information

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