Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics			,		
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \underline{\text{Fig. 7}}$	2	-	35	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	-	35	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 18 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.3	1.5	V
Dynamic	characteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 536 V; $T_{j}$ = 125 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	500	-	-	V/µs
dI <sub>com</sub> /dt	rate of change of commutating current	$V_D$ = 400 V; $T_j$ = 125 °C; $I_{T(RMS)}$ = 16 A; $dV_{com}/dt$ = 20 V/ $\mu$ s; (snubberless condition); gate open circuit	15	-	-	A/ms

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		N 1
2	T2	main terminal 2		T2—T1
3	G	gate		sym051
mb	T2	mounting base; main terminal 2		

## 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
BTA316B-800C	TO263	BTA316B-800CJ	Reel	800	TO263E	26-May-2017

## 7. Marking

### **Table 4. Marking codes**

Type number	Marking codes
BTA316B-800C	BTA316B-800C

## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Values	Unit
$V_{DRM}$	repetitive peak off-state voltage		800	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 101 °C Fig 1; Fig 2; Fig 3	16	А
I <sub>TSM</sub>	non-repetitive peak on- state current	full sine wave; $T_{j(init)}$ = 25 °C; $t_p$ = 20 ms Fig 4; Fig 5	140	А
		full sine wave; $T_{j(init)} = 25 \text{ °C}$ ; $t_p = 16.7 \text{ ms}$	150	Α
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t <sub>P</sub> = 10 ms; SIN	98	A <sup>2</sup> s
dl <sub>⊤</sub> /dt	rate of rise of on-state current	I <sub>G</sub> = 70 mA	100	A/µs
I <sub>GM</sub>	peak gate current		2	А
$P_{GM}$	peak gate power		5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	0.5	W
T <sub>stg</sub>	storage temperature		-40 to 150	°C
T <sub>j</sub>	junction temperature		125	°C

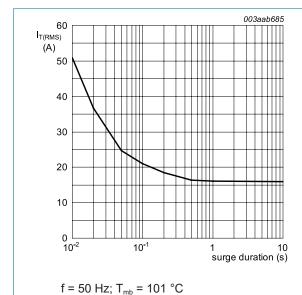


Fig. 1. RMS on-state current as a function of surge duration; maximum values

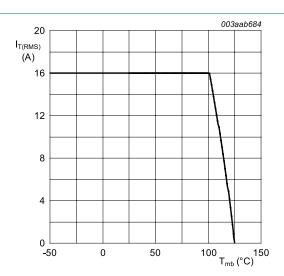
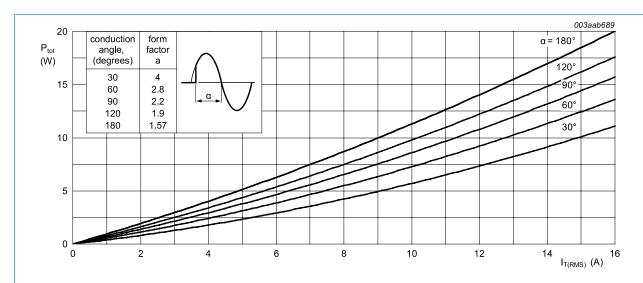


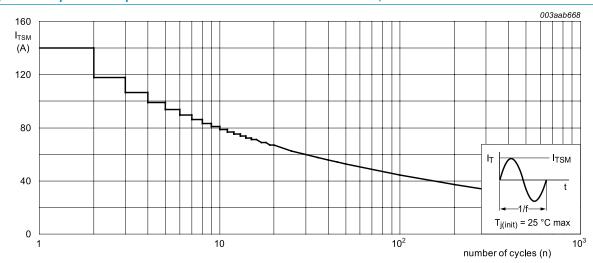
Fig. 2. RMS on-state current as a function of mounting base temperature; maximum values



 $\alpha$  = conduction angle

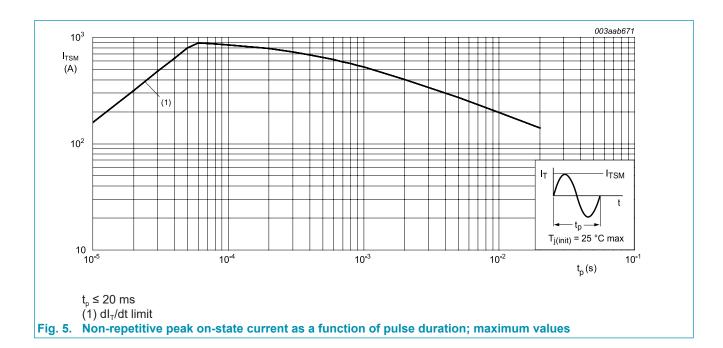
 $a = form factor = I_{T(RMS)} / I_{T(AV)}$ 

Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values



f = 50 Hz

Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{\text{th(j-mb)}}$	thermal resistance	full cycle; Fig 6	-	-	1.2	K/W
	from junction to mounting base	half cycle; Fig 6	-	-	1.7	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	printed circuit board mounted; minimum footprint	-	55	-	K/W

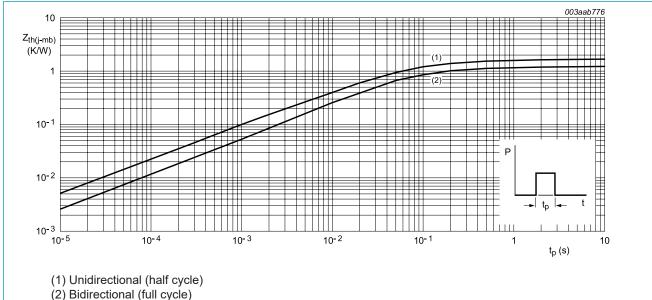
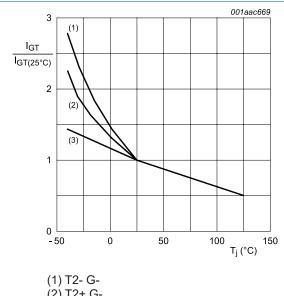


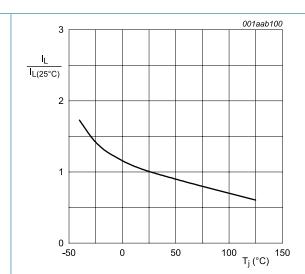
Fig. 6. Transient thermal impedance from junction to mounting base as a function of pulse duration

## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G+;  T_j = 25 °C; Fig. 7$	2	-	35	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G-; T_j = 25 °C; Fig. 7$	2	-	35	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \underline{\text{Fig. 7}}$	2	-	35	mA
I <sub>L</sub>	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{T2+ G+};$ $T_j = 25 \text{ °C}; \text{Fig. 8}$	-	-	50	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2+ G-};$ $T_j = 25 \text{ °C}; \text{Fig. 8}$	-	-	60	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; \text{ T2- G-};$ $T_j = 25 \text{ °C}; \text{Fig. 8}$	-	-	50	mA
I <sub>H</sub>	holding current	V <sub>D</sub> = 12 V; T <sub>j</sub> = 25 °C; <u>Fig. 9</u>	-	-	35	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 18 A; T <sub>j</sub> = 25 °C; <u>Fig. 10</u>	-	1.3	1.5	V
$V_{GT}$	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ Fig. 11	-	0.8	1	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C};$ Fig. 11	0.25	0.4	-	V
I <sub>D</sub>	off-state current	V <sub>D</sub> = 800 V; T <sub>j</sub> = 125 °C	-	0.1	0.5	mA
Dynamic	characteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM}$ = 536 V; $T_{j}$ = 125 °C; ( $V_{DM}$ = 67% of $V_{DRM}$ ); exponential waveform; gate open circuit	500	-	-	V/µs
dI <sub>com</sub> /dt	rate of change of commutating current	$V_D = 400 \text{ V; } T_j = 125 \text{ °C; } I_{T(RMS)} = 16 \text{ A;}$ $dV_{com}/dt = 20 \text{ V/}\mu\text{s; (snubberless condition); gate open circuit}$	15	-	-	A/ms

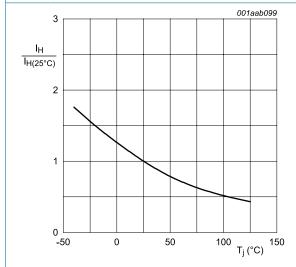


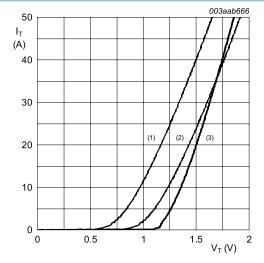


- (2) T2+ G-
- (3) T2+ G+

Fig. 7. Normalized gate trigger current as a function of junction temperature



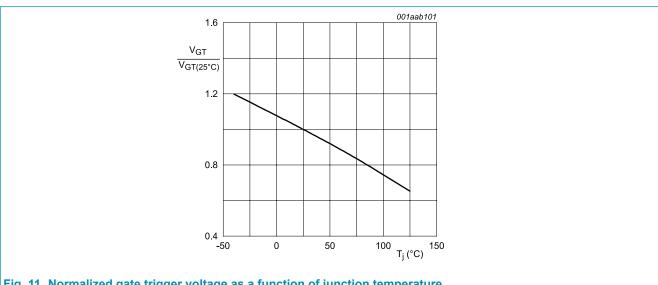




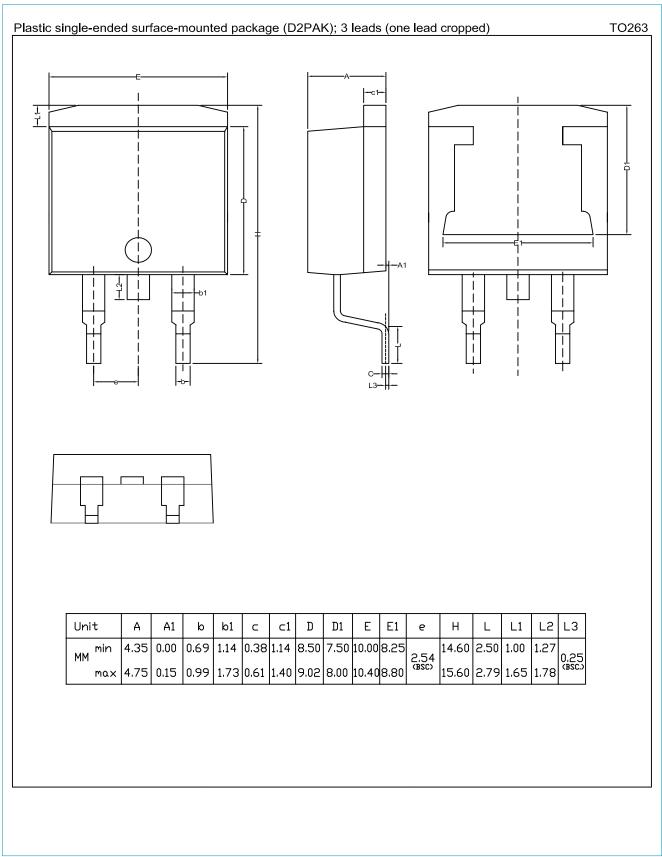
- $V_o$  = 1.024 V;  $R_s$  = 0.021 Ω (1)  $T_j$  = 125 °C; typical values
- (2)  $T_i = 125$  °C; maximum values
- (3) T<sub>i</sub> = 25 °C; maximum values

Fig. 9. Normalized holding current as a function of junction temperature

Fig. 10. On-state current as a function of on-state voltage



## 11. Package outline



### 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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