ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND	0.3V to +6V
SHDN, GAIN, BAND to GND	0.3V to (V _{CC} + 0.3V)
GSMIN, DCSIN	
Input Current (all digital inputs)	±10mA
Continuous Power Dissipation	
8-Pin µMAX (derate 4.5mW/°C abov	e +70°C)362mW

10-Pin µMAX (derate 5.6mW/°C above +	70°C)444mW
Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to

CAUTION! ESD SENSITIVE DEVICE

DC ELECTRICAL CHARACTERISTICS-MAX2651

absolute maximum rating conditions for extended periods may affect device reliability.

 $(V_{CC} = +2.7V \text{ to } +3.3V, \text{BAND} = \text{GAIN} = V_{CC}, \text{ no input signal, all input/output ports terminated in 50}\Omega, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted. Typical values are at V}_{CC} = +3V, T_A = +25^{\circ}\text{C}.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range	Vcc		2.7		3.3	V
Input Supply Current		DCS band, high-gain mode (BAND = GAIN = V _{CC})		5.7	8.7	
		DCS band, low-gain mode (BAND = V _{CC} , GAIN = GND)		2.2	3.3	mA
	Icc	GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})		5.9	9.6	
		GSM band, low-gain mode (BAND = GND, GAIN = V_{CC})		2.2	3.6	
Input Logic Threshold High	VIH				1.5	V
Input Logic Threshold Low	VIL	0.5		V		
Input Logic High Current	lін	V _{IN} = V _{CC} 1.5		μA		
Input Logic Low Current	Ι _Ι	V _{IN} = GND -1.5				μA

DC ELECTRICAL CHARACTERISTICS—MAX2652

 $(V_{CC} = +2.7V \text{ to } +3.3V, \text{BAND} = \text{GAIN} = V_{CC}, \text{ no input signal, all input/output ports terminated in 50}\Omega, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted.}$ Typical values are at $V_{CC} = +3V, T_A = +25^{\circ}\text{C}.$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range	V _{CC}		2.7		3.3	V
Input Supply Current		DCS band, high-gain mode (BAND = GAIN = V _{CC})		7.0	11.0	
		DCS band, low-gain mode (BAND = V_{CC} , GAIN = GND)			3.6	
	ICC	GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})		7.2	11.0	mA
		GSM band, low-gain mode (BAND = GND, GAIN = V _{CC})2.43.7		3.7		
Shutdown Supply Current		SHDN = GND		0.25		μA





DC ELECTRICAL CHARACTERISTICS—MAX2653

 $(V_{CC} = +2.7V \text{ to } +3.3V, \overline{SHDN} = GAIN = V_{CC}$, no input signal, all input/output ports terminated in 50 Ω , T_A = -40°C to +85°C, unless otherwise noted. Typical values are at V_{CC} = +3V, T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Logic Threshold High	VIH				1.5	V
Input Logic Threshold Low	VIL		0.5			V
Input Logic High Current	IIН	V _{IN} = V _{CC}			1.5	μA
Input Logic Low Current	Ι _{ΙL}	V _{IN} = GND	-1.5			μA
Input Voltage Range	Vcc		2.7		3.3	V
	laa	DCS band, high-gain mode (GAIN = V_{CC})		5.4	8.5	mA
Input Supply Current	Icc	DCS band, low-gain mode (GAIN = GND)		2.2	3.2	
Shutdown Supply Current	ISHDN	SHDN = GND		0.25		μA
Input Logic Threshold High	VIH				1.5	V
Input Logic Threshold Low	VIL		0.5			V
Input Logic High Current	IIН	VIN = VCC			3	μA
Input Logic Low Current	١ _{١L}	V _{IN} = GND	-1.5			μA

Note 1: Devices are production tested at $T_A = +25$ °C. Minimum and maximum limits are guaranteed by design and characterization.

AC ELECTRICAL CHARACTERISTICS—MAX2651

(MAX2651 EV kit, f_{IN} = 945MHz (GSM band), f_{IN} = 1850MHz (DCS band), P_{IN} = -30dBm, input and output matching networks are optimized for the frequency band of interest, all input/output ports terminated in 50 Ω , V_{CC} = +3V, T_A = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	COND	MIN	ТҮР	MAX	UNITS	
Input Frequency Range	fin	DCS band (BAND = V_{CC})		1805		1880	MHz
(Note 3)	UIN	GSM band (BAND = GNE))	925		960	
		DCS band, high-gain mode (BAND = GAIN =	TA = +25°C	16.5	18	19.5	
		V _{CC})	$TA = -40^{\circ}C \text{ to } +85^{\circ}C$	16		20	
		DCS band, low-gain	$TA = +25^{\circ}C$	-3.5	-2	-0.5	
Power Cain (Note 4)	G	mode (BAND = V _{CC} , GAIN = GND)	$TA = -40^{\circ}C \text{ to } +85^{\circ}C$	-4		0	dB
Power Gain (Note 4)	G	GSM band, high-gain mode (BAND = GND, GAIN = V_{CC})	TA = +25°C	16.5	18	19.5	
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	16		20	
		GSM band, low-gain mode (BAND = GAIN = GND)	TA = +25°C	-3.5	-2	-0.5	
			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	-4		0	1
		DCS band, high-gain mod (BAND = GAIN = V _{CC})	de		1.8	2.05	
Noise Figure (Note 4)	NF	DCS band, low-gain mod (BAND = V _{CC} , GAIN = GI			5.5	6.0	dB
		GSM band, high-gain mode (BAND = GND, GAIN = V_{CC})			1.2	1.4	
		GSM band, low-gain mode (BAND = GAIN = GND)			5.3	6.7	

AC ELECTRICAL CHARACTERISTICS—MAX2651 (continued)

(MAX2651 EV kit, f_{IN} = 945MHz (GSM band), f_{IN} = 1850MHz (DCS band), P_{IN} = -30dBm, input and output matching networks are optimized for the frequency band of interest, all input/output ports terminated in 50 Ω , V_{CC} = +3V, T_A = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
		(Note 5)	DCS band, high-gain mode $(BAND = GAIN = V_{CC})$	-11.5	-10		
Input Third-Order Intercept Point	IIP3	(NOLE 5)	DCS band, low-gain mode (BAND = V _{CC} , GAIN = GND)	-1.5	0.5		dBm
		(Note 6)	GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})	-10.5	-8.5		ubm
		(10010-0)	GSM band, low-gain mode (BAND = GND, GAIN = GND)	-2.5	0.5		
			d, high-gain mode GAIN = V _{CC})	-20	-18		
Input 1dB Compression Point	IP-1dB		d, low-gain mode V _{CC} , GAIN = GND)	-9.5	-8		dBm
	11 - TUB	(BAND =	id, high-gain mode GND, GAIN = V _{CC})	-20	-18		dBill
			id, Iow-gain mode GND, GAIN = GND)	-10.5	-8.5		
	S11 ²		d, high-gain mode GAIN = V _{CC})		-11	-8.5	
			DCS band, low-gain mode (BAND = V _{CC} , GAIN = GND)		-14	-12	dB
Input Return Loss		GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})			-11	-8.5	ar ar
			id, low-gain mode GAIN = GND)		-20	-15	
			d, high-gain mode GAIN = V _{CC})		-14	-10	
	10 12		d, low-gain mode V _{CC} , GAIN = GND)		-12	-10	-10
Output Return Loss	S ₂₂ ²		id, high-gain mode GND, GAIN = V _{CC})		-19	-15	dB
			id, Iow-gain mode GAIN = GND)		-20	-15	
			d, high-gain mode GAIN = V _{CC})		-32	-29	
Reverse Isolation			d, low-gain mode V _{CC} , GAIN = GND)		-20	-18	
	S ₁₂ ²		id, high-gain mode GND, GAIN = V _{CC})		-39	-35	dB
			nd, Iow-gain mode GAIN = GND)		-22	-20	

AC ELECTRICAL CHARACTERISTICS—MAX2652

(MAX2652 EV kit, f_{IN} = 945MHz (GSM band), f_{IN} = 1850MHz (DCS band), P_{IN} = -30dBm, input and output matching networks are optimized for the frequency band of interest, all input/output ports terminated in 50 Ω , V_{CC} = +3V, T_A = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL					ТҮР	MAX	UNITS
Input Frequency Range	f _{IN}	DCS band (BAND = V_{CC})			1805		1880	MHz
(Note 3)	IIN	GSM bar	nd (BAND = GND))	925		960	
			d, high-gain AND = GAIN =	TA = +25°C	16	17.5	19]
		V _{CC})		$TA = -40^{\circ}C \text{ to } +85^{\circ}C$	15.5		19.5	
			d, low-gain AND = V _{CC} ,	TA = +25°C	-3.5	-2	-0.5	
Power Gain (Note 4)	G	GAIN = 0	GND)	$TA = -40^{\circ}C \text{ to } +85^{\circ}C$	-4		0	dB
	G		nd, high-gain AND = GND,	TA = +25°C	16.5	18	19.5	GD
		GAIN = \		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	16		20	
			nd, low-gain AND = GAIN =	TA = +25°C	-4.0	-2	-0.5	
		GND)		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	-4.5		0	
			d, high-gain moo GAIN = V _{CC})	de		1.8	2.0	
Noise Figure (Note 4)	NF		DCS band, low-gain mode (BAND = V _{CC} , GAIN = GND)			5.8	6.3	dB
Noise Figure (Note 4)		GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})			1.3	1.5		
			GSM band, low-gain mode (BAND = GAIN = GND)			5.5	6.7	
			DCS band, hig (BAND = GAIN		-8.5	-7		
Input Third-Order		(Note 5)	DCS band, low (BAND = V _{CC} ,		0	1.5		dBm
Intercept Point	IIP3		GSM band, hig (BAND = GND		-9	-7.5		UDITI
		(Note 6)	GSM band, lov (BAND = GND	v-gain mode , GAIN = GND)	0.5	2.5		
			d, high-gain moo GAIN = V _{CC})	de	-18	-16.5		
Input 1dB Compression Point	IP-1dB	DCS band, low-gain m (BAND = V _{CC} , GAIN =			-10.5	-8		dPm
			GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})		-18	-16.5		dBm
		GSM bar	nd, low-gain mod GND, GAIN = G	е	-10	-8.5		

AC ELECTRICAL CHARACTERISTICS—MAX2652 (continued)

(MAX2652 EV kit, f_{IN} = 945MHz (GSM band), f_{IN} = 1850MHz (DCS band), P_{IN} = -30dBm, input and output matching networks are optimized for the frequency band of interest, all input/output ports terminated in 50 Ω , V_{CC} = +3V, T_A = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNIT	
		DCS band, high-gain mode (BAND = GAIN = V _{CC})		-12	-9.5		
Input Deturn Loop	1012	DCS band, low-gain mode (BAND = V_{CC} , GAIN = GND)		-12	-9.5	dB	
Input Return Loss	S ₁₁ ²	GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})		-11	-9.5	UD	
		GSM band, low-gain mode (BAND = GAIN = GND)		-20	-15		
	S ₂₂ ²	DCS band, high-gain mode $(BAND = GAIN = V_{CC})$		-11	-9.5		
		DCS band, low-gain mode (BAND = V_{CC} , GAIN = GND)		-17	-13	dB	
Output Return Loss		522	GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})		-12.5	-11	
		GSM band, low-gain mode (BAND = GAIN = GND)		-15	-13		
		DCS band, high-gain mode (BAND = GAIN = V _{CC})		-29	-26		
Reverse Isolation	S ₁₂ ²	DCS band, low-gain mode (BAND = V_{CC} , GAIN = GND)		-19	-17	dB	
	512	GSM band, high-gain mode (BAND = GND, GAIN = V _{CC})		-37	-34	UD	
		GSM band, low-gain mode (BAND = GAIN = GND)		-23	-20		

AC ELECTRICAL CHARACTERISTICS—MAX2653

(MAX2653 EV kit, f_{IN} = 1850MHz (DCS band), f_{IN} = 1960MHz (PCS band), P_{IN} = -30dBm, input and output matching networks are optimized for the frequency band of interest, all input/output ports terminated in 50 Ω , V_{CC} = +3V, T_A = +25°C, unless otherwise noted.) (Note 2)

PARAMETER	SYMBOL	COND	ITIONS	MIN	ТҮР	MAX	UNITS
Input Frequency Range	f., ,	$BAND = V_{CC}$	DCS band	1805		1880	MHz
(Note 3)	fin	DAIND = VCC	PCS band	1930		1990	IVILIZ
		DCS and PCS band, high-gain mode	$T_A = +25^{\circ}C$	17	18.5	20	
Power Gain (Note 4)	G	$(BAND = GAIN = V_{CC})$	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	16		20	dB
1 Ower Gain (Note 4)	U U	DCS and PCS band, low-gain mode (BAND =	$T_A = +25^{\circ}C$	-4	-2.5	-1	
		V_{CC} , GAIN = GND)	$T_{A} = -40^{\circ}C \text{ to } +85^{\circ}C$	-4		0	
		High-gain mode	DCS band		1.7	1.9	
Noise Figure (Note 4)	NF	$(BAND = GAIN = V_{CC})$	PCS band		1.8	2.05	dB
		DCS and PCS band, low- (BAND = V_{CC} , GAIN = G			5.3	6.7	UD UD
Input Third-Order			-10.5	-8.5		dBm	
Intercept Point (Notes 5, 7)	IIP3	DCS and PCS band, low- (BAND = V _{CC} , GAIN = G		-1	+1.5		UDITI
Input 1dB Compression		DCS and PCS band, high (BAND = GAIN = V _{CC})	-20	-18		10	
Point	IP _{-1dB}	DCS and PCS band, low- (BAND = V _{CC} , GAIN = Gi	-9	-7		dB	
		DCS and PCS band, high (BAND = GAIN = V _{CC})		-11	-8.5		
Input Return Loss	S ₁₁ ²	DCS and PCS band, low-gain mode (BAND = V_{CC} , GAIN = GND)			-11	-9.5	dB
		DCS and PCS band, high (BAND = GAIN = V _{CC})	-gain mode		-12	-9.5	
Output Return Loss	S ₂₂ ²	DCS and PCS band, low-gain mode (BAND = V_{CC} , GAIN = GND)			-15	-12	dB
		High-gain mode (BAND	DCS band		-35	-31	
Reverse Isolation	10,012	= GAIN $=$ V _{CC})	PCS band		-33	-29	dB
neverse isolation	S ₁₂ ²	Low-gain mode (BAND =	DCS band		-21	-18	ub
		V _{CC} , GAIN = GND)	PCS band		-21	-18	

-21

Note 2: Minimum and maximum limits are guaranteed by design and characterization, but not production tested.

Note 3: The part has been fully characterized at the specified frequency range. Operation outside of this range is possible but not guaranteed.

Note 4: Specification excludes circuit board losses.

Note 5: Measured with two tones, f_{IN1} = 1850MHz, f_{IN2} = 1850.8MHz, P_{IN} = -33dBm for each tone.

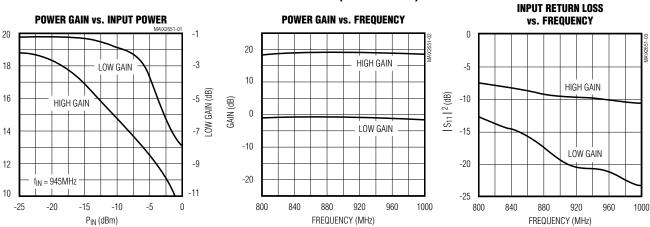
Note 6: Measured with two tones, $f_{IN1} = 945MHz$, $f_{IN2} = 945.8MHz$, $P_{IN} = -33dBm$ for each tone.

Note 7: Measured with two tones, f_{IN1} = 1960MHz, f_{IN2} = 1960.8MHz, P_{IN} = -33dBm for each tone.

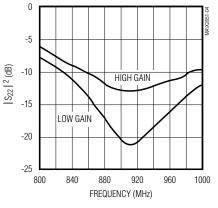
Typical Operating Characteristics

(MAX2651 EV kit, V_{CC} = +3.0V, input and output matched with recommended matching networks, T_A = +25°C, unless otherwise noted.)

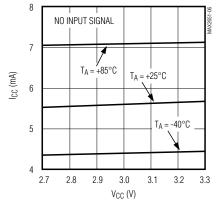
MAX2651 (GSM Band)



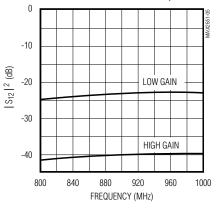




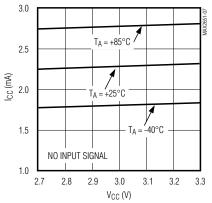




REVERSE ISOLATION vs. FREQUENCY



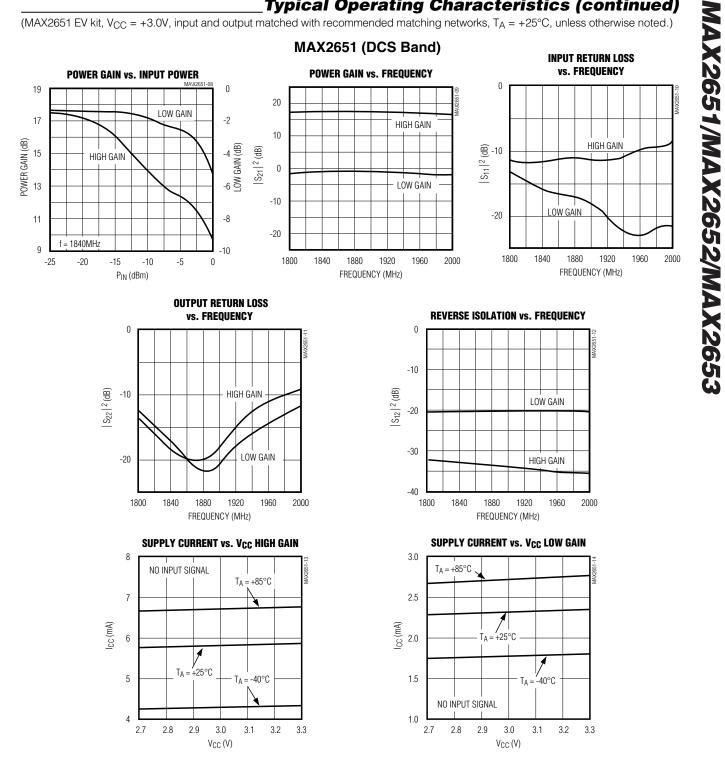
SUPPLY CURRENT vs. V_{CC} LOW GAIN



POWER GAIN (dB)

Typical Operating Characteristics (continued)

(MAX2651 EV kit, V_{CC} = +3.0V, input and output matched with recommended matching networks, T_A = +25°C, unless otherwise noted.)



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-2

-4

LOW GAIN (dB) -6

-10

-12

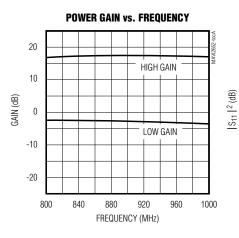
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Typical Operating Characteristics (continued)

(MAX2652 EV kit, V_{CC} = +3.0V, input and output matched with recommended matching networks, T_A = +25°C, unless otherwise noted.)

MAX2652 (GSM Band)



INPUT RETURN LOSS vs. FREQUENCY 0 -10 HIGH GAIN -20 LOW GAIN -30 -40 -50 800 840 880 920 960 1000 FREQUENCY (MHz)

OUTPUT RETURN LOSS vs. FREQUENCY

PIN (dBm)

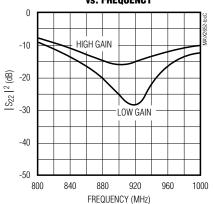
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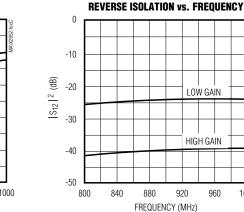
-5

-15

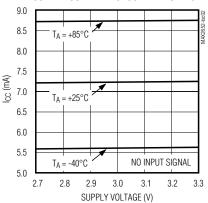
POWER GAIN vs. INPUT POWER

LOW GAIN

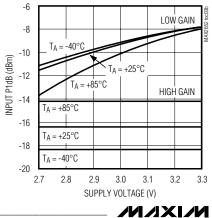




HIGH-GAIN MODE SUPPLY CURRENT vs. SUPPLY VOLTAGE



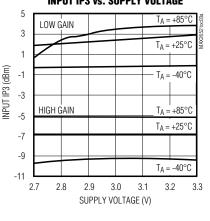
INPUT P1dB vs. SUPPLY VOLTAGE



LOW-GAIN MODE SUPPLY CURRENT vs. SUPPLY VOLTAGE 3.5 3.0 1 $T_A = +85^{\circ}C$ 2.5 Icc (mA) 1 $T_A = +25^{\circ}C$ 2.0 1 $T_A = -40^{\circ}C$ 1.5 NO INPUT SIGNAL 1.0 2.7 2.8 2.9 3.0 3.1 3.2 3.3 SUPPLY VOLTAGE (V) 10

INPUT IP3 vs. SUPPLY VOLTAGE

1000





20

18

16

14

12

10

8

-25

HIGH GAIN (dB)

HIGH GAIN

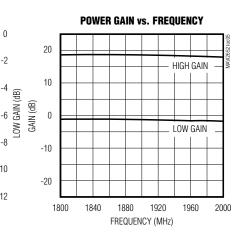
f = 945 MHz

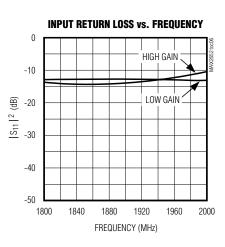
-20

Typical Operating Characteristics (continued)

(MAX2652 EV kit, V_{CC} = +3.0V, input and output matched with recommended matching networks, T_A = +25°C, unless otherwise noted.)

MAX2652 (DCS Band)





OUTPUT RETURN LOSS vs. FREQUENCY

PIN (dBm)

-10

-5

POWER GAIN vs. INPUT POWER

HIGH GAIN

f = 945MHz

-20

-15

LOW GAIN

20

18

16

14

12

10

8

-25

HIGH GAIN (dB)

0

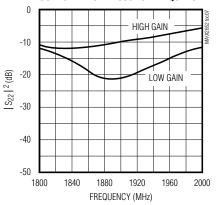
-2

-4

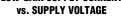
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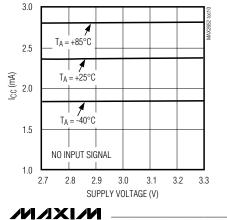
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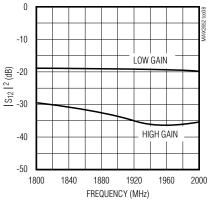


LOW-GAIN SUPPLY CURRENT

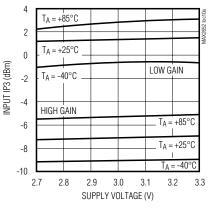




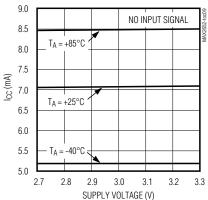




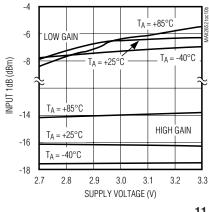
INPUT IP3 vs. SUPPLY VOLTAGE



HIGH-GAIN SUPPLY CURRENT vs. SUPPLY VOLTAGE



INPUT P1dB vs. SUPPLY VOLTAGE



Typical Operating Characteristics (continued)

(MAX2653 EV kit, V_{CC} = +3.0V, input and output matched with recommended matching networks, T_A = +25°C, unless otherwise noted.)

MAX2653 (DCS Band) **INPUT RETURN LOSS POWER GAIN vs. INPUT POWER POWER GAIN vs. FREQUENCY** vs. FREQUENCY 0 -3 20 18 HIGH GAIN LOW GAIN -4 16 -5 10 LOW GAIN (dB) | S₁₁ | ² (dB) GAIN (dB) HIGH GAIN 14 0 -8 LOW GAIN LOW GAIN 12 -9 -10 -12 HIGH GAIN 10 -11 -20 f = 1840MHz -16 -25 -20 -15 -10 -5 0 1800 1840 1880 1920 1960 2000 1800 1840 1880 1920 1960 2000 PIN (dBm) FREQUENCY (MHz) FREQUENCY (MHz) **OUTPUT RETURN LOSS REVERSE ISOLATION vs. FREQUENCY** vs. FREQUENCY 0 0 HIGH GAIN -10 -10 | S₂₂ | ² (dB) | S₁₂ | ² (dB) LOW GAIN LOW GAIN -20 -20 -30 -30 HIGH GAIN -40 -40 1800 1840 1880 1920 1960 2000 1800 1840 1880 1920 1960 2000 FREQUENCY (MHz) FREQUENCY (MHz) SUPPLY CURRENT vs. V_{CC} HIGH GAIN SUPPLY CURRENT vs. V_{CC} LOW GAIN 8 3.0 NO INPUT SIGNAL $T_A = +85^{\circ}C$ $T_A = +85^{\circ}C$ 7 2.5 Icc (mA) (mA) 500 $T_A = +25^{\circ}C$ 6 T_A = +25°C $T_A = -40^{\circ}C$ $T_A = -40^{\circ}C$ 5 1.5 NO INPUT SIGNAL 4 1.0 2.7 2.8 2.9 3.0 3.1 3.2 3.3 2.7 2.8 2.9 3.0 3.3 3.1 3.2 V_{CC} (V) $V_{CC}(V)$

POWER GAIN (dB)

Pin Description

	PIN		NAME	FUNCTION
MAX2651	MAX2652	MAX2653		FUNCTION
	1	1	SHDN	Shutdown Logic Input. Drive low to enter shutdown; drive high or connect to V_{CC} for normal operation.
1, 3, 6	3, 6	2, 4, 5	GND	RF Ground. Connect to the ground plane as close to the IC as possible to minimize trace inductance.
2	2	_	GSMIN	RF Input for GSM Band
4	4	3	DCSIN	RF Input for DCS and PCS bands
5	5	_	BAND	Band-Select Logic Input. Drive BAND high to enable DCS/PCS LNA; drive low to enable GSM LNA.
7	7	6	DCSOUT	RF Output for DCS and PCS Bands
8	8	7	Vcc	Supply Voltage Input, +2.7V < V _{CC} < +3.3V
9	9	_	GSMOUT	RF Output for GSM Band
10	10	8	GAIN	Gain-Select Logic Input. Drive GAIN high for high-gain operation; drive GAIN low for low-gain operation.

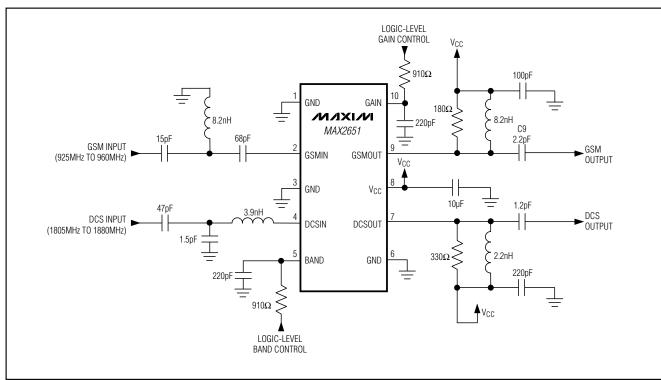


Figure 1. MAX2651 Typical Application Circuit

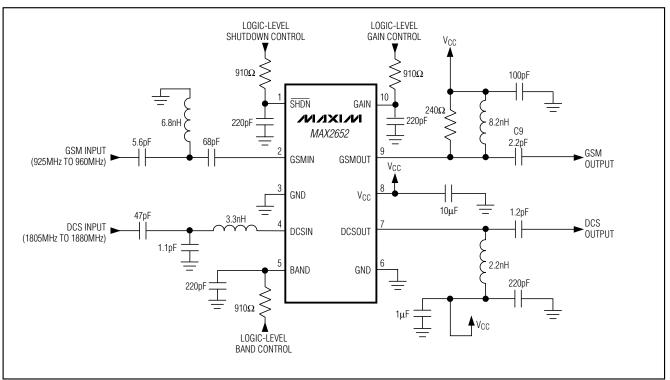


Figure 2. MAX2652 Typical Application Circuit

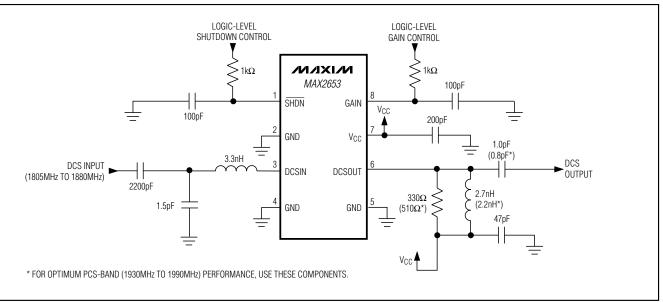


Figure 3. MAX2653 Typical Application Circuit

Detailed Description

Gain Select

The MAX2651/MAX2652/MAX2653 offer a step gain in the LNA to optimize the receiver's dynamic range. A logic-level low at GAIN reduces the active LNA's gain by about 20dB and reduces supply current by 3.5mA.

Shutdown Functionality

The MAX2652/MAX2653 offer a low-current shutdown feature. Drive \overline{SHDN} low to power down the LNA and reduce supply current to less than 0.25µA.

Applications Information

External Components

The MAX2651/MAX2652/MAX2653 require matching circuits at their inputs and outputs for operation in a 50 Ω system. The application circuits in Figures 1, 2, and 3 describe the matching circuits for each device's LNA; suggested component values, suppliers, and part numbers are listed in the MAX2651/MAX2652/MAX2653 EV kits manual. These values are optimized for best simultaneous noise figure, gain, and return loss performance.

Input and output impedance matching networks are very sensitive to layout-related parasitics. It is important to keep all matching components as close to the device as possible to minimize the effects of stray inductance and stray capacitance of PC board traces, particularly for the 1800MHz and 1900MHz bands.

Using the Collector Load Resistor to Set Gain

The MAX2651/MAX2652/MAX2653 provide open-collector output stages to allow an external resistor to set the gain. The collector pull-up resistors set the gain for each LNA to about 18dB. Lower gains are achieved by reducing this resistance, and higher gains are achieved by increasing it. The maximum achievable gain is defined by the maximum collector current swing. Note that the value of the collector gain-setting resistor principally defines the LNA's output impedance, and that the matching networks are tuned to match this impedance to 50Ω . Redefining the LNA gain by changing the collector gain-setting resistor requires retuning the output matching networks.

Layout and Power-Supply Bypassing

A properly designed PC board is essential to any RF/microwave circuit. Be sure to use controlled impedance lines on all high-frequency inputs and outputs. Proper grounding of the GND pins is fundamental; if the PC board uses a topside RF ground, connect all GND pins directly to it. For boards where the ground plane is not on the component side, it's best to connect all GND pins to the ground plane with plated throughholes close to the package.

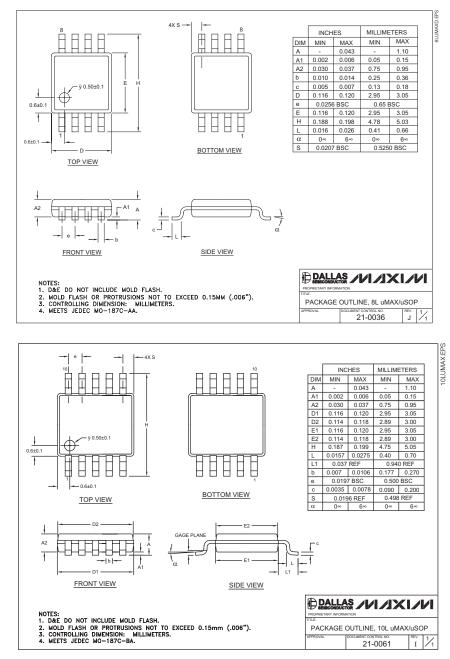
To minimize coupling between different sections of the system, the ideal power-supply layout is a star configuration with a large decoupling capacitor at a central V_{CC} node. The V_{CC} traces branch out from this central node, each leading to a separate V_{CC} node on the PC board. A second bypass capacitor that has low ESR at the RF frequency of operation is placed at the end of each trace. This arrangement provides local decoupling at the V_{CC} pin. At high frequencies, any signal leaking out of one supply pin sees a relatively high impedance (formed by the V_{CC} trace inductance) to the central V_{CC} node and an even higher impedance to any other supply pin, as well as a low impedance to ground through its bypass capacitor.

_Chip Information

MAX2651 TRANSISTOR COUNT: 272 MAX2652 TRANSISTOR COUNT: 272 MAX2653 TRANSISTOR COUNT: 253

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



Note: The packages for these devices do not feature the exposed pad.

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