Characteristics ESDA8V2-1J

#### 1 Characteristics

Table 1. Absolute maximum ratings ( $T_{amb} = 25$  °C)

Symbol	Parameter	Value	Unit
V <sub>PP</sub>	ESD discharge: IEC 61000-4-2 air discharge on input pin IEC 61000-4-2 contact discharge on input pin MIL STD 883G - Method 3015-7: class 3B	±30 ±30 ±30	kV
P <sub>PP</sub>	Peak pulse power dissipation (8/20 μs) <sup>(1)</sup> $T_{j \text{ initial}} = T_{amb}$	500	W
I <sub>PP</sub>	Peak pulse current (8/20 µs)	25	Α
Tj	Junction temperature range	-40 to +125	°C
T <sub>stg</sub>	Storage temperature range	- 55 to +150	°C
TL	Maximum lead temperature for soldering during 10 s	260	°C

<sup>1.</sup> For a surge greater than the maximum values, the diode will fail in short-circuit

Table 2. Electrical characteristics (definitions)

Symbol	Parameter	'1
$V_{BR}$	Breakdown voltage	1 <sub>F</sub>
I <sub>RM</sub>	Leakage current @ V <sub>RM</sub>	
V <sub>RM</sub>	Stand-of voltage	V <sub>CL</sub> V <sub>BR</sub> V <sub>RM</sub>
V <sub>CL</sub>	Clamping voltage	I RM
I <sub>PP</sub>	Peak pulse current	
С	Input capacitance	Slope= 1/R <sub>d</sub>

Table 3. Electrical characteristics (values,  $T_{amb} = 25 \,^{\circ}\text{C}$ )

allip/						
Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
$V_{BR}$	Breakdown voltage	I <sub>R</sub> = 1 mA	8.2	-	-	V
I <sub>RM</sub>	Leakage current @ V <sub>RM</sub>	V <sub>RM</sub> = 5 V	-	0.1	0.5	μΑ
V <sub>CL</sub>	Clamping voltage (8/20 µs waveform)	I <sub>PP</sub> = 1 A	-	-	11	V
		I <sub>PP</sub> = 5 A	-	-	13	V
		I <sub>pp</sub> = 25 A	-	-	20	V
С	Input capacitance	$V_R = 0 \text{ V}, F_{osc} = 1 \text{ MHz},$ $V_{osc} = 30 \text{ mV}$	-	210	250	pF

ESDA8V2-1J Characteristics

Figure 2. Relative variation of peak pulse power versus initial junction temperature

Figure 3. Peak pulse power versus exponential pulse duration

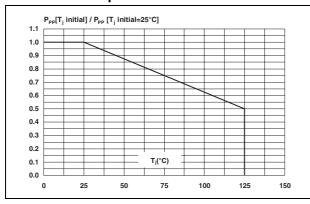
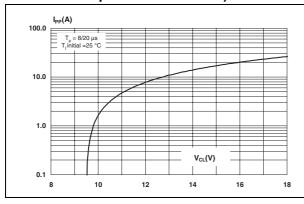


Figure 4. Clamping voltage versus peak pulse current (typical values, exponential waveform)

Figure 5. Forward voltage drop versus peak forward current (typical values)



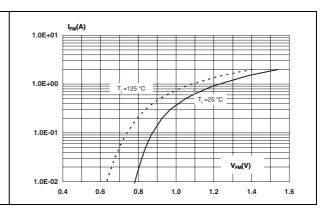
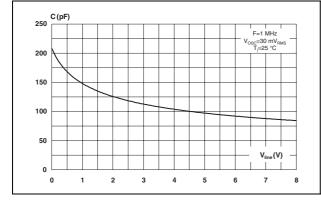


Figure 6. Junction capacitance versus reverse voltage applied (typical values)

Figure 7. Relative variation of leakage current versus juntion temperature (typical values)



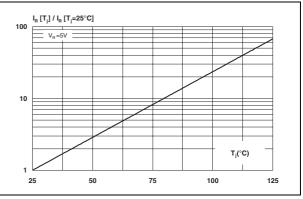
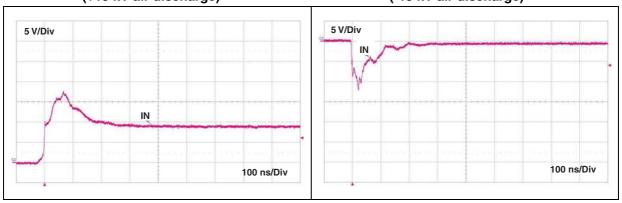


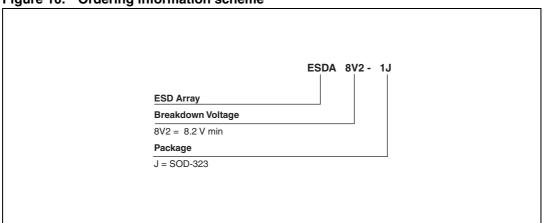
Figure 8. ESD response to IEC 61000-4-2 (+15 kV air discharge)

Figure 9. ESD response to IEC 61000-4-2 (-15 kV air discharge)



# 2 Ordering information scheme

Figure 10. Ordering information scheme



ESDA8V2-1J Package information

#### 3 Package information

- Epoxy meets UL94, V0
- Bar indicates cathode

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 4. SOD-323 dimensions

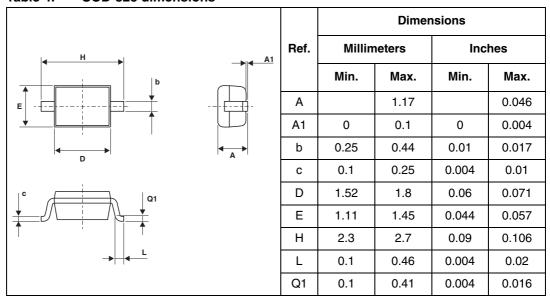
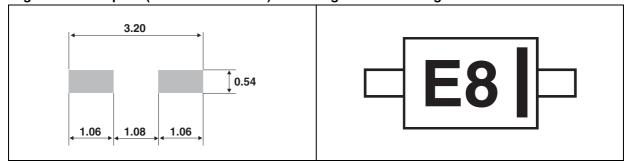


Figure 11. Footprint (dimensions in mm) Figure 12. Marking



Ordering information ESDA8V2-1J

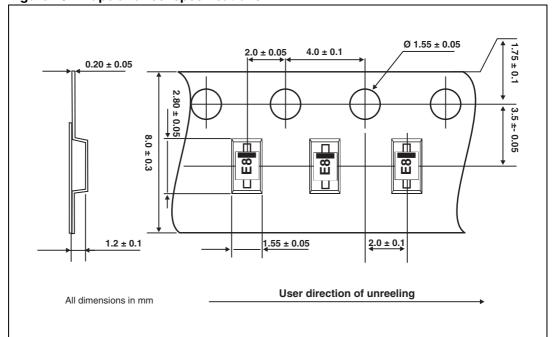


Figure 13. Tape and reel specifications

## 4 Ordering information

Table 5. Ordering information

Order code	Marking	Weight	Base qty	Delivery mode
ESDA8V2-1J	E8	5 mg	3000	Tape and reel

### 5 Revision history

Table 6. Document revision history

Date	Revision	Changes
11-Aug-2009	1	Initial release

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