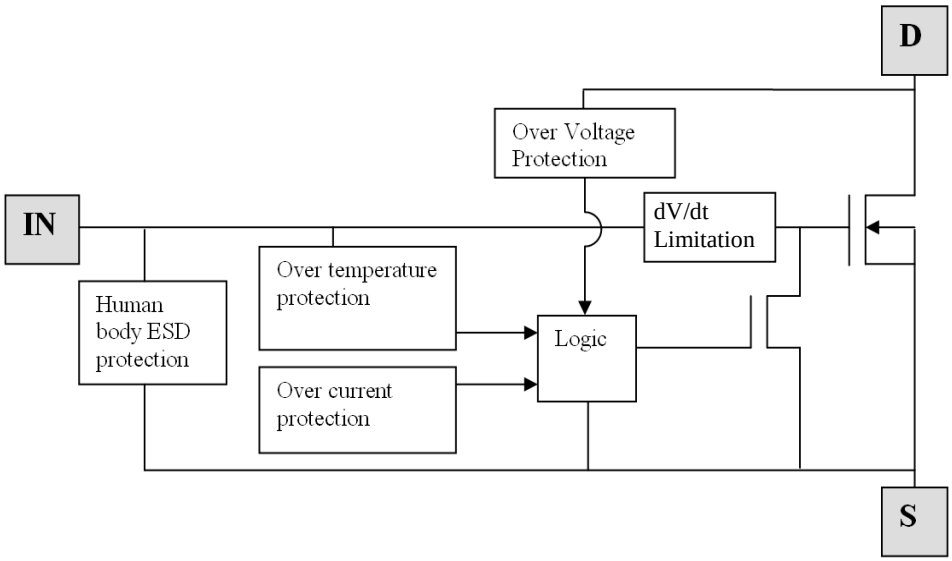
Functional Block Diagram



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Value	Unit
Continuous Drain-Source Voltage	V_{DS}	60	V
Drain-Source Voltage for Short Circuit Protection	$V_{DS(SC)}$	24	V
Continuous Input Voltage	V_{IN}	-0.5 to +6	V
Continuous Input Current @-0.2V ≤ V_{IN} ≤ 6V Continuous Input Current @V_{IN} < -0.2V or V_{IN} > 6V	I_{IN}	No limit $\lvert I_{IN} \rvert$ ≤2	mA
Pulsed Drain Current @V_{IN} = 3.3V	I_{DM}	5	A
Pulsed Drain Current @V_{IN} = 5V	I_{DM}	6	A
Continuous Source Current (Body Diode) (Note 5)	I_S	2.5	A
Pulsed Source Current (Body Diode)	I_{SM}	10	A
Unclamped Single Pulse Inductive Energy, T_J = +25°C, I_D = 0.5A, V_{DD} = 24V	E_{AS}	480	mJ
Electrostatic Discharge (Human Body Model)	V_{ESD}	4000	V
Charged Device Model	V_{CDM}	1000	V

Thermal Characteristics (@T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Value	Unit
Power Dissipation at T_A = +25°C (Note 5) Linear Derating Factor	P_D	1.0 8.0	W mW/°C
Power Dissipation at T_A = +25°C (Note 6) Linear Derating Factor	P_D	1.6 12.8	W mW/°C
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	83	°C/W
Thermal Resistance, Junction to Case (Note 7)	$R_{\theta JC}$	39	°C/W
Operating Temperature Range	T_J	-40 to +150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

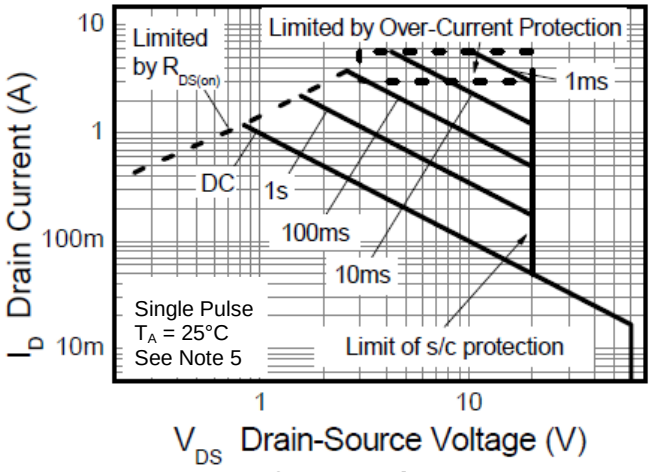
Notes:
5. For a device surface mounted on 15mm x 15mm single sided 1oz weight copper on 1.6mm FR-4 board, in still air conditions. Sink split drain 80% and source 20% to isolate connections.
6. For a device surface mounted on 50mm x 50mm single sided 2oz weight copper on 1.6mm FR-4 board, in still air conditions. Sink split drain 80% and source 20% to isolate connections.
7. Thermal resistance between junction and the mounting surfaces of drain and source pins.

Recommended Operating Conditions

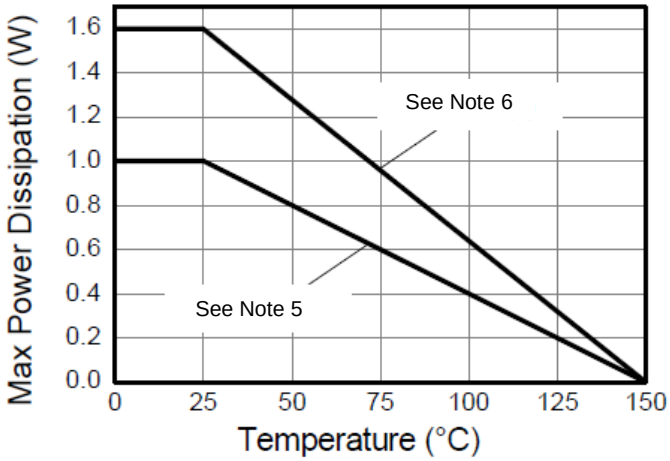
The ZXMS6005SG is optimized for use with µC operating from 3.3V and 5V supplies.

Characteristic	Symbol	Min	Max	Unit
Input Voltage Range	V_{IN}	0	5.5	V
Ambient Temperature Range	T_A	-40	+125	°C
High Level Input Voltage for MOSFET to be on	V_{IH}	3	5.5	V
Low Level Input Voltage for MOSFET to be off	V_{IL}	0	0.7	V
Peripheral Supply Voltage (voltage to which load is referred)	V_P	0	24	V

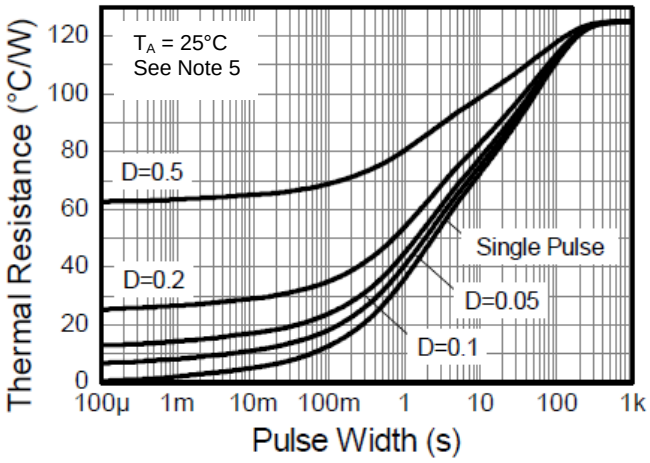
Thermal Characteristics



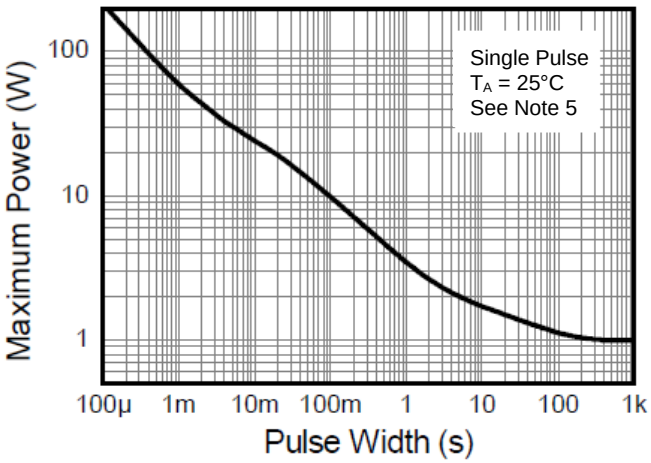
Safe Operating Area



Derating Curve



Transient Thermal Impedance



Pulse Power Dissipation

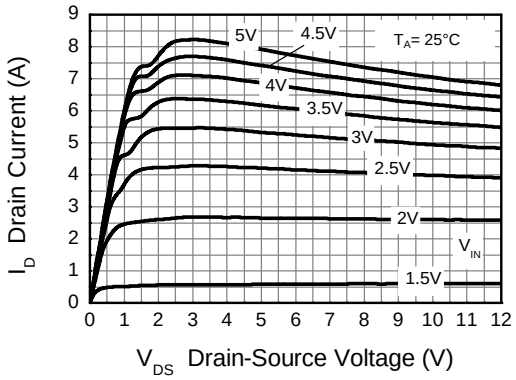
Electrical Characteristics (@T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Static Characteristics						
Drain-Source Clamp Voltage	$V_{DS(AZ)}$	60	65	70	V	I_D = 10mA
Off State Drain Current	I_{DSS}	—	—	1	μA	V_{DS} = 12V, V_{IN} = 0V
		—	—	2		V_{DS} = 36V, V_{IN} = 0V
Input Threshold Voltage	$V_{IN(TH)}$	0.7	1.2	1.5	V	V_{DS} = V_{GS}, I_D = 1mA
Input Current	I_{IN}	—	60	100	μA	V_{IN} = 3V
		—	120	200		V_{IN} = 5V
Input Current While Over Temperature Active	—	—	—	300	μA	V_{IN} = 5V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	—	170	250	mΩ	V_{IN} = 3V, I_D = 1A
		—	150	200		V_{IN} = 5V, I_D = 1A
Continuous Drain Current (Note 5)	I_D	1.4	—	—	A	V_{IN} = 3V, T_A = +25°C
		1.6	—	—		V_{IN} = 5V, T_A = +25°C
Continuous Drain Current (Note 6)		1.9	—	—		V_{IN} = 3V, T_A = +25°C
		2.0	—	—		V_{IN} = 5V, T_A = +25°C
Current Limit (Note 8)	$I_{D(LIM)}$	2.2	5	—	A	V_{IN} = 3V
		3.3	7	—		V_{IN} = 5V
Dynamic Characteristics						
Turn On Delay Time	$t_{D(ON)}$	—	6	—	μs	V_{DD} = 12V, I_D = 1A, V_{GS} = 5V
Rise Time	t_R	—	14	—		
Turn Off Delay Time	$t_{D(OFF)}$	—	34	—		
Fall Time	t_F	—	19	—		
Over-Temperature Protection						
Thermal Overload Trip Temperature (Note 9)	T_{JT}	+150	+175	—	°C	—
Thermal Hysteresis (Note 9)	—	—	+10	—	°C	—

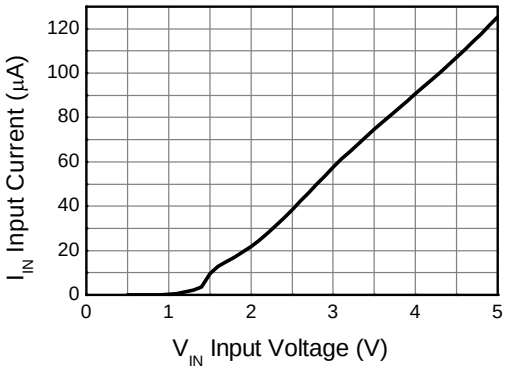
Notes: 8. The drain current is restricted only when the device is in saturation (see graph 'Typical Output Characteristic'). This allows the device to be used in the fully on state without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unnecessary.

9. Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal operating range, so this part is not designed to withstand over-temperature for extended periods.

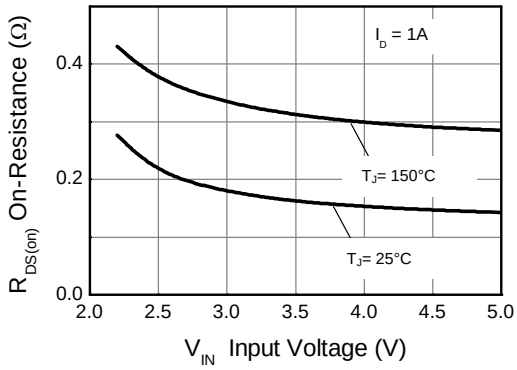
Typical Characteristics



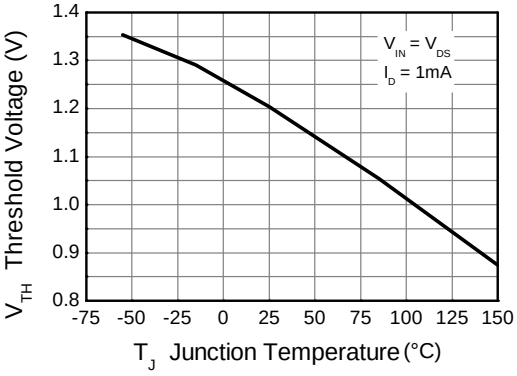
Typical Output Characteristic



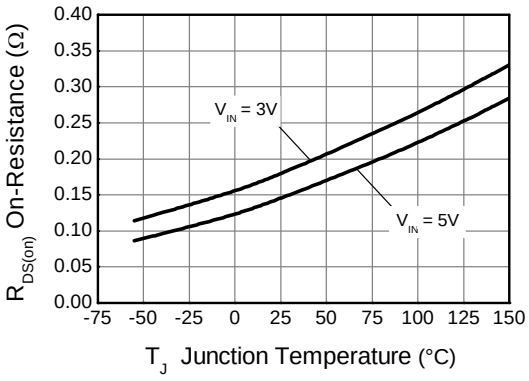
Input Current vs Input Voltage



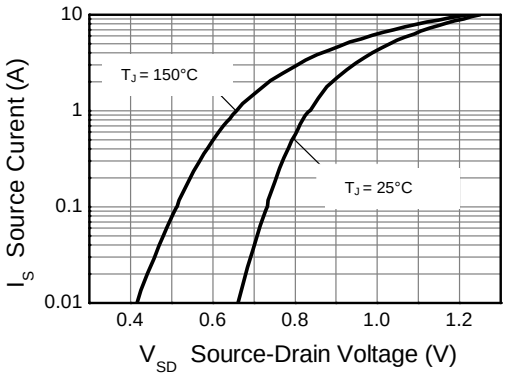
On-Resistance vs Input Voltage



Threshold Voltage vs Temperature

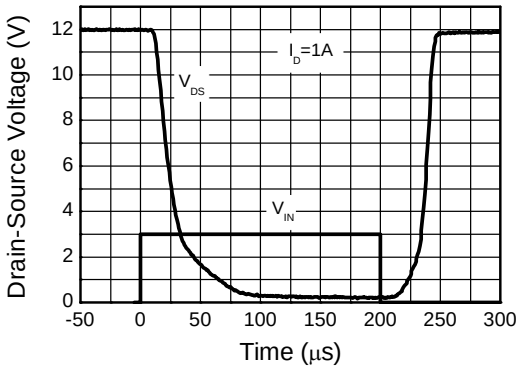


On-Resistance vs Temperature

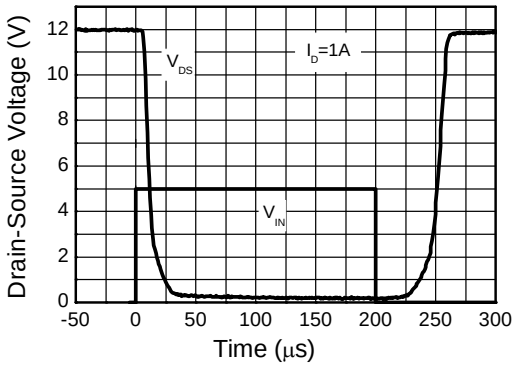


Reverse Diode Characteristic

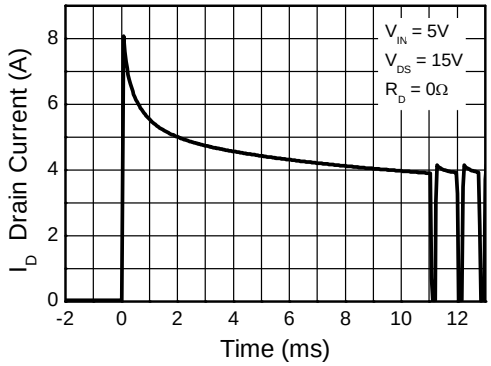
Typical Characteristics (Cont.)



Switching Speed



Switching Speed

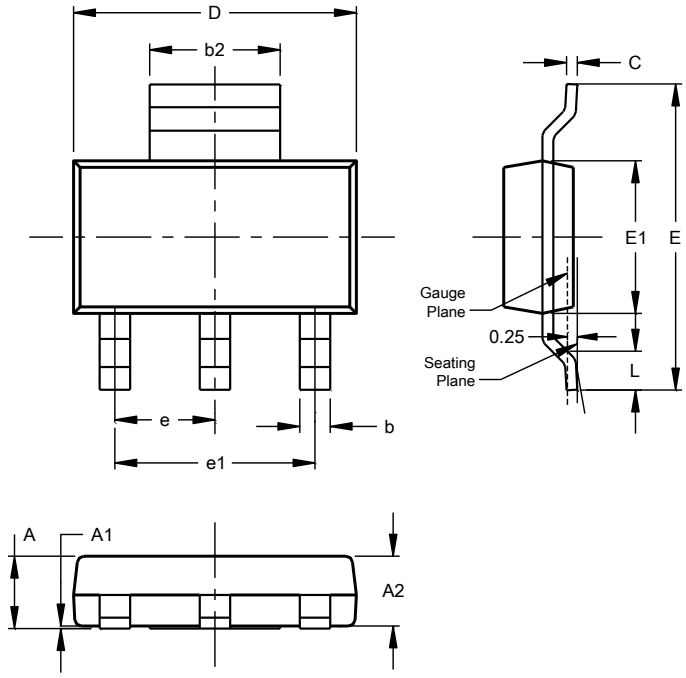


Typical Short Circuit Protection

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)

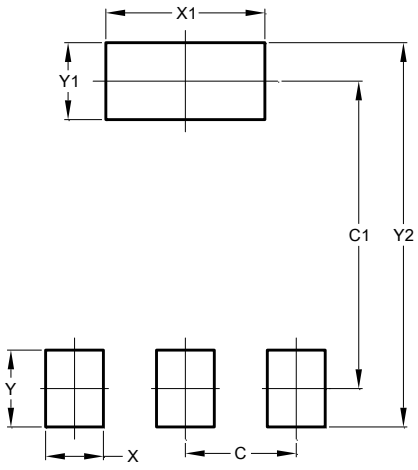


SOT223 (Type DN)			
Dim	**Min**	**Max**	**Typ**
A	--	1.70	--
A1	0.01	0.15	--
A2	1.50	1.68	1.60
b	0.60	0.80	0.70
b2	2.90	3.10	--
c	0.20	0.32	--
D	6.30	6.70	--
E	6.70	7.30	--
E1	3.30	3.70	--
e	--	--	2.30
e1	--	--	4.60
L	0.85	--	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

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