

Thermal Performance

Parameter	Symbol	Limit	Unit
Lead Temperature (1/8" from case)	T_L	10	S
Thermal Resistance - Junction to Ambient	$R\theta_{JA}$	62.5	$^{\circ}\text{C/W}$

Notes: Surface mounted on FR4 board $t \leq 10\text{sec}$

Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	BV_{DSS}	600	--	--	V
Drain-Source On-State Resistance	$V_{GS} = 10\text{V}, I_D = 0.6\text{A}$	$R_{DS(ON)}$	--	10.5	12	Ω
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(TH)}$	2.0	--	4.0	V
Zero Gate Voltage Drain Current	$V_{DS} = 600\text{V}, V_{GS} = 0\text{V}$	I_{DSS}	--	--	10	μA
Gate Body Leakage	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	I_{GSS}	--	--	± 100	nA
Forward Transconductance	$V_{DS} \geq 50\text{V}, I_D = 0.5\text{A}$	g_{fs}	--	10	--	S
Diode Forward Voltage	$I_S = 1\text{A}, V_{GS} = 0\text{V}$	V_{SD}	--	--	1.5	V

Dynamic^b

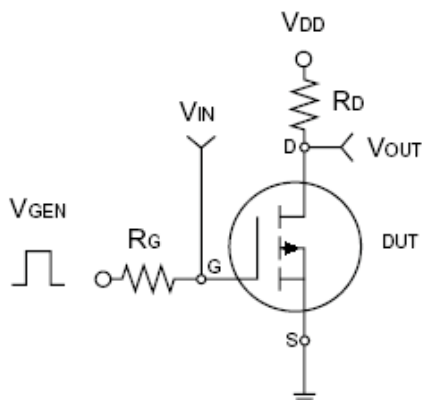
Total Gate Charge	$V_{DS} = 400\text{V}, I_D = 1\text{A},$ $V_{GS} = 10\text{V}$	Q_g	--	8.5	14	nC
Gate-Source Charge		Q_{gs}	--	1.8	--	
Gate-Drain Charge		Q_{gd}	--	4	--	
Input Capacitance	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V},$ $f = 1.0\text{MHz}$	C_{iss}	--	210	--	pF
Output Capacitance		C_{oss}	--	28	--	
Reverse Transfer Capacitance		C_{rss}	--	4.2	--	

Switching^c

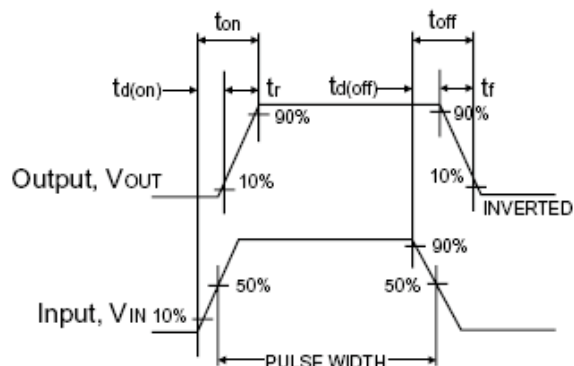
Turn-On Delay Time	$V_{GS} = 10\text{V}, I_D = 1\text{A},$ $V_{DS} = 300\text{V}, R_G = 6\Omega$	$t_{d(on)}$	--	8	--	nS
Turn-On Rise Time		t_r	--	21	--	
Turn-Off Delay Time		$t_{d(off)}$	--	18	--	
Turn-Off Fall Time		t_f	--	24	--	

Notes:

- Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- For design reference only, not subject to production testing.
- Switching time is essentially independent of operating temperature.



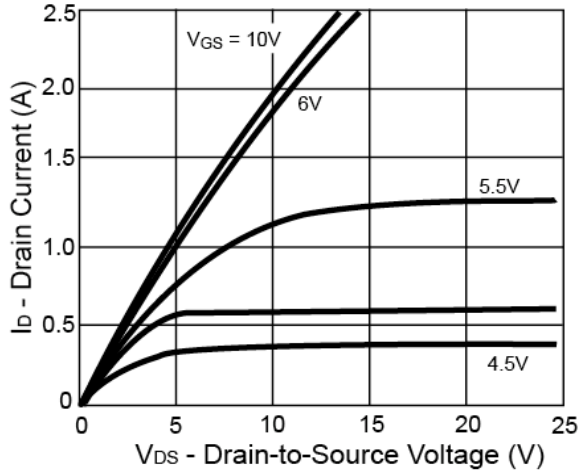
Switching Test Circuit



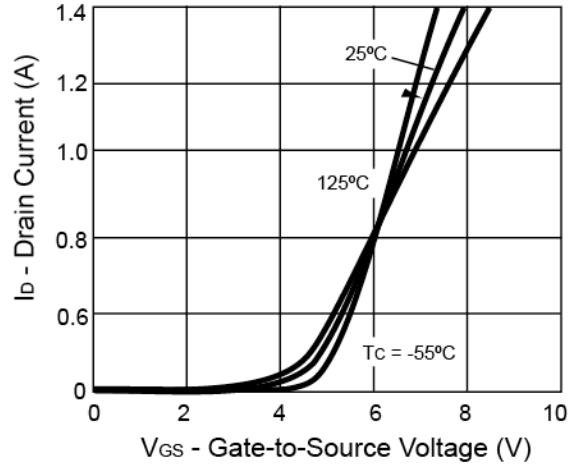
Switchin Waveforms

Electrical Characteristics Curve ($T_a = 25^\circ\text{C}$, unless otherwise noted)

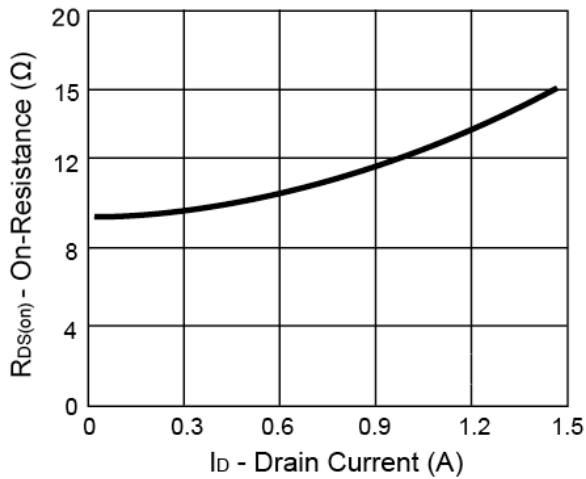
Output Characteristics



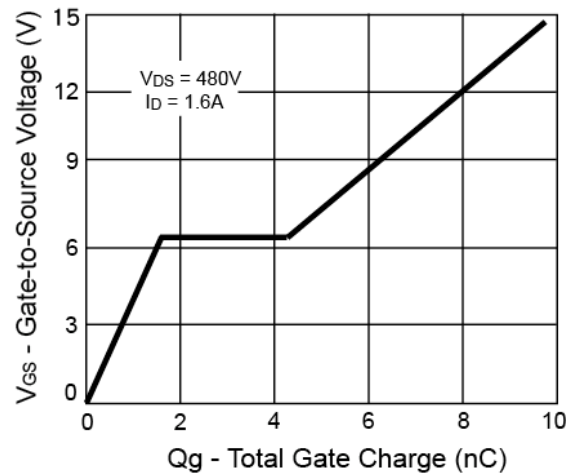
Transfer Characteristics



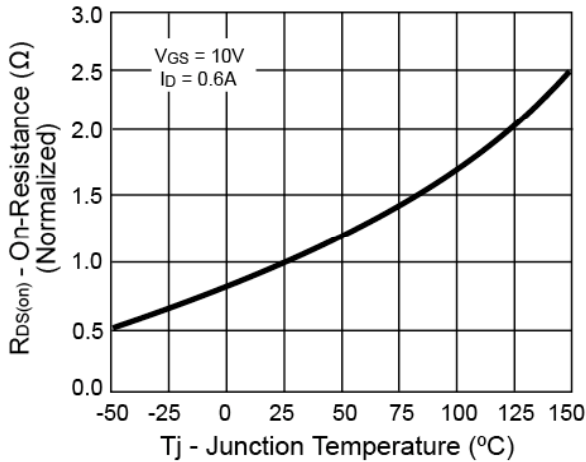
On-Resistance vs. Drain Current



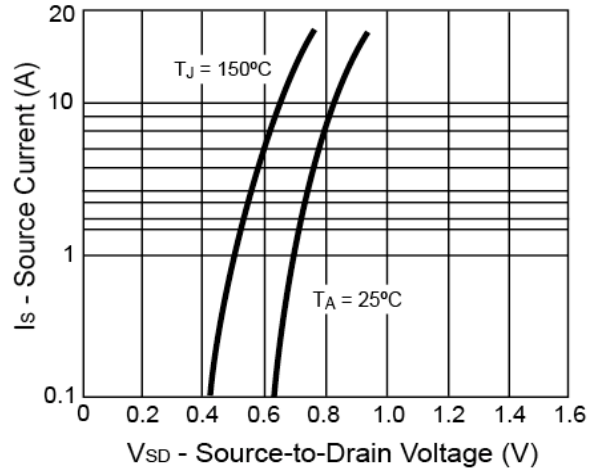
Gate Charge



On-Resistance vs. Junction Temperature

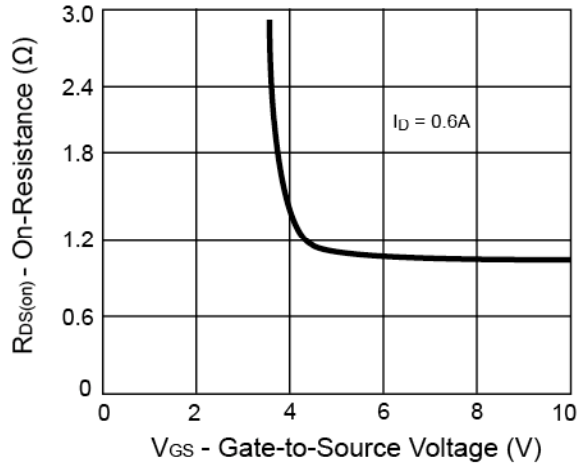


Source-Drain Diode Forward Voltage

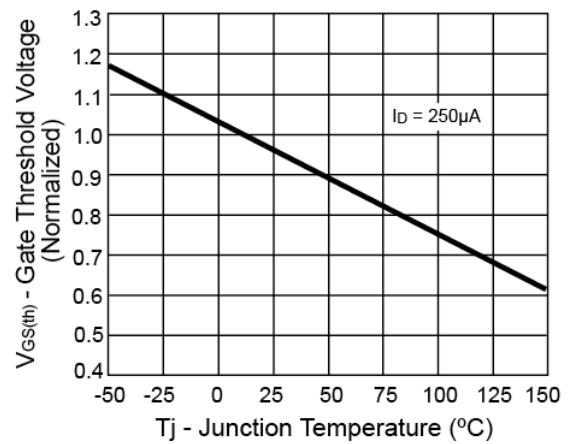


Electrical Characteristics Curve ($T_a = 25^\circ\text{C}$, unless otherwise noted)

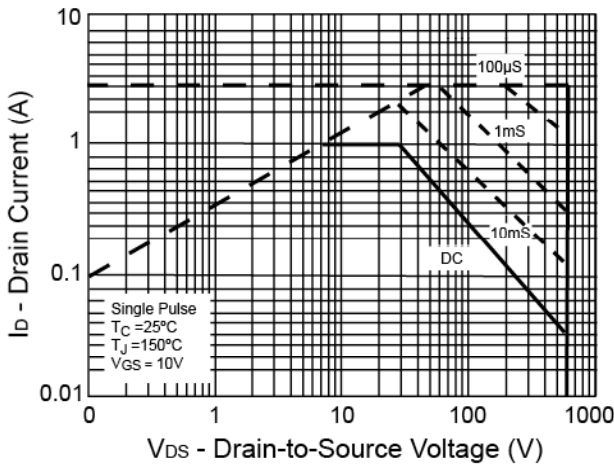
On-Resistance vs. Gate-Source Voltage



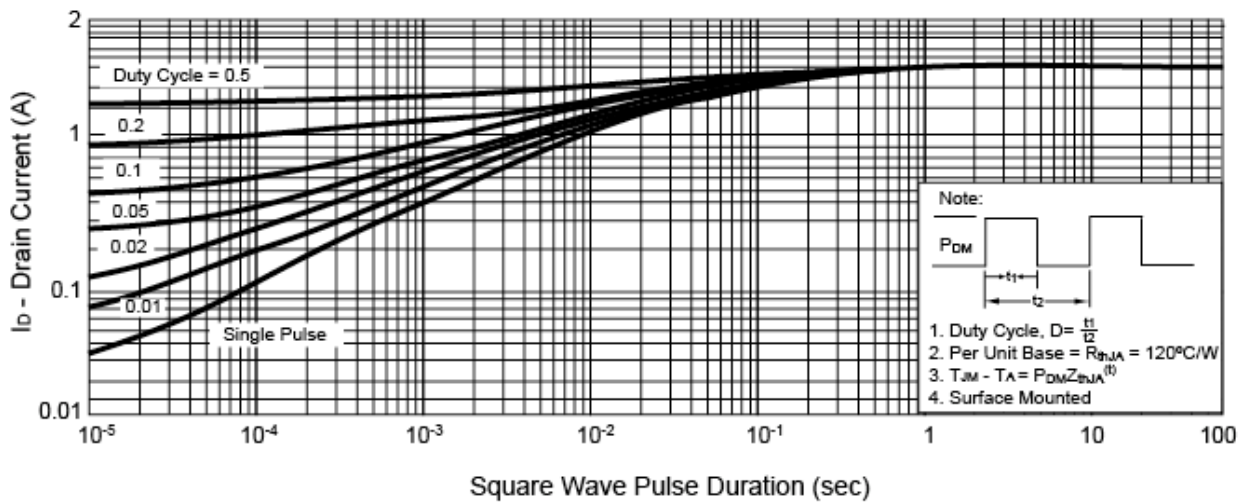
Threshold Voltage



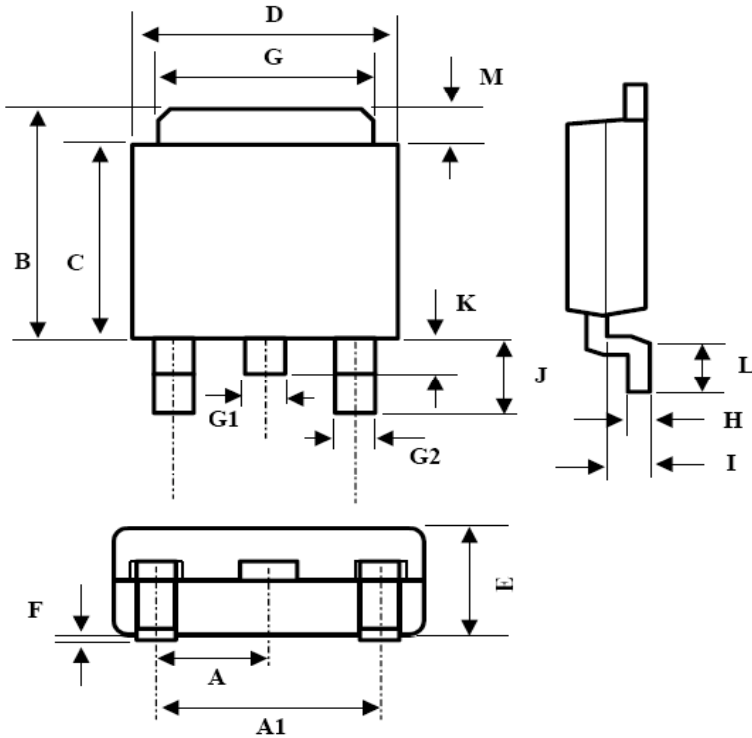
Maximum Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

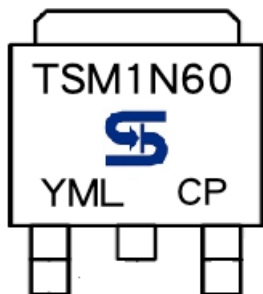


SOT-252 Mechanical Drawing



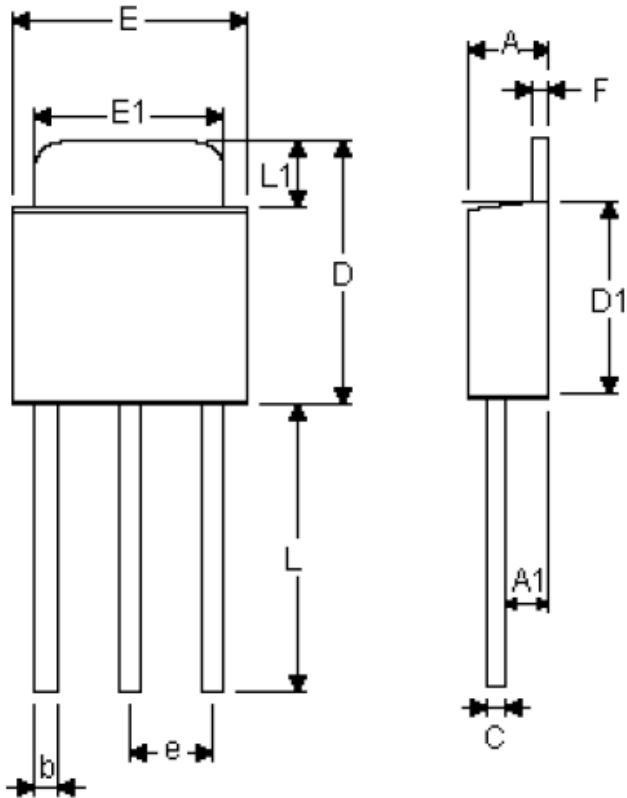
DIM	TO-252 DIMENSION			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.3BSC		0.09BSC	
A1	4.6BSC		0.18BSC	
B	6.80	7.20	0.268	0.283
C	5.40	5.60	0.213	0.220
D	6.40	6.65	0.252	0.262
E	2.20	2.40	0.087	0.094
F	0.00	0.20	0.000	0.008
G	5.20	5.40	0.205	0.213
G1	0.75	0.85	0.030	0.033
G2	0.55	0.65	0.022	0.026
H	0.35	0.65	0.014	0.026
I	0.90	1.50	0.035	0.059
J	2.20	2.80	0.087	0.110
K	0.50	1.10	0.020	0.043
L	0.90	1.50	0.035	0.059
M	1.30	1.70	0.051	0.67

Marking Diagram



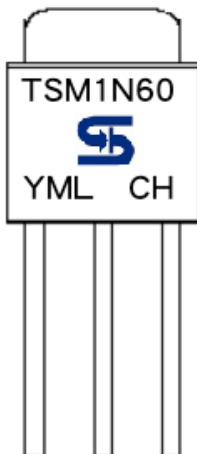
- Y = Year Code
- M = Month Code
(A=Jan, B=Feb, C=Mar, D=Apr, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)
- L = Lot Code

SOT-251 Mechanical Drawing



TO-251 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.20	2.4	0.087	0.095
A1	1.10	1.30	0.043	0.051
b	0.40	0.80	0.016	0.032
C	0.40	0.60	0.016	0.024
D	6.70	7.30	0.264	0.287
D1	5.40	5.65	0.213	0.222
E	6.40	6.65	0.252	0.262
e	2.10	2.50	0.083	0.098
F	0.40	0.60	0.016	0.024
L	7.00	8.00	0.276	0.315
L1	1.60	1.86	0.063	0.073

Marking Diagram



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