

TIP31G, TIP31AG, TIP31BG, TIP31CG (NPN), TIP32G, TIP32AG, TIP32BG, TIP32CG (PNP)

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Sustaining Voltage (Note 2) (I _C = 30 mAdc, I _B = 0) TIP31G, TIP32G TIP31AG, TIP32AG TIP31BG, TIP32BG TIP31CG, TIP32CG	V _{CEO(sus)}	40 60 80 100	- - - -	Vdc
Collector Cutoff Current (V _{CE} = 30 Vdc, I _B = 0) TIP31G, TIP32G, TIP31AG, TIP32AG (V _{CE} = 60 Vdc, I _B = 0) TIP31BG, TIP31CG, TIP32BG, TIP32CG	I _{CEO}	- -	0.3 0.3	mAdc
Collector Cutoff Current (V _{CE} = 40 Vdc, V _{EB} = 0) TIP31G, TIP32G (V _{CE} = 60 Vdc, V _{EB} = 0) TIP31AG, TIP32AG (V _{CE} = 80 Vdc, V _{EB} = 0) TIP31BG, TIP32BG (V _{CE} = 100 Vdc, V _{EB} = 0) TIP31CG, TIP32CG	I _{CES}	- - - -	200 200 200 200	μAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	-	1.0	mAdc
ON CHARACTERISTICS (Note 2)				
DC Current Gain (I _C = 1.0 Adc, V _{CE} = 4.0 Vdc) (I _C = 3.0 Adc, V _{CE} = 4.0 Vdc)	h _{FE}	25 10	- 50	-
Collector-Emitter Saturation Voltage (I _C = 3.0 Adc, I _B = 375 mAdc)	V _{CE(sat)}	-	1.2	Vdc
Base-Emitter On Voltage (I _C = 3.0 Adc, V _{CE} = 4.0 Vdc)	V _{BE(on)}	-	1.8	Vdc
DYNAMIC CHARACTERISTICS				
Current-Gain – Bandwidth Product (I _C = 500 mAdc, V _{CE} = 10 Vdc, f _{test} = 1.0 MHz)	f _T	3.0	-	MHz
Small-Signal Current Gain (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{fe}	20	-	-

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

TIP31G, TIP31AG, TIP31BG, TIP31CG (NPN), TIP32G, TIP32AG, TIP32BG, TIP32CG (PNP)

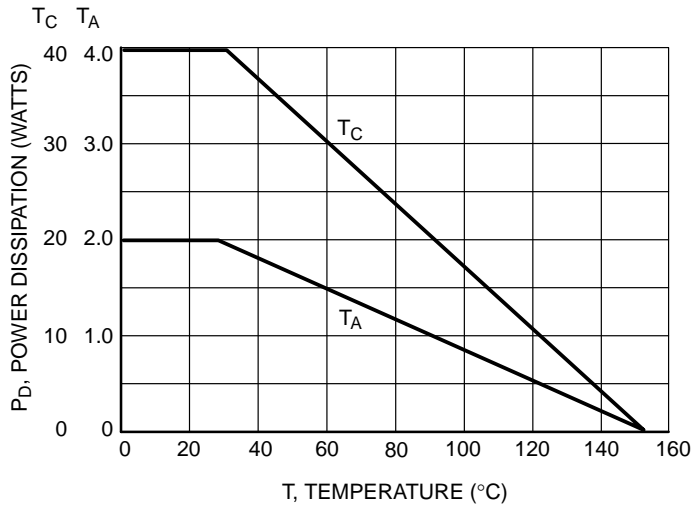
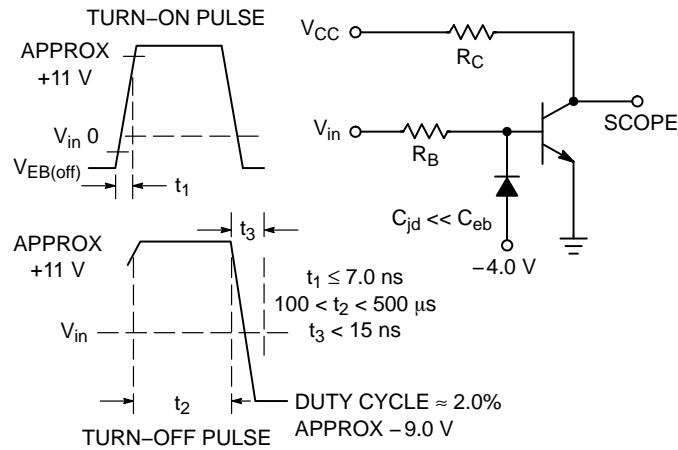


Figure 1. Power Derating



R_B and R_C VARIED TO OBTAIN DESIRED CURRENT LEVELS.

Figure 2. Switching Time Equivalent Circuit

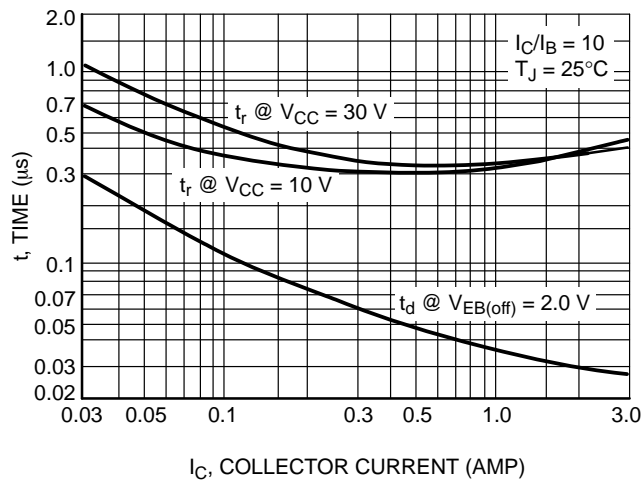


Figure 3. Turn-On Time

TIP31G, TIP31AG, TIP31BG, TIP31CG (NPN), TIP32G, TIP32AG, TIP32BG, TIP32CG (PNP)

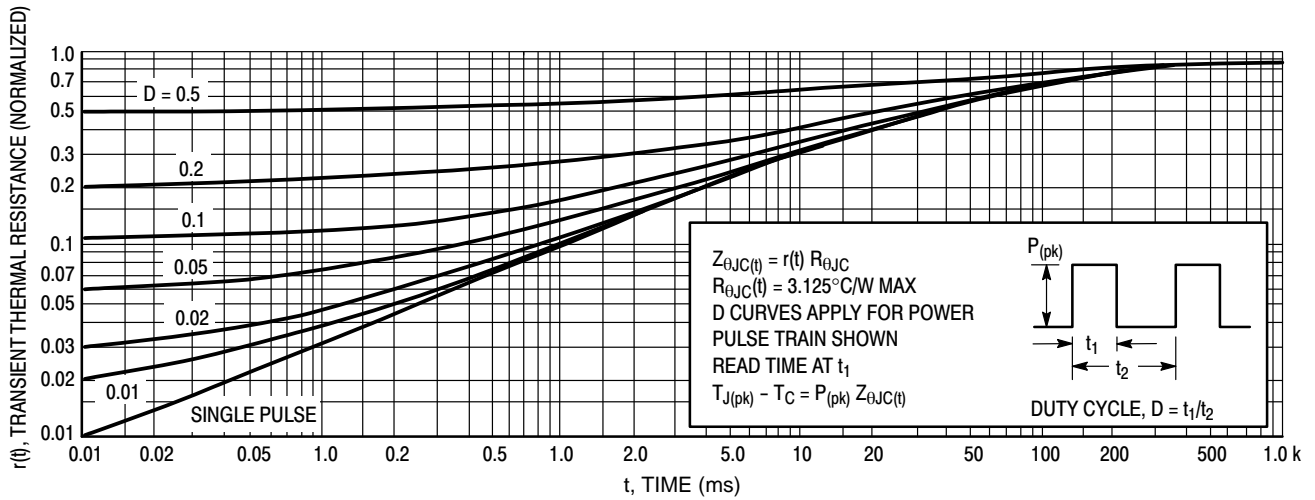


Figure 4. Thermal Response

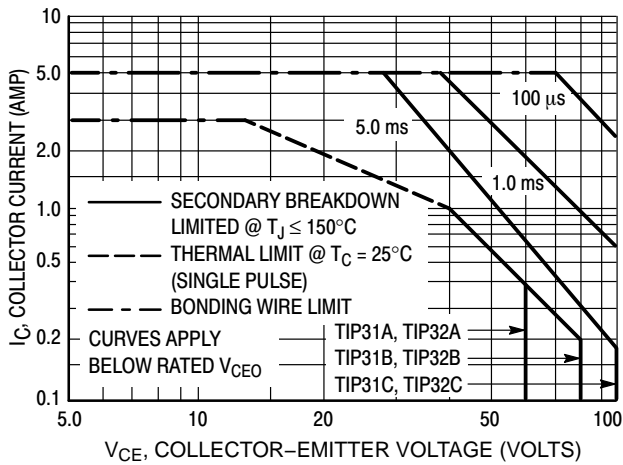


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on $T_{J(pk)} = 150^\circ\text{C}$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \leq 150^\circ\text{C}$. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

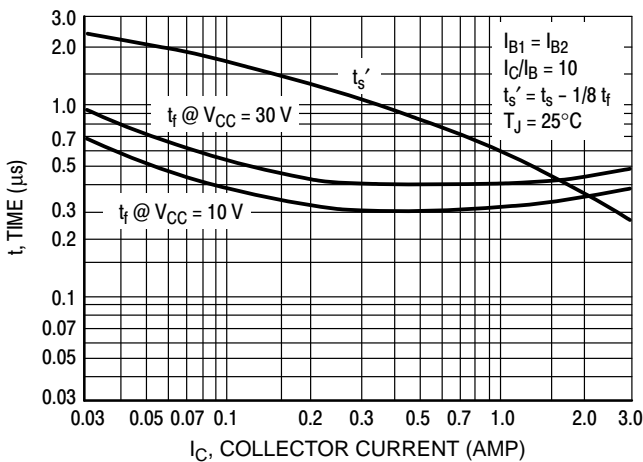


Figure 6. Turn-Off Time

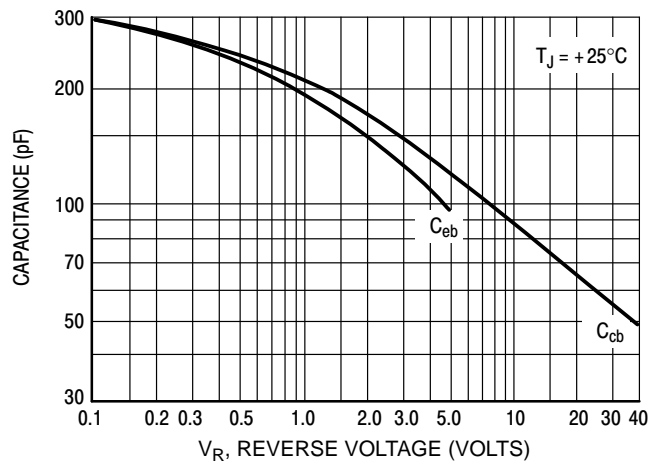


Figure 7. Capacitance

TIP31G, TIP31AG, TIP31BG, TIP31CG (NPN), TIP32G, TIP32AG, TIP32BG, TIP32CG (PNP)

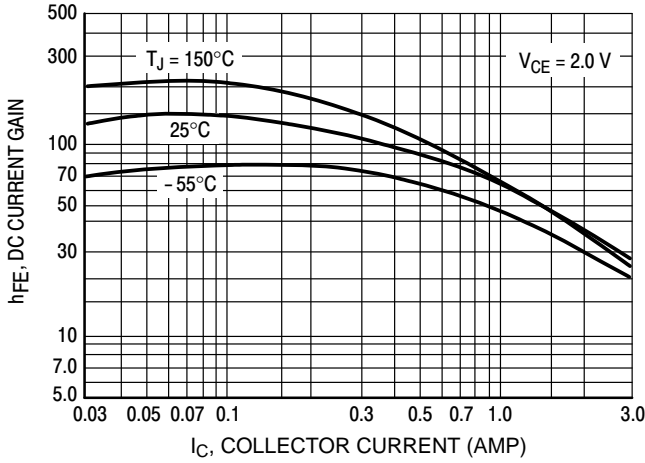


Figure 8. DC Current Gain

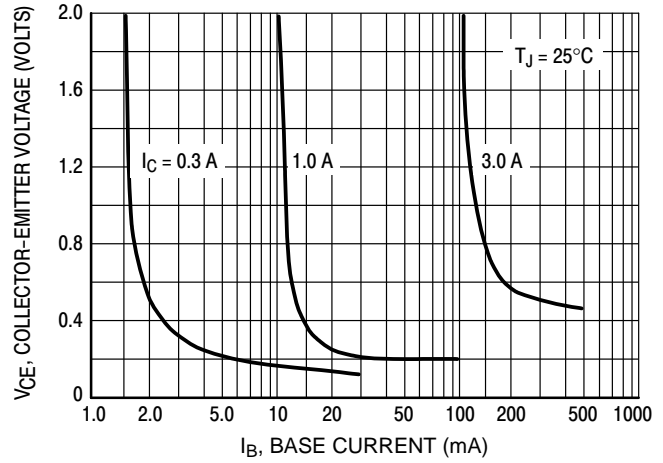


Figure 9. Collector Saturation Region

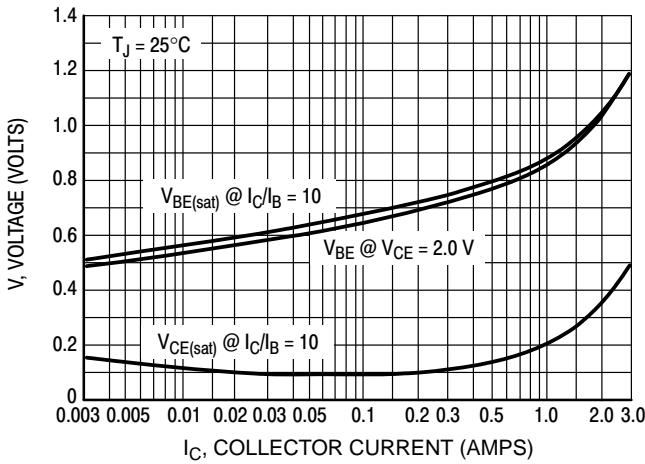


Figure 10. "On" Voltages

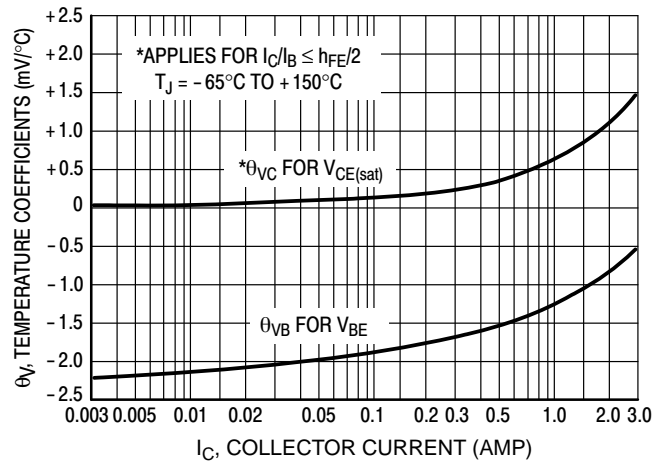


Figure 11. Temperature Coefficients

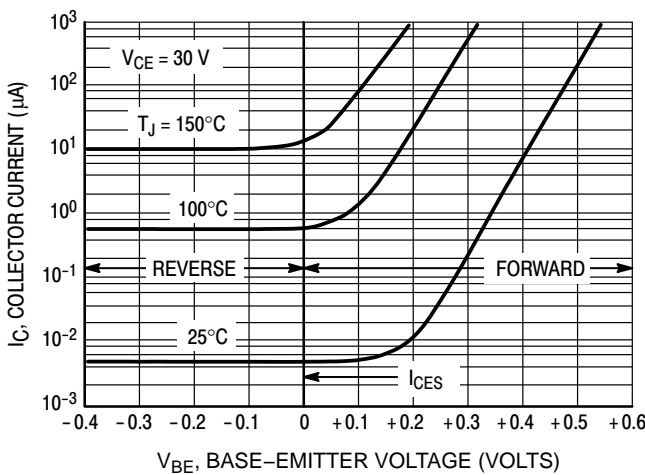


Figure 12. Collector Cut-Off Region

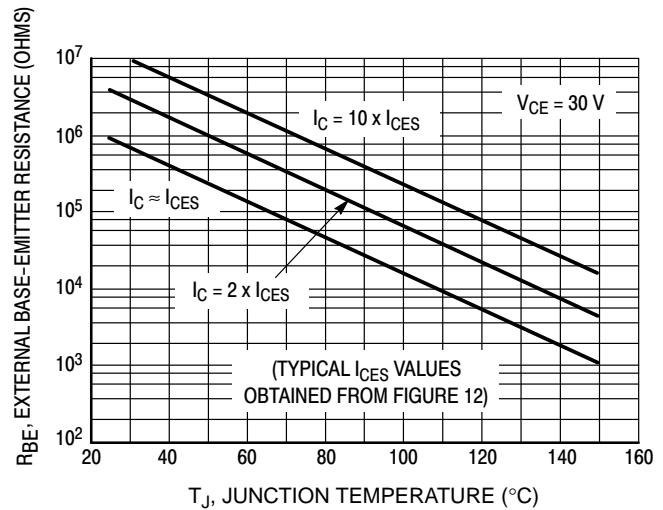


Figure 13. Effects of Base-Emitter Resistance

**TIP31G, TIP31AG, TIP31BG, TIP31CG (NPN), TIP32G, TIP32AG, TIP32BG,
TIP32CG (PNP)**

ORDERING INFORMATION

Device	Package	Shipping
TIP31G	TO-220 (Pb-Free)	50 Units / Rail
TIP31AG	TO-220 (Pb-Free)	50 Units / Rail
TIP31BG	TO-220 (Pb-Free)	50 Units / Rail
TIP31CG	TO-220 (Pb-Free)	50 Units / Rail
TIP32G	TO-220 (Pb-Free)	50 Units / Rail
TIP32AG	TO-220 (Pb-Free)	50 Units / Rail
TIP32BG	TO-220 (Pb-Free)	50 Units / Rail
TIP32CG	TO-220 (Pb-Free)	50 Units / Rail

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



SCALE 1:1



TO-220 CASE 221A ISSUE AK

DATE 13 JAN 2022

NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 2009.
2. CONTROLLING DIMENSION: INCHES
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.
4. MAX WIDTH FOR F102 DEVICE = 1.35MM

DIM	INCHES		MILLIMETERS	
	MIN.	MAX.	MIN.	MAX.
A	0.570	0.620	14.48	15.75
B	0.380	0.415	9.66	10.53
C	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.60	4.09
G	0.095	0.105	2.42	2.66
H	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.41
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

STYLE 1:
PIN 1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

STYLE 2:
PIN 1. BASE
2. EMITTER
3. COLLECTOR
4. EMITTER

STYLE 3:
PIN 1. CATHODE
2. ANODE
3. GATE
4. ANODE

STYLE 4:
PIN 1. MAIN TERMINAL 1
2. MAIN TERMINAL 2
3. GATE
4. MAIN TERMINAL 2

STYLE 5:
PIN 1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

STYLE 6:
PIN 1. ANODE
2. CATHODE
3. ANODE
4. CATHODE

STYLE 7:
PIN 1. CATHODE
2. ANODE
3. CATHODE
4. ANODE

STYLE 8:
PIN 1. CATHODE
2. ANODE
3. EXTERNAL TRIP/DELAY
4. ANODE

STYLE 9:
PIN 1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

STYLE 10:
PIN 1. GATE
2. SOURCE
3. DRAIN
4. SOURCE

STYLE 11:
PIN 1. DRAIN
2. SOURCE
3. GATE
4. SOURCE

STYLE 12:
PIN 1. MAIN TERMINAL 1
2. MAIN TERMINAL 2
3. GATE
4. NOT CONNECTED

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