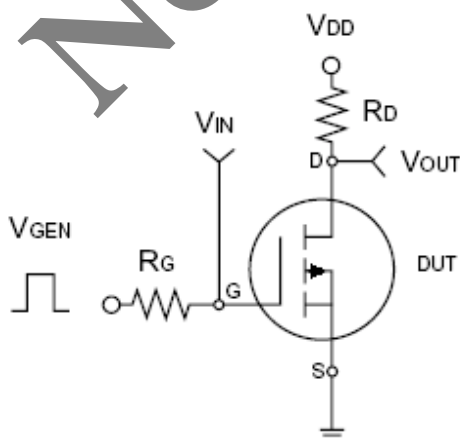


### Electrical Specifications (Ta = 25°C unless otherwise noted)

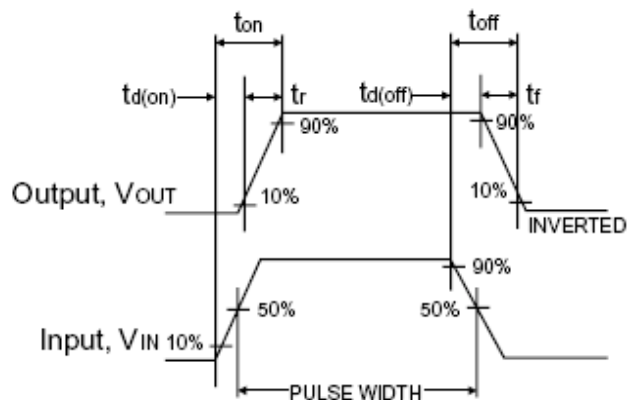
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	$BV_{DSS}$	30	--	--	V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	1	1.8	3	V
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	$I_{GSS}$	--	--	$\pm 100$	nA
Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V$	$I_{DSS}$	--	--	1.0	$\mu A$
On-State Drain Current <sup>a</sup>	$V_{DS} \geq 5V, V_{GS} = 10V$	$I_{D(ON)}$	40	--	--	A
Drain-Source On-State Resistance <sup>a</sup>	$V_{GS} = 10V, I_D = 15A$	$R_{DS(ON)}$	--	6.2	7.5	m $\Omega$
	$V_{GS} = 4.5V, I_D = 13A$		--	8.3	10	
Forward Transconductance <sup>a</sup>	$V_{DS} = 15V, I_D = 15A$	$g_{fs}$	--	42	--	S
Diode Forward Voltage	$I_S = 2.7A, V_{GS} = 0V$	$V_{SD}$	--	0.72	1.1	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$V_{DS} = 15V, I_D = 15A,$ $V_{GS} = 5V$	$Q_g$	--	26	--	nC
Gate-Source Charge		$Q_{gs}$	--	6	--	
Gate-Drain Charge		$Q_{gd}$	--	5	--	
Input Capacitance	$V_{DS} = 15V, V_{GS} = 0V,$ $f = 1.0MHz$	$C_{iss}$	--	2134	--	pF
Output Capacitance		$C_{oss}$	--	343	--	
Reverse Transfer Capacitance		$C_{rss}$	--	134	--	
<b>Switching<sup>c</sup></b>						
Turn-On Delay Time	$V_{DD} = 15V, R_L = 1.0\Omega,$ $I_D = 1A, V_{GEN} = 10V,$ $R_G = 3\Omega$	$t_{d(on)}$	--	17	--	nS
Turn-On Rise Time		$t_r$	--	3.5	--	
Turn-Off Delay Time		$t_{d(off)}$	--	40	--	
Turn-Off Fall Time		$t_f$	--	6	--	

**Notes:**

- a. pulse test: PW  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
- b. For DESIGN AID ONLY, not subject to production testing.
- b. Switching time is essentially independent of operating temperature.



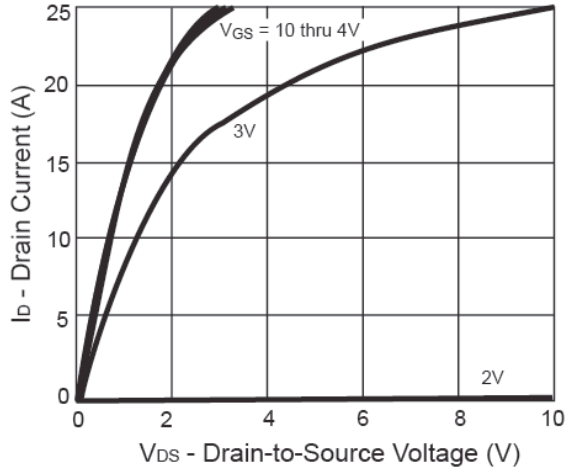
**Switching Test Circuit**



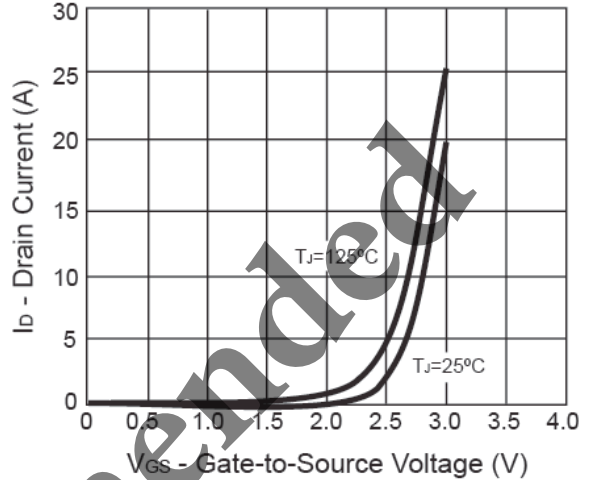
**Switchin Waveforms**

**Electrical Characteristics Curve** (Ta = 25°C, unless otherwise noted)

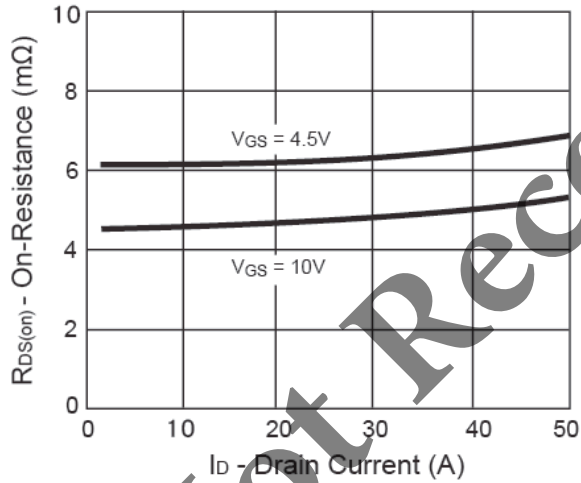
**Output Characteristics**



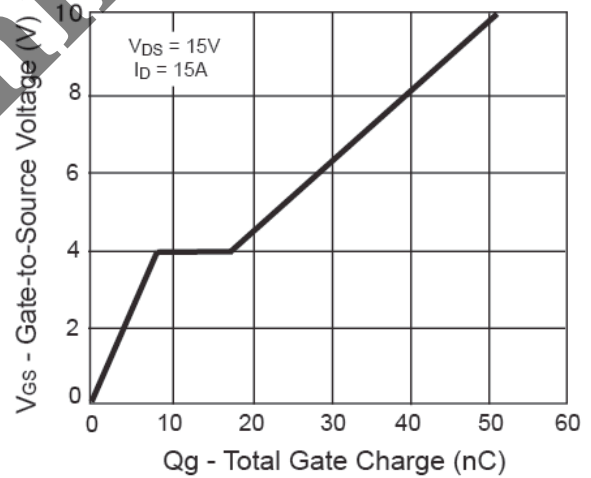
**Transfer Characteristics**



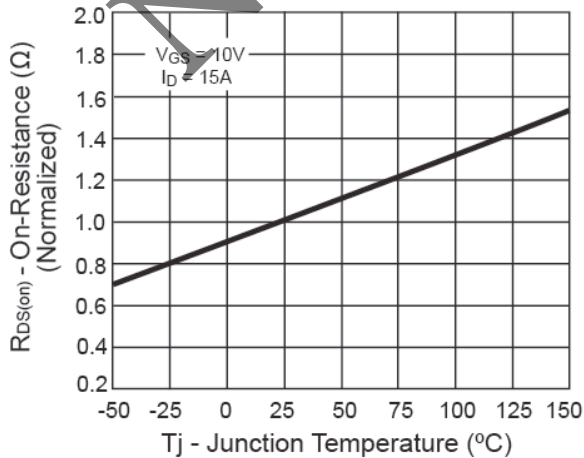
**On-Resistance vs. Drain Current**



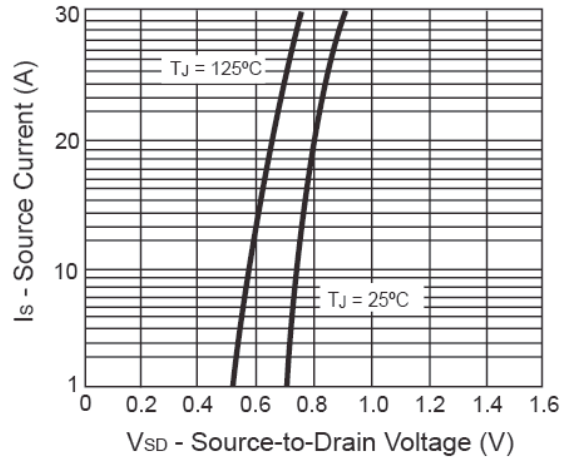
**Gate Charge**



**On-Resistance vs. Junction Temperature**

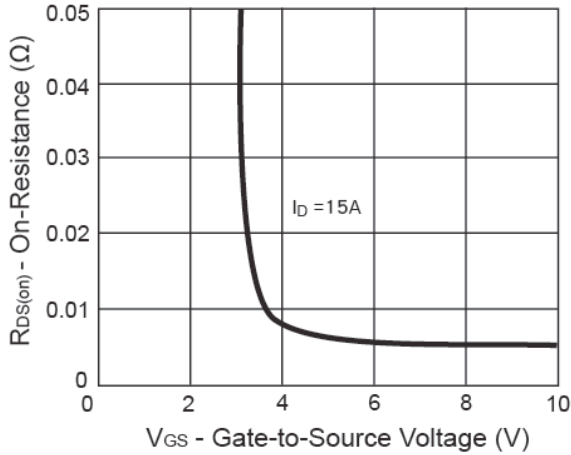


**Source-Drain Diode Forward Voltage**

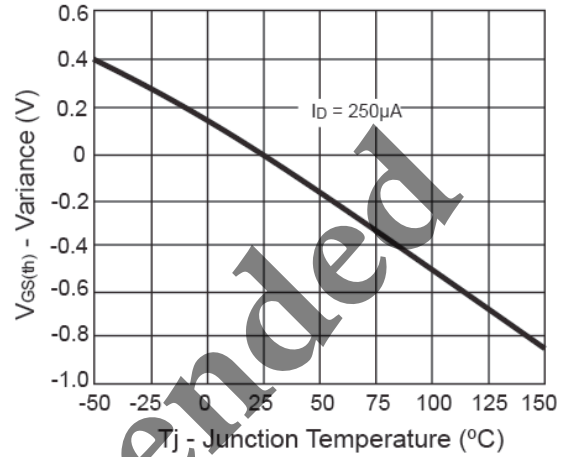


**Electrical Characteristics Curve** (Ta = 25°C, unless otherwise noted)

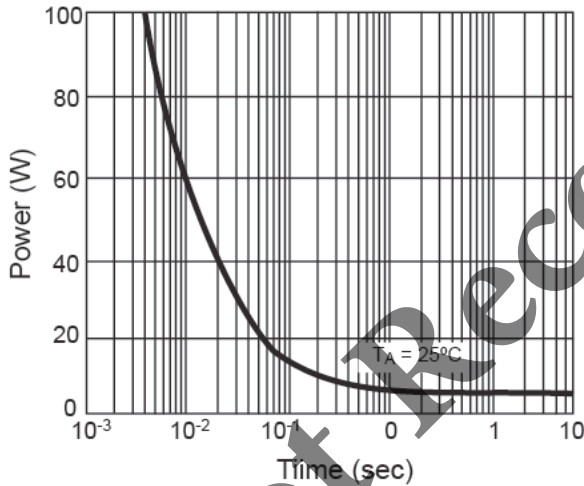
**On-Resistance vs. Gate-Source Voltage**



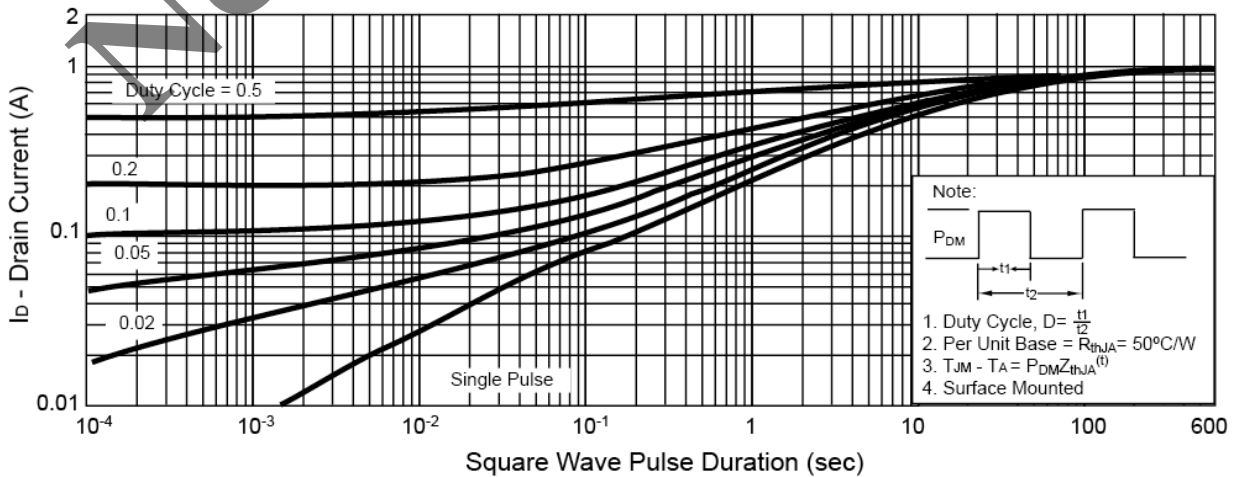
**Threshold Voltage**



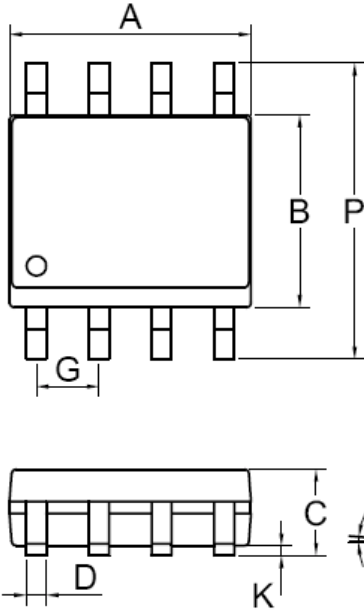
**Single Pulse Power**



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

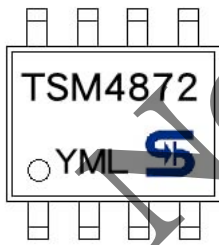


**SOP-8 Mechanical Drawing**



SOP-8 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX.
A	4.80	5.00	0.189	0.196
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27BSC		0.05BSC	
K	0.10	0.25	0.004	0.009
M	0° - 7°		0° - 7°	
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

**Marking Diagram**



- Y** = Year Code
- M** = Month Code  
(A=Jan, B=Feb, C=Mar, D=Apr, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)
- L** = Lot Code

**Not Recommended**

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