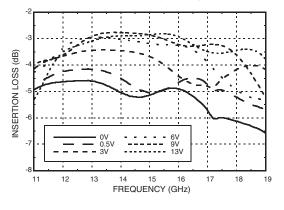


12 - 18 GHz

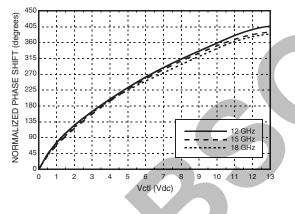
v02.0311



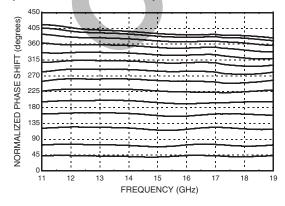
Insertion Loss vs. Frequency



Phase Shift vs. Vctl



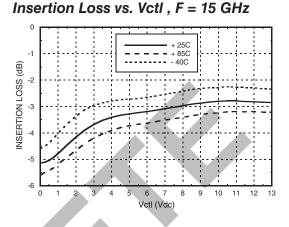
Phase Shift vs. Frequency (Relative to Vctl = 0V) Vctl = 0.5 to 13V



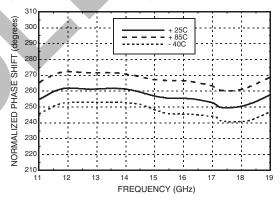
^{[1] 0} to 10.5V provides 0 - 360 degrees phase shift range

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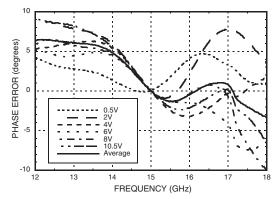
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Phase Shift vs. Frequency @ Vctl = 6V (Relative to Vctl = 0V)



Phase Error vs. Frequency, Fmean = 15 GHz ^[1]



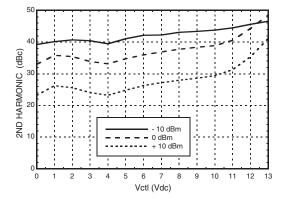


12 - 18 GHz

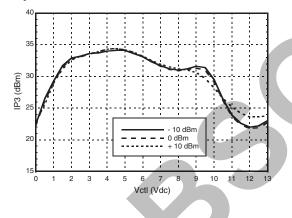
v02.0311



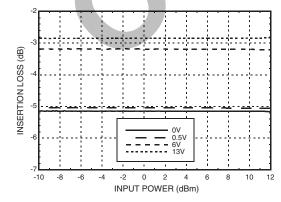
Second Harmonics vs. Vctl, F = 15 GHz

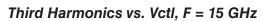


Input IP3 vs. Vctl, F = 15 GHz

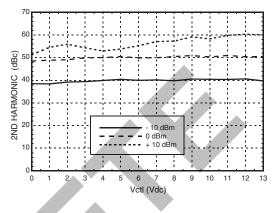


Insertion Loss vs. Pin @ 15 GHz

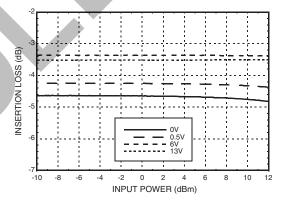




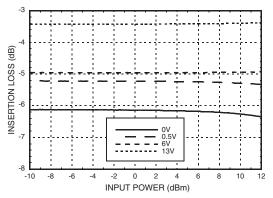
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Insertion Loss vs. Pin @ 12 GHz



Insertion Loss vs. Pin @ 18 GHz



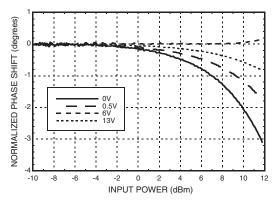
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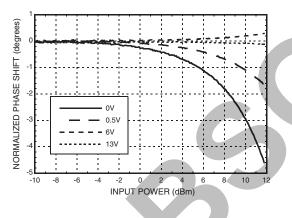
v02.0311



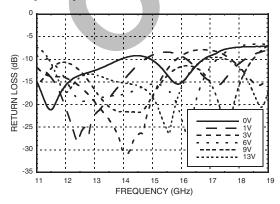
Phase Shift vs. Pin @ 12 GHz



Phase Shift vs. Pin @ 18 GHz

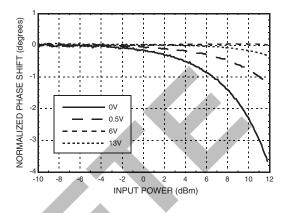


Output Return Loss vs. Frequency, Vctl = 0 to +13V

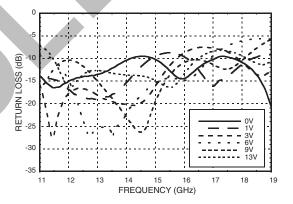




Phase Shift vs. Pin @ 15 GHz



Input Return Loss vs. Frequency, Vctl = 0 to +13V



Reliability Information

Junction Temperature (Tj)		150 °C
Nominal Junction Ter (T = 85 °C, Pin = 10 $^{\circ}$	and the second	87 °C
Thermal Resistance (Junction to GND Pa	ddle)	80 °C/W
Operating Temperatu	ire	-40 to +85 °C

Absolute Maximum Ratings

Input Power (RFIN)	+26 dBm	
Control Voltage (Vctl)	-0.5V to +15V	
Storage Temperature	-65 to +150 °C	
ESD Sensitivity (HBM)	Class 1B	



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

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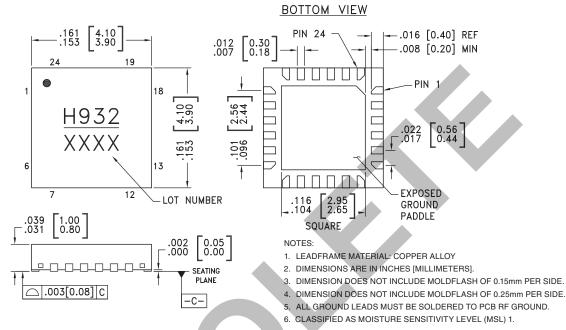
12 - 18 GHz

390° ANALOG PHASE SHIFTER,

v02.0311



Outline Drawing



Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[1]
HMC932LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>H932</u> XXXX

[1] 4-Digit lot number XXXX

[2] Max peak reflow temperature of 260 °C

Pin Descriptions

Pin Number	Function	Description	Interface Schematic	
1, 5 - 14, 18 - 20, 22 - 24	N/C	No connection required. These pins may be connected to RF/DC ground without affecting performance.		
2, 4, 15, 17	GND	Ground: Backside of package has exposed metal ground slug that must be connected to ground thru a short path. Vias under the device are required.		
3	RFIN	Port is DC blocked.		
16	RFOUT	Port is DC blocked.		
21	Vctl	Phase shift control pin. Application of a voltage between 0 and 13 volts causes the transmission phase to change. The DC equivalent circuit is a series connected diode and resistor.	on phase to change. The DC equivalent $17pF$	

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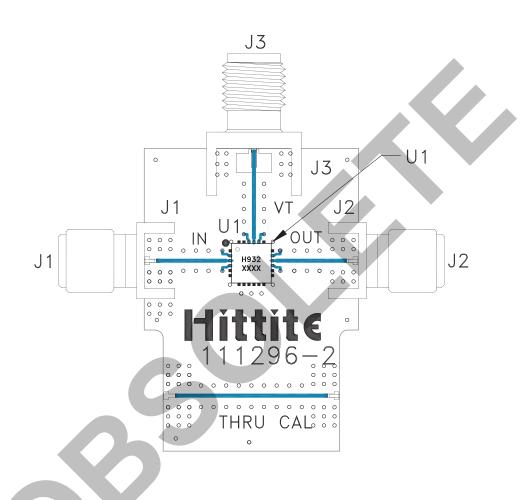


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390° ANALOG PHASE SHIFTER, 12 - 18 GHz



Evaluation PCB



List of Materials for Evaluation PCB 108812 [1]

Item	Description	
J1, J2	PCB Mount SMA Connector, SRI	
J3	PCB Mount SMA Connector	
U1	HMC932LP4E Analog Phase Shifter	
PCB [2]	111296 Evaluation PCB	

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

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