Features

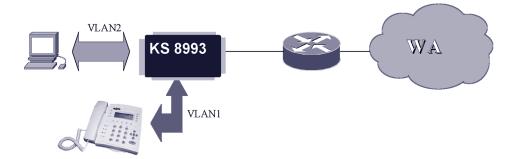
- World's first 3-port 10/100 integrated switch with physical layer transceivers
- 64kB SRAM on chip for frame buffering
- 1.0Gbps high performance memory bandwidth
- Low power dissipation: 300mA (0.75W) including physical transmit drivers
- Supports port-based VLAN
- Supports 802.1p-based priority or port-based priority.
- Support for UTP or fiber installations
- Indicators for link, activity, full/half-duplex and speed
- Unmanaged operation via strapping at system reset time
- Hardware-based 10/100, full/half, flow control and autonegotiation
- Individual port-forced modes (full-duplex, 100BaseTX) when auto-negotiation is disabled
- On-chip integrated address look-up engine, supports 1K absolute MAC addresses

- Automatic address learning, address aging and address migration
- Full-duplex IEEE 802.3x flow control
- Half-duplex back pressure flow control
- Optional external MAC interface (MII or SNI) for router applications
- 10BaseT, 100BaseTX and 100BaseFX modes of operation
- Single 2.5V power supply
- Available in 128-pin PQFP package

System Level Applications

The KS8993 can be configured to fit either in a three port 10/100 application or as a two port 10/100 network interface with an extra MII port. This MII port can be connected to an external processor and used for routing purposes or public network access. The major benefits of using the KS8993 are the lower power consumption, unmanaged operation, flexible configuration and built in frame buffering.

Typical Enterprise IP phone application using KS8993 switch to connect one port to phone and other port to PC.



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