Table 1. PS088-315 Signal Descriptions

Pin #	Name	Description	Pin #	Name	Description
1	GND	Ground. Must be connected to PCB ground using lowest possible inductance path.	5	GND	Ground. Must be connected to PCB ground using lowest possible inductance path.
2	V_CONTROL	Control voltage input (voltage applied is nominally equal to the voltage applied to pin 4)	6	J1	RF input/output
3	GND	Ground. Must be connected to PCB ground using lowest possible inductance path.	7	GND	Ground. Must be connected to PCB ground using lowest possible inductance path.
4	V_CONTROL	Control voltage input (voltage applied is nominally equal to the voltage applied to pin 2)	8	J2	RF output/input

Table 2. PS088-315 Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
RF input power	P_{IN}		+20	dBm
Control voltage	$V_{CONTROL}$		15	V
Storage temperature	T_{STG}	–65	+150	°C
Operating temperature	T_{OP}	–40	+85	°C

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

***CAUTION**: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times. The PS088-315 Phase Shifter is a Class 1B Human Body Model (HBM) ESD device.*

Electrical and Mechanical Specifications

The absolute maximum ratings of the PS088-315 are provided in Table 2. Electrical specifications are provided in Table 3.

Performance characteristics for the PS088-315 are illustrated in Figures 3 through 8.

Table 3. PS088-315 Electrical Specifications (Note 1)
(Characteristic Impedance [Z_0] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Phase shift		V_CONTROL = 12 V from V_CONTROL = 0 V, f = 849 to 869 MHz	85	100		deg
Control voltage range	$V_{CONTROL}$		0		12	V
Control current	$I_{CONTROL}$	$V_{CONTROL}$ = 12 V			1	µA
Insertion loss in bandwidth	IL	$V_{CONTROL}$ = 0 to 12 V, 849 to 869 MHz			2.8	dB
Insertion loss deviation in bandwidth	$IL_{DEVIATION}$	$V_{CONTROL}$ = 0 to 12 V, 849 to 869 MHz			1.8	dB
Return loss in bandwidth	RL	$V_{CONTROL}$ = 0 to 12 V, 849 to 869 MHz	10			dB
3rd Order Intermodulation	IM3	P_{IN} = +8 dBm, $V_{CONTROL}$ = 0 V, @ 900 MHz and 905 MHz			–50	dBc
3rd Order Intercept Point	IP3	Derived from IM3	+33			dBm

Note 1: Performance is guaranteed only under the conditions listed in this Table.

Typical Performance Characteristics

(Characteristic Impedance [Z_0] = 50 Ω, Unless Otherwise Noted)

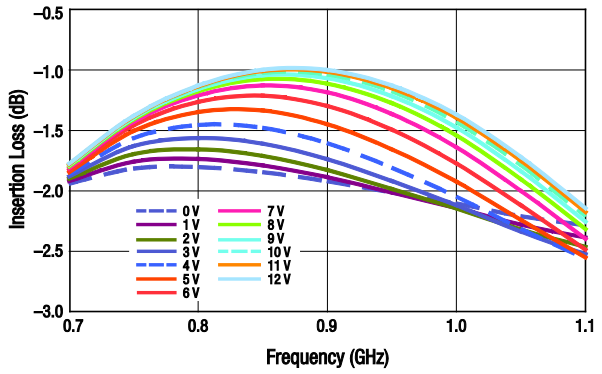


Figure 3. Insertion Loss vs Frequency Over Control Voltage

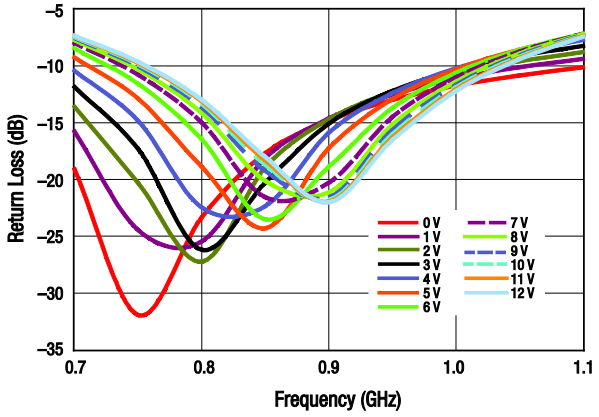


Figure 4. Input Return Loss vs Frequency Over Control Voltage

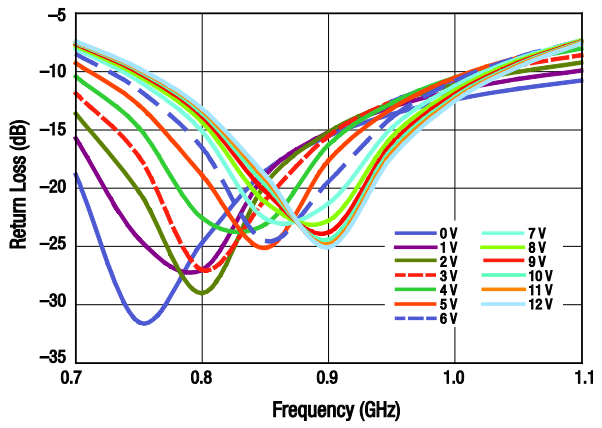


Figure 5. Output Return Loss vs Frequency Over Control Voltage

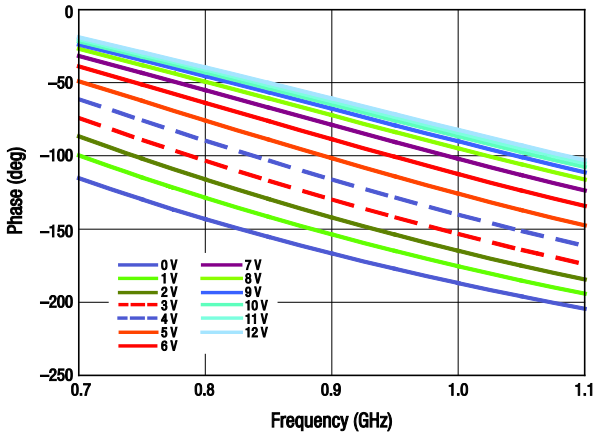


Figure 6. Insertion Phase vs Frequency Over Control Voltage

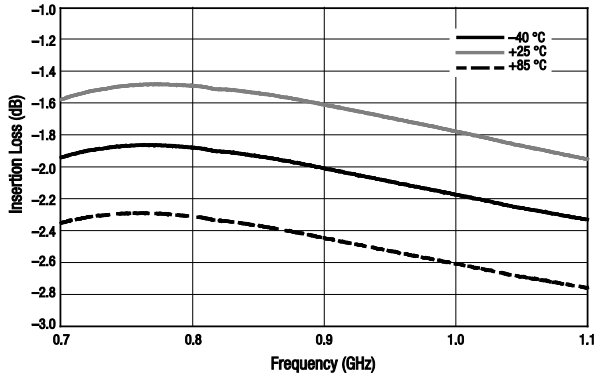


Figure 7. Insertion Loss vs Frequency Over Temperature (V_CONTROL = 0 V)

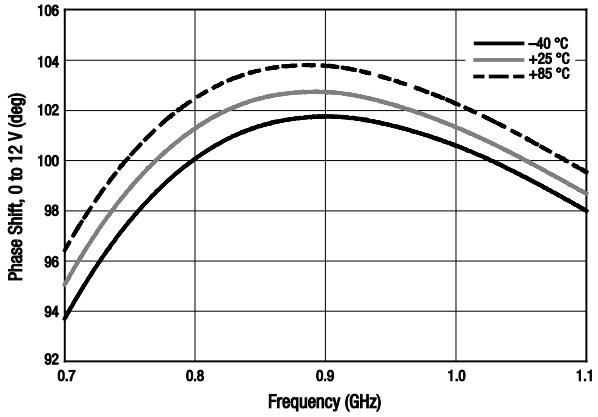


Figure 8. Phase Shift Delta vs Frequency Over Temperature

Evaluation Board Description

The PS088-315 Evaluation Board is used to test the performance of the PS088-315 voltage controlled phase shifter. An assembly drawing for the Evaluation Board is shown in Figure 9. The Evaluation Board layer detail characteristics are shown in Figure 10.

The phase shift level of the PS088-315 is controlled by applying 0 to 12 V to the V_CONTROL pin.

Package Dimensions

The PCB layout footprint for the PS088-315 is shown in Figure 11. Typical case markings are shown in Figure 12. Package dimensions for the 8-pin MCM are shown in Figure 13, and tape and reel dimensions are provided in Figure 14.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

THE PS088-315 is rated to Moisture Sensitivity Level 3 (MSL3) at 250 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *PCB Design & SMT Assembly/Rework Guidelines for MCM-L Packages*, document number 101752.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

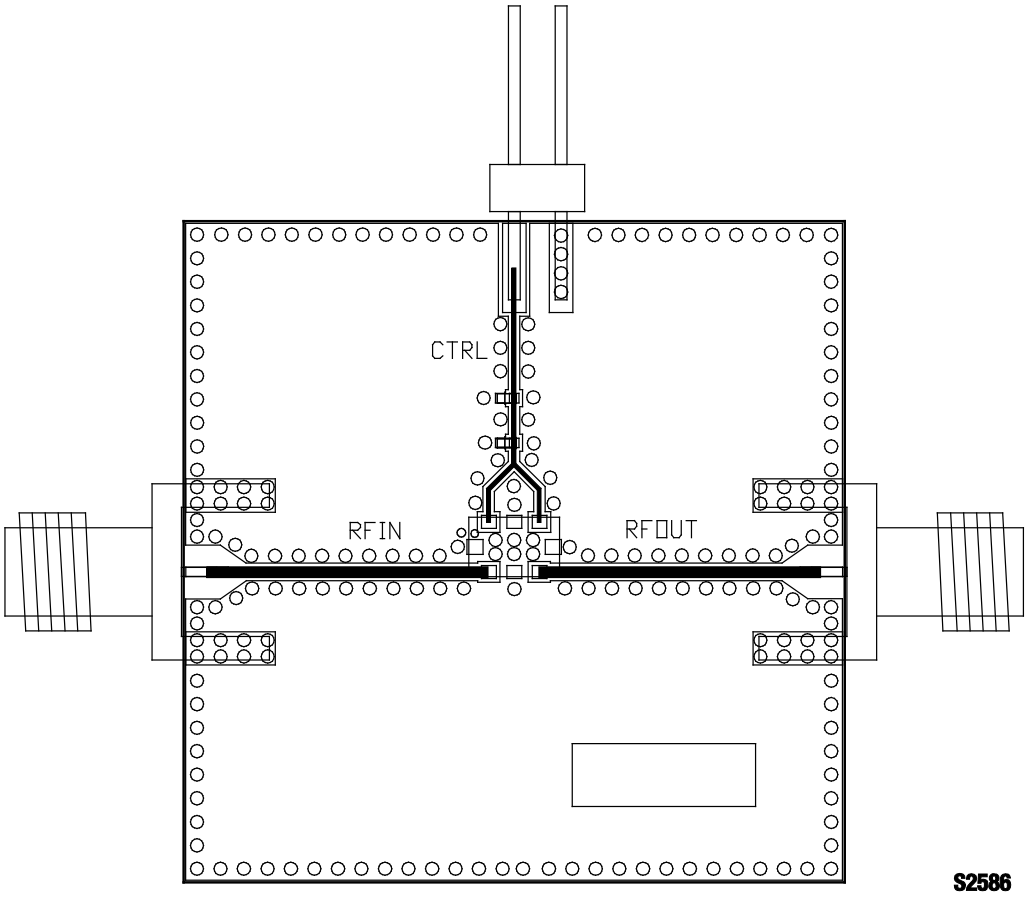


Figure 9. PS088-315 Evaluation Board Assembly Diagram

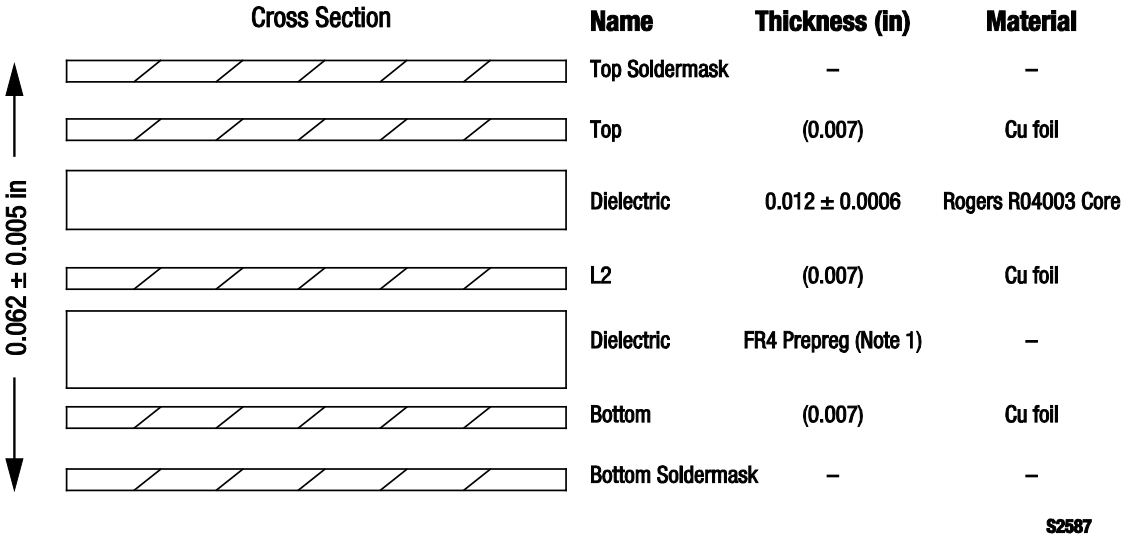


Figure 10. Layer Detail Physical Characteristics

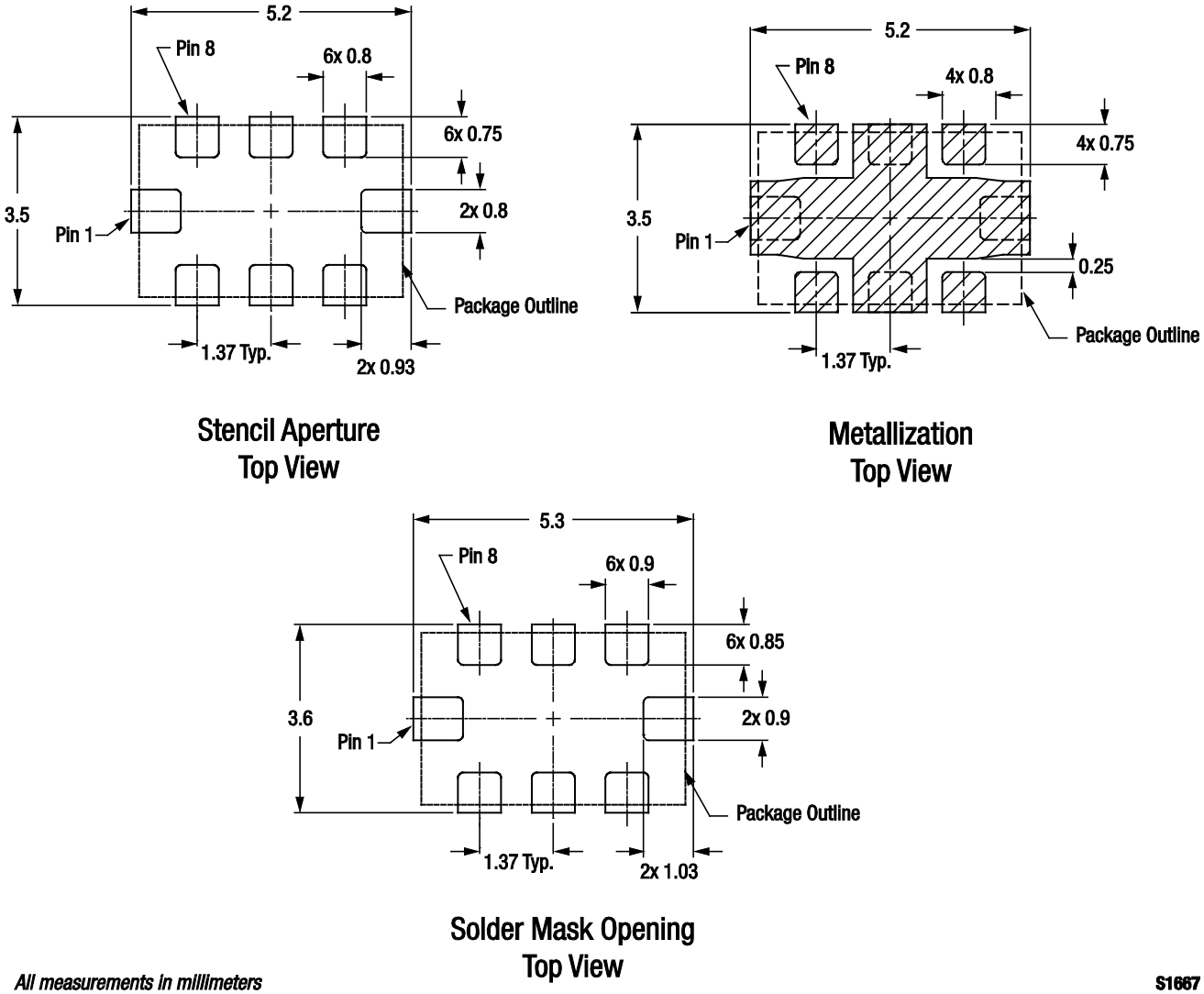


Figure 11. PS088-315 PCB Layout Footprint

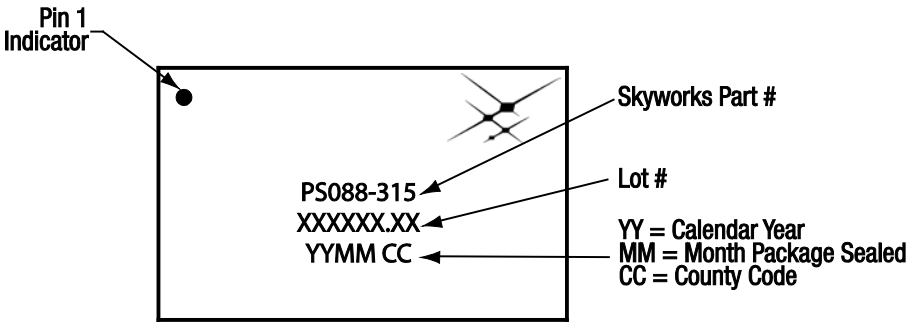
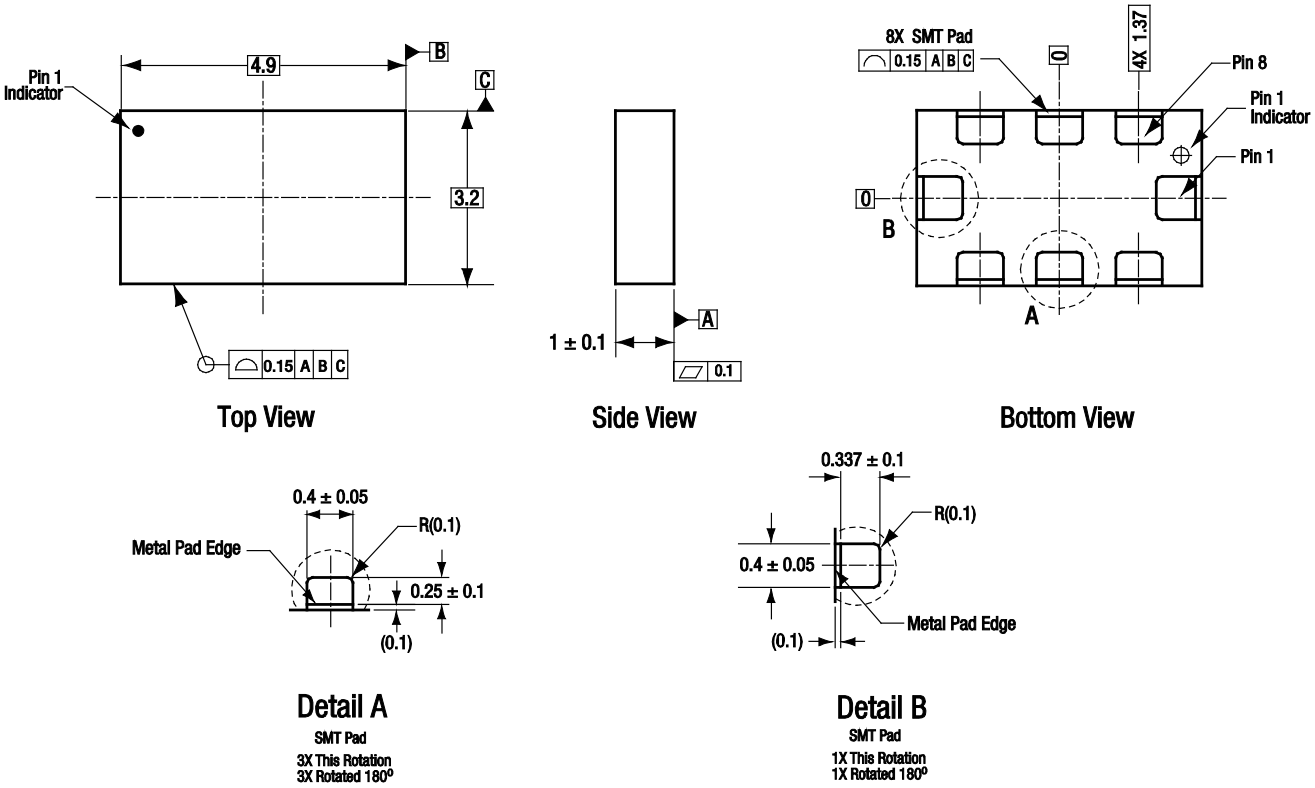


Figure 12. Typical Part Markings (Top View)



All measurements are in millimeters.

Dimensioning and tolerancing according to ASME Y14.5M-1994.

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Figure 13. PS088-315 8-Pin MCM Package Dimensions

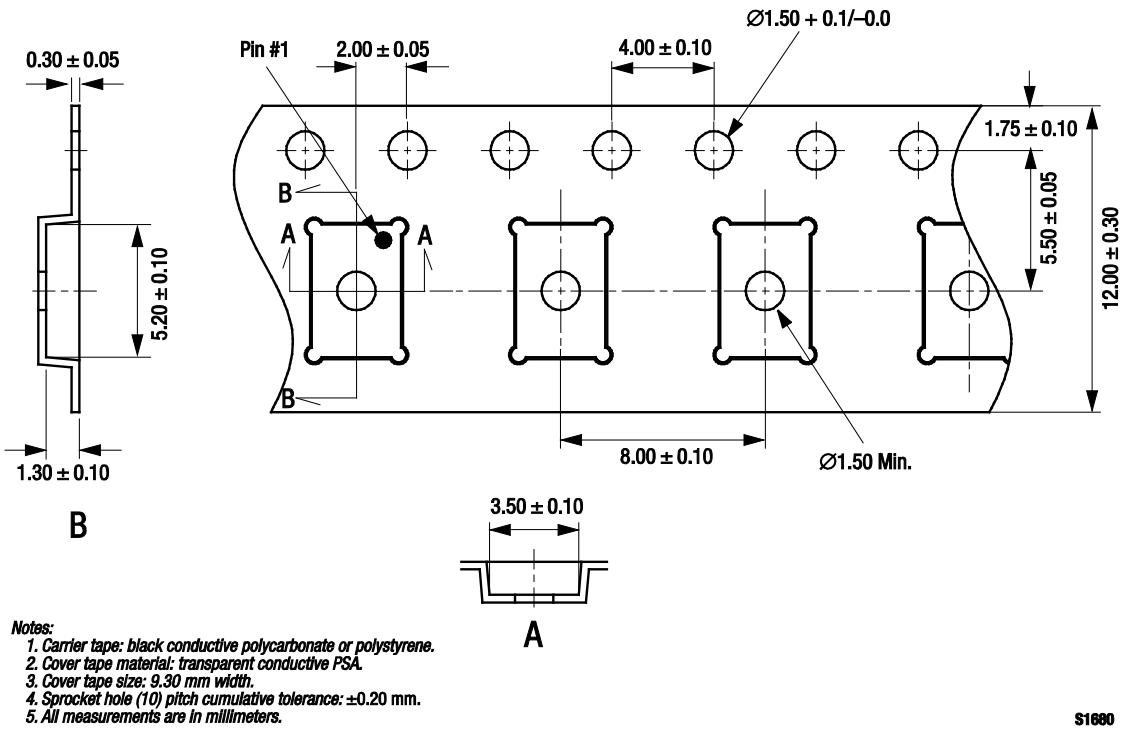

Figure 14. PS088-315 Tape and Reel Dimensions

Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
PS088-315 Voltage Controlled Phase Shifter	PS088-315	PS088-315-EVB

Mouser Electronics

Authorized Distributor

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