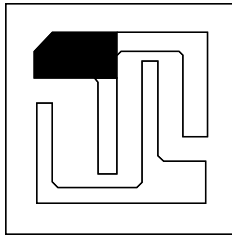
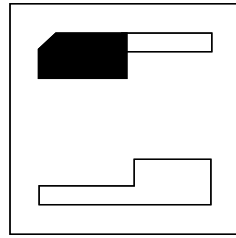
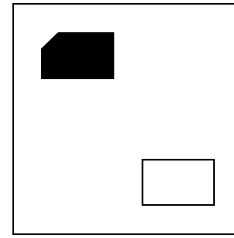
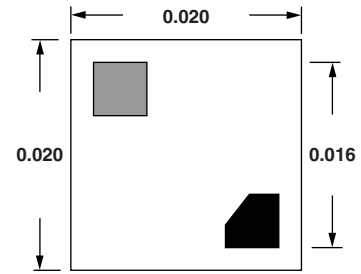
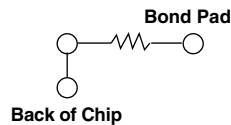


**DIMENSIONS** in inches

**TYPICAL RANGE**  
10 Ω to 29 Ω

**TYPICAL RANGE**  
30 Ω to 81 Ω

**TYPICAL RANGE**  
82 Ω to 819 Ω

**TYPICAL RANGE**  
820 Ω to 1 MΩ

**Note**

- Notched shaded area represents top bonding pad. The backside of the chip constitutes the second resistor connection

**SCHEMATIC**


MECHANICAL SPECIFICATIONS	
PARAMETER	VALUE
Chip size	0.020" x 0.020" ± 0.002" (0.50 mm x 0.50 mm ± 0.05 mm)
Chip thickness	0.010" ± 0.002" (0.254 mm ± 0.05 mm)
Chip substrate material	Oxidized silicon, 10 kÅ minimum SiO <sub>2</sub>
Resistor material	Tantalum nitride, self-passivating
Bonding pad size	0.004" x 0.004" (0.100 mm x 0.100 mm)
Number of pads	1
Pad material	10 kÅ minimum aluminum (15 kÅ minimum gold optional)
Backing	3 kÅ minimum gold
Recommended attachment method	Eutectic or conductive epoxy

GLOBAL PART NUMBER INFORMATION													
Global Part Number: BCR50000FKAHWS													
Global Part Number Description: BCR 5K 1 %, 100 ppm/°C, Al term., Class H, WS													
B	C	R	5	0	0	0	0	F	K	A	H	W	S
MODEL	RESISTANCE	RESISTANCE MULTIPLIER CODE	TOLERANCE CODE	TCR (ppm/°C)	TERMINATION	VISUAL CLASS (per MIL-STD-883, method 2032)	PACKAGING						
<b>BCR</b> 20 x 20 size back contact resistor	First 4 digits are significant figures of resistance	<b>B</b> = 0.01 <b>A</b> = 0.1 <b>0</b> = 1 <b>1</b> = 10 <b>2</b> = 100 <b>3</b> = 1000	<b>B</b> = 0.1 % <b>C</b> = 0.25 % <b>D</b> = 0.5 % <b>F</b> = 1.0 % <b>G</b> = 2.0 % <b>J</b> = 5.0 % <b>K</b> = 10 %	<b>E</b> = ± 25 <b>C</b> = ± 50 <b>K</b> = ± 100 <b>M</b> = ± 250 <b>R</b> = 0 / - 250	<b>G</b> = gold <b>A</b> = aluminum	<b>H</b> = class H <b>K</b> = class K	<b>WS</b> = waffle pack 100 min./mult.						



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