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Absolute Maximum Ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V _{DD} (see Note 1)	6 V
Output current, I _O	±10 mA
Duration of short-circuit current at (or below) 25°C (see Note 2)	5s
Operating free-air temperature range, T _A	–25°C to 85°C
Storage temperature range, T _{stg}	–25°C to 85°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds (S Package)	
Reflow solder, in accordance with J-STD-020C or J-STD-020D (SM Package)	260°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltages are with respect to GND.

2. Output may be shorted to supply.

Recommended Operating Conditions

	MIN	NOM MAX	UNIT
Supply voltage, V _{DD}	2.7	5.5	V
Operating free-air temperature, T _A	0	70	°C

Electrical Characteristics at V_{DD} = 5 V, T_A = 25°C, λ p = 635 nm, R_L = 10 k Ω (unless otherwise noted) (see Notes 3, 4, and 5)

		TEST	TSL253R			UNIT	
PARAMETER		CONDITIONS	MIN	MIN TYP MAX			
V_{D}	Dark voltage	E _e = 0	0	5	10	mV	
V_{OM}	Maximum output voltage	V _{DD} = 4.5 V	3.0	3.3		V	
Vo	Output voltage	$E_e = 14.6 \ \mu W/cm^2$	1.5	2	2.5	V	
	Temperature coefficient of output voltage (V_O) $V_O = 2 V @ 25^{\circ}C,$ $T_A = 0^{\circ}C to 70^{\circ}C (see Note 6)$		2		mV/°C		
α_{VO}		$T_A = 0^{\circ}C$ to $70^{\circ}C$ (see Note 6)		0.1		%/°C	
R _e	Irradiance responsivity	See Notes 5 and 7		137		mV/(μW/cm²)	
I _{DD}	Supply current	$E_e = 14.6 \ \mu W/cm^2$		1.1	1.7	mA	

NOTES: 3. Measurements are made with $R_L = 10 \text{ k}\Omega$ between output and ground.

4. Optical measurements are made using small-angle incident radiation from an LED optical source.

5. The input irradiance E_e is supplied by an AlInGaP LED with peak wavelength λ_p = 635 nm

6. The temperature coefficient of output voltage measurement is made by adjusting irradiance such that V_0 is approximately 2 V at 25°C and then with irradiance held constant, measuring V_0 while varying the temperature between 0°C and 70°C.

7. Irradiance responsivity is characterized over the range $V_O = 0.05$ to 2.9 V. The best-fit straight line of Output Voltage V_O versus irradiance E_e over this range will typically have a positive extrapolated V_O value for $E_e = 0$.

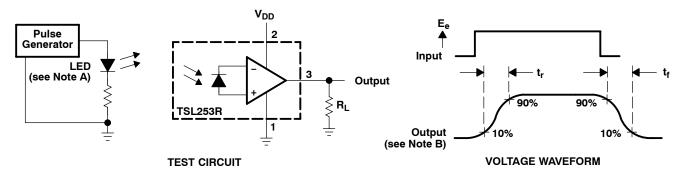
Dynamic Characteristics at V_{DD} = 5 V, T_A = 25°C, λp = 635 nm, R_L = 10 k Ω (see Figure 1)

DADAWETED		TEAT CONDITIONS		TSL253R			
	PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNIT
t _r	Output pulse rise time	V _{O(peak)} = 2 V			7		μs
t _f	Output pulse fall time	V _{O(peak)} = 2 V			7		μs
V _n	Output noise voltage	$E_{e} = 0, V_{O} = V_{D} \qquad f = I_{D}$	= 1 kHz		1		μV/√Hz
		$E_{e} = 0, V_{O} = V_{D} \qquad f = I_{D}$	= 10 kHz		3		
		V _O = 2 V f =	= 1 kHz		4		
		V _O = 2 V f =	= 10 kHz		5		



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PARAMETER MEASUREMENT INFORMATION



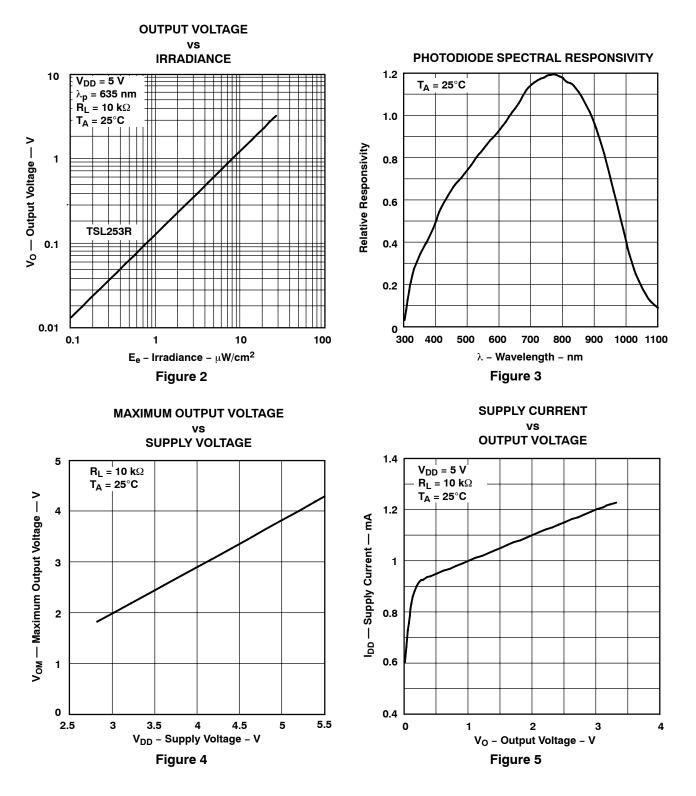
- NOTES: A. The input irradiance is supplied by a pulsed AlInGaP light-emitting diode with the following characteristics: $\lambda_p = 635$ nm, $t_r < 1 \ \mu s$, $t_f < 1 \ \mu s$.
 - B. The output waveform is monitored on an oscilloscope with the following characteristics: $t_r < 100$ ns, $Z_i \ge 1$ M Ω , $C_i \le 20$ pF.

Figure 1. Switching Times



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TYPICAL CHARACTERISTICS

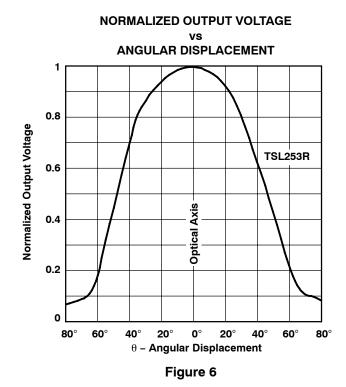
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TYPICAL CHARACTERISTICS





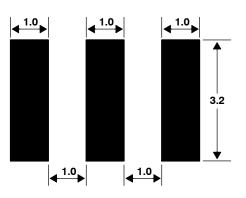
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APPLICATION INFORMATION

PCB Pad Layout

Suggested PCB pad layout guidelines for the SM surface mount package are shown in Figure 7.



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

Figure 7. Suggested SM Package PCB Layout

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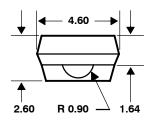
MECHANICAL INFORMATION

The device is supplied in a clear plastic three-lead package (S). The integrated photodiode active area is typically 1,0 mm² (0.0016 in²).

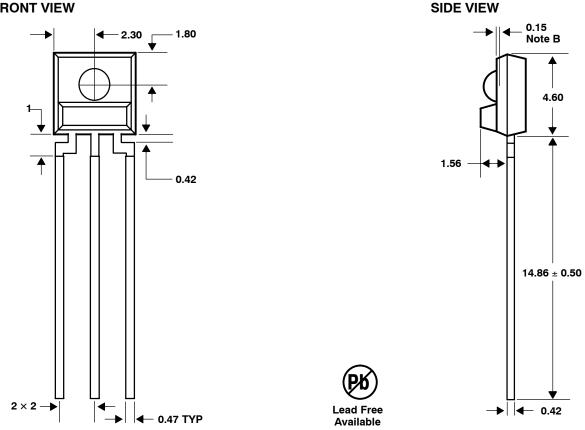
PACKAGE S

PLASTIC SINGLE-IN-LINE SIDE-LOOKER PACKAGE

TOP VIEW



FRONT VIEW



NOTES: A. All linear dimensions are in millimeters; tolerance is ± 0.25 mm unless otherwise stated.

- B. Dimension is to center of lens arc, which is located below the package face.
- C. The integrated photodiode active area is typically located in the center of the lens and 0.97 mm below the top of the lens surface.
- D. Index of refraction of clear plastic is 1.55.

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- E. Lead finish for TSL253R solder dipped, 63% Sn/37% Pb. Lead finish for TSL253R-LF: solder dipped, 100% Sn.
- F. This drawing is subject to change without notice.

Figure 8. Package S — Single-In-Line Side-Looker Package Configuration



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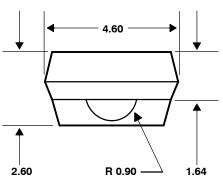
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MECHANICAL DATA

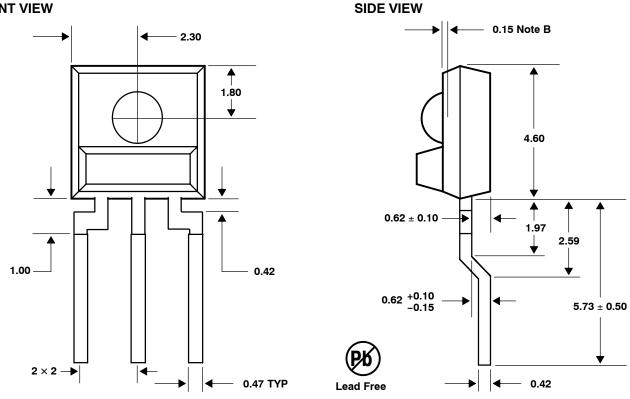
PACKAGE SM

PLASTIC SURFACE MOUNT SIDE-LOOKER PACKAGE

TOP VIEW



FRONT VIEW



NOTES: A. All linear dimensions are in millimeters; tolerance is ± 0.25 mm unless otherwise stated.

- B. Dimension is to center of lens arc, which is located below the package face.
- C. The integrated photodiode active area is typically located in the center of the lens and 0.97 mm below the top of the lens surface.
- D. Index of refraction of clear plastic is 1.55.
- E. Lead finish for TSL253RSM-LF: solder dipped, 100% Sn.
- F. This drawing is subject to change without notice.

Figure 9. Package SM — Surface Mount Side-Looker Package Configuration

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