

**New
Smaller Models
Lower Cost**

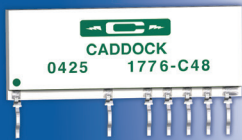
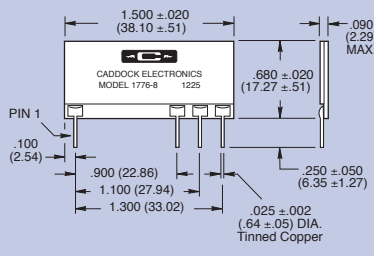


Figure 1



RATIO DEFINITION:
ALL RESISTORS RATIO
ONE ANOTHER

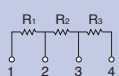
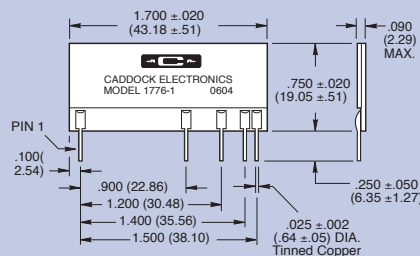


Figure 2



RATIO DEFINITION:
ALL RESISTORS RATIO
ONE ANOTHER

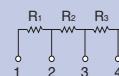
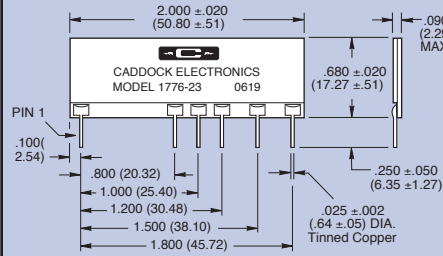


Figure 3



RATIO DEFINITION:
ALL RESISTORS RATIO
ONE ANOTHER

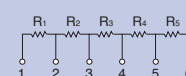
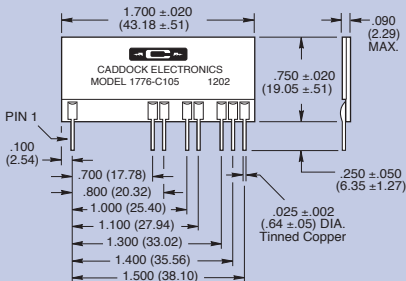


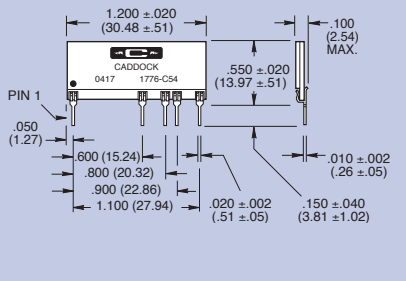
Figure 4



RATIO DEFINITION:
ALL RESISTORS RATIO
ONE ANOTHER



Figure 5



RATIO DEFINITION:
ALL RESISTORS RATIO
ONE ANOTHER

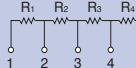
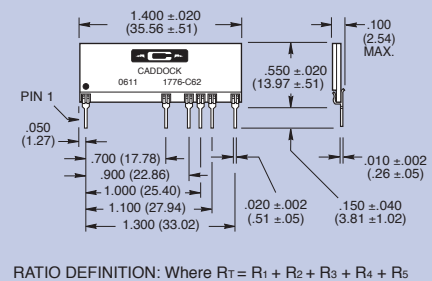


Figure 6



RATIO DEFINITION: Where $R_T = R_1 + R_2 + R_3 + R_4 + R_5$
RATIO 1 $(R_2 + R_3 + R_4 + R_5):R_T$
RATIO 2 $(R_3 + R_4 + R_5):R_T$
RATIO 3 $(R_4 + R_5):R_T$
RATIO 4 $(R_5):R_T$

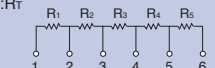
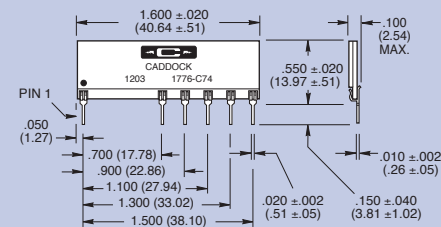


Figure 7



RATIO DEFINITION:
ALL RESISTORS RATIO
ONE ANOTHER

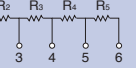
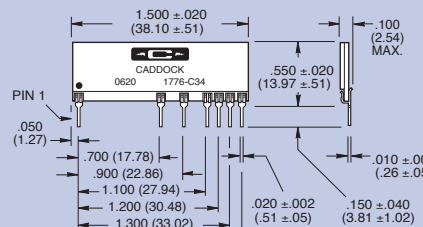


Figure 8



RATIO DEFINITION:
RATIO 1 $R_1:R_2$
RATIO 2 $R_1:R_3$
RATIO 3 $R_1:R_4$
RATIO 4 $R_1:R_5$

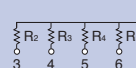
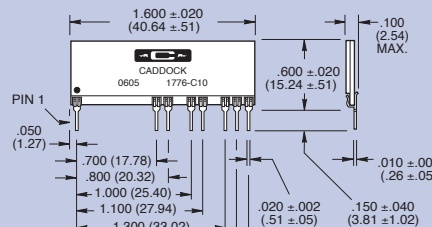


Figure 9



RATIO DEFINITION:
ALL RESISTORS RATIO
ONE ANOTHER

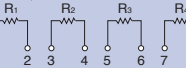
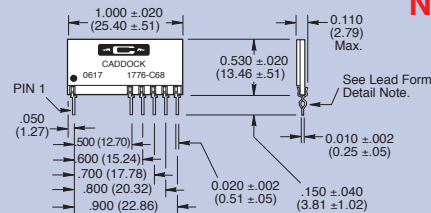
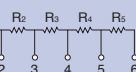


Figure 10



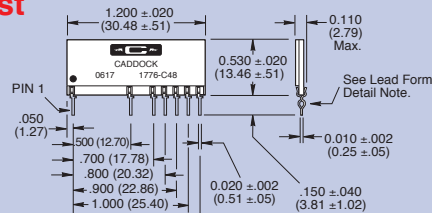
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RATIO 2 $(R_3 + R_4 + R_5):R_T$
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RATIO 4 $(R_5):R_T$



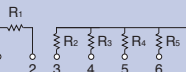
New Smaller Models at Lower Cost

Lead Form Detail Note: Lead form detail provides interference in the circuit board hole to achieve a vertical mount of the device. Recommended circuit board hole is nominally 0.039 inch (.99 mm).

Figure 11



RATIO DEFINITION:
RATIO 1 $R_1:R_2$
RATIO 2 $R_1:R_3$
RATIO 3 $R_1:R_4$
RATIO 4 $R_1:R_5$



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