

UPB1510GV

ABSOLUTE MAXIMUM RATINGS¹ (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V _{CC}	Supply Voltage	V	6.0
V _{IN}	Input Voltage	V	6.0
P _D	Total Power Dissipation ²	mW	250
T _A	Operating Ambient Temp.	°C	-40 to +85
T _{STG}	Storage Temperature	°C	-55 to +150

Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.
2. Mounted on a double-sided copper clad 50x50x1.6 mm epoxy glass PWB (T_A = +85°C).

PRODUCT LINE-UP

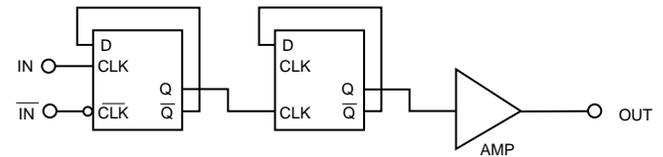
Product No.	I _{CC} (mA)	V _{CC} (V)	+4 f _{IN} (GHz)	Package
UPB585G	18	4.5 to 5.5	0.5 to 2.5	8 pin SOP
UPB1510GV	15	4.5 to 5.5	0.5 to 3.0	8 pin SSOP

Note: This table shows typical values only.

RECOMMENDED OPERATING CONDITIONS

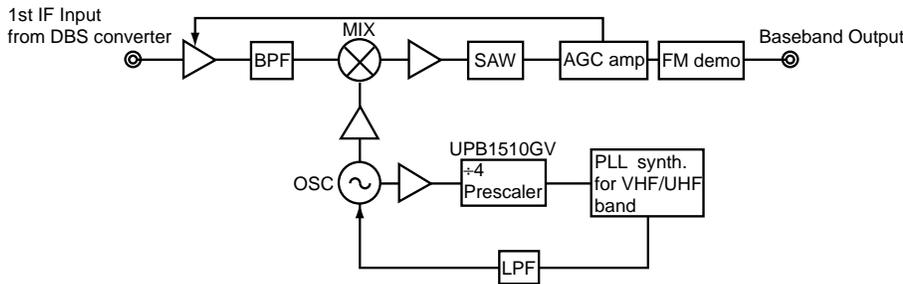
SYMBOL	PARAMETER	UNITS	MIN	TYP	MAX
V _{CC}	Supply Voltage	V	4.5	5.0	5.5
T _A	Operating Ambient Temp.	°C	-40	+25	+85

INTERNAL BLOCK DIAGRAM



SYSTEM APPLICATION EXAMPLE

RF UNIT BLOCK OF DBS TUNER

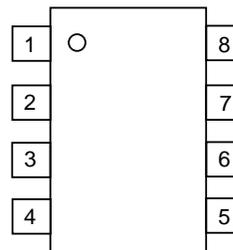
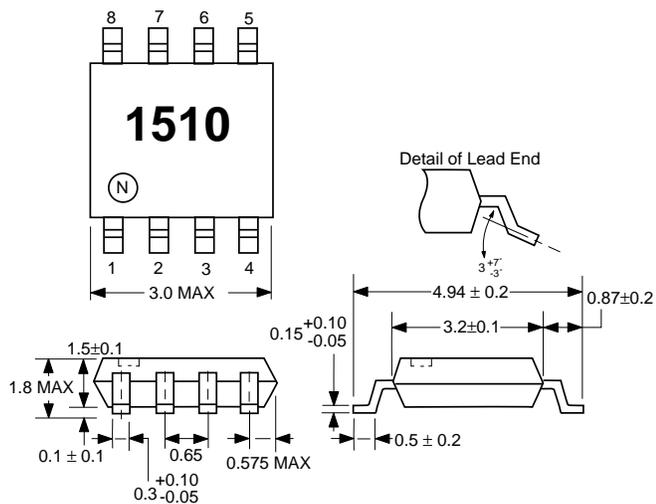


PIN DESCRIPTIONS

Pin No.	Symbol	Applied Voltage	Description
1	V _{CC}	4.5 to 5.5	Power supply pin. This pin must be decoupled with a bypass capacitor (e.g. 1000 pF).
2	IN	-	Signal input pin. This pin should be coupled to source with a capacitor (e.g. 1000 pF).
3	$\overline{\text{IN}}$	-	Signal input bypass pin. This pin must be equipped with a bypass capacitor (e.g. 1000 pF) to ground.
4	GND	0	Ground pin. Ground pattern on the board should be formed as wide as possible to minimize ground impedance.
5	GND	0	
6	NC	-	No connection, this pin should be left open.
7	OUT	-	Divided frequency output pin. This pin is designed as an emitter follower output, and should be coupled to the load with a capacitor (e.g. 1000 pF).
8	NC	-	No connection, this pin should be left open.

OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S08



PIN CONNECTIONS

- 1. Vcc 5. GND
- 2. IN 6. NC
- 3. $\overline{\text{IN}}$ 7. OUT
- 4. GND 8. NC

ORDERING INFORMATION

PART NUMBER	QUANTITY
UPB1510GV-E1	1000/Reel

Note:

- 1. Embossed tape 8 mm wide.
Pin 1 is in the tape pull-out direction.

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