

## **Maximum Ratings** @T<sub>A</sub> = +25°C unless otherwise specified

Characteristic		Symbol	Value	Units
Drain-Source Voltage		$V_{DSS}$	60	V
Drain-Gate Voltage R <sub>GS</sub> ≤ 1.0MΩ		$V_{DGR}$	60	V
Gate-Source Voltage (Note 5)	Continuous Pulsed	$V_{GSS}$	±20 ±40	V
Drain Current (Note 5)	Continuous	I <sub>D</sub>	280	mA
Drain Current (Note 5)	Pulsed	I <sub>DM</sub>	1.5	A

## Thermal Characteristics @TA = +25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation	$P_{D}$	150	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	833	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

## Electrical Characteristics @TA = +25°C unless otherwise specified

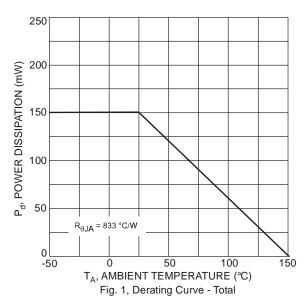
	Symbol					
Characteristic		Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	70	_	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current $@T_C = +2$ $@T_C = +12$	Inco	_	_	1.0 500	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Body Leakage	I <sub>GSS</sub>			±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTIC (Note 6)						
Gate Threshold Voltage	$V_{GS(th)}$	1.0	_	2.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	R <sub>DS</sub> (ON)			7.5 13.5	Ω	$V_{GS} = 5V$ , $I_D = 0.05A$ , $V_{GS} = 10V$ , $I_D = 0.5A$ , $T_i = 125$ °C
On-State Drain Current	I <sub>D(ON)</sub>	0.5	1.0	_	Α	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 7.5V
Forward Transconductance	g <sub>FS</sub>	80	_	_	mS	$V_{DS} = 10V, I_D = 0.2A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>iss</sub>	_	_	50	рF	
Output Capacitance		_	_	25	pF	$V_{DS} = 25V$ , $V_{GS} = 0V$ , $f = 1.0MHz$
Reverse Transfer Capacitance	C <sub>rss</sub>	_	_	5.0	рF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time		_	_	20	ns	$V_{DD} = 30V, I_D = 0.2A, R_L = 150\Omega,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	_	20	ns	$V_{GEN} = 10V$ , $R_{GEN} = 25\Omega$

Notes:

<sup>5.</sup> Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com.

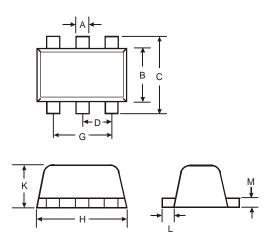
<sup>6.</sup> Short duration pulse test used to minimize self-heating effect.





## **Package Outline Dimensions**

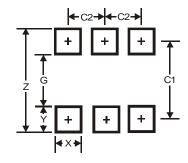
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT563					
Dim	Min	Max	Тур		
Α	0.15	0.30	0.20		
В	1.10	1.25	1.20		
C	1.55	1.70	1.60		
D	-	-	0.50		
G	0.90	1.10	1.00		
Н	1.50	1.70	1.60		
K	0.55	0.60	0.60		
L	0.10	0.30	0.20		
M	0.10	0.18	0.11		
All Dimensions in mm					

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



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