VS-70TPS12PbF, VS-70TPS16PbF High Voltage Series



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PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 82 °C, 180° conduction half sine wave	70	
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}	Lead current limitation		А
Maximum peak, one-cycle		10 ms sine pulse, rated V _{RRM} applied	930	
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied	1100	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V_{RRM} applied Initial $T_J = T_{c}$	4325	A ² s
Maximum - tior rusing	1-1	10 ms sine pulse, no voltage reapplied	6115	
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	61 150	A²√s
Low level value of threshold voltage	V _{T(TO)1}		0.916	V
High level value of threshold voltage	V _{T(TO)2}	T 105 %O	1.21	
Low level value of on-state slope resistance	r _{t1}	T _J = 125 °C	4.138	mΩ
High level value of on-state slope resistance	r _{t2}		3.43	
Maximum peak on-state voltage	V _{TM}	100 A, T _J = 25 °C	1.4	V
Maximum rate of rise of turned-on current	dl/dt	T _J = 25 °C		A/µs
Maximum holding current	Ι _Η	Anode supply = 6 V, resistive load, initial I_T = 1 A, T_J = 25	°C 200	
Maximum latching current	١ _L	Anode supply = 6 V, resistive load, $T_J = 25 \degree C$	400	
	I _{RRM} /I _{DRM}	$T_J = 25 \text{ °C}$ $V_R = \text{rated } V_{RRM}/V_{DRM}$	1.0	mA
Maximum reverse and direct leakage current		$T_J = 125 \text{ °C}$ $(T_J = T_J \text{ max., linear to 80 \%})$	15	
Maximum rate of rise of off-state voltage	dV/dt	$T_J = 125 \text{ °C}$ $V_{DRM} = R_g - k = \text{open}$	500	V/µs

TRIGGERING						
PARAMETER	SYMBOL		TEST CONDITIONS	VALUES	UNITS	
Maximum peak gate power	P _{GM}	T = 30 µs		10	W	
Maximum average gate power	P _{G(AV)}	T = 30 μs		2.5	vv	
Maximum peak gate current	I _{GM}			2.5	А	
Maximum peak negative gate voltage	-V _{GM}			10		
Maximum required DC gate voltage to trigger		T _J = - 40 °C	Anode supply = 6 V resistive load	1.8	v	
	V _{GT}	T _J = 25 °C		1.5	v	
		T _J = 125 °C		1.1		
		T _J = - 40 °C	Anode supply = 6 V resistive load	150		
Maximum required DC gate current to trigger	I _{GT}	T _J = 25 °C		100	mA	
		T _J = 125 °C		80		
Maximum DC gate voltage not to trigger	V _{GD}	T = 105 °C V	0.25	V		
Maximum DC gate current not to trigger	I _{GD}	$T_{\rm J} = 125 ^{\circ}\text{C}, V_{\rm DRM} = \text{rated value} $			mA	

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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum junction temperature	range	TJ		-40 to +125	ာ
Maximum storage temperature	range	T _{Stg}		-40 to +150	U
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.27	
Maximum thermal resistance, junction to ambient		R _{thJA}		40	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2	
A				6	g
Approximate weight	Approximate weight			0.21	oz.
Mounting torque	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf ⋅ in)
Marking device			Case style Super TO 247	70TPS	12
			Case style Super TO-247	70TPS	16

DEVICE	SINE HALF WAVE CONDUCTION RECTANGULAR WAVE CONDUCT						ON				
DEVICE	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VS-70TPSPbF	0.078	0.092	0.117	0.172	0.302	0.053	0.092	0.125	0.180	0.306	°C/W

Note

The table above shows the increment of thermal resistance RthJ-hs when devices operate at different conduction angles than DC

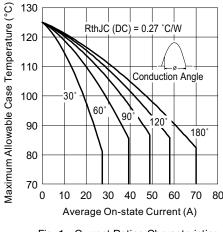
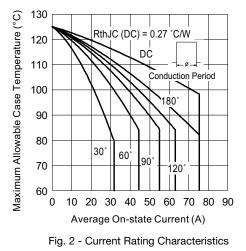
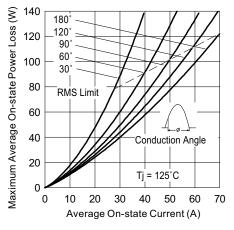


Fig. 1 - Current Rating Characteristics





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Fig. 3 - On-State Power Loss Characteristics

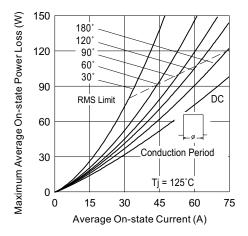


Fig. 4 - On-State Power Loss Characteristics

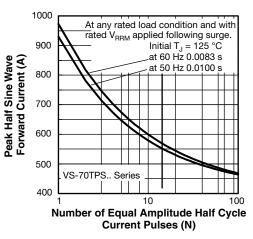


Fig. 5 - Maximum Non-Repetitive Surge Current

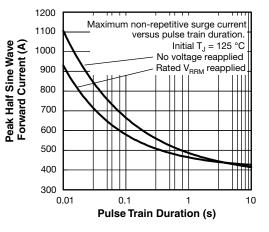
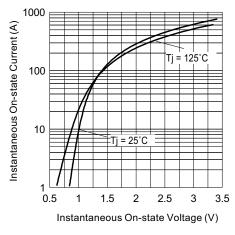
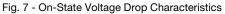
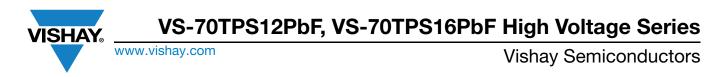
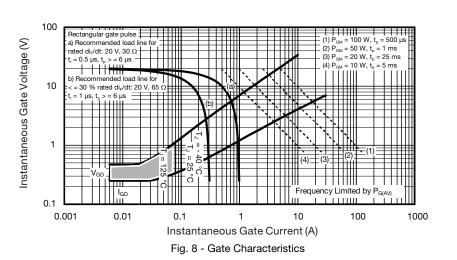


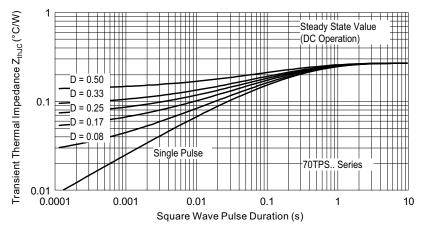
Fig. 6 - Maximum Non-Repetitive Surge Current













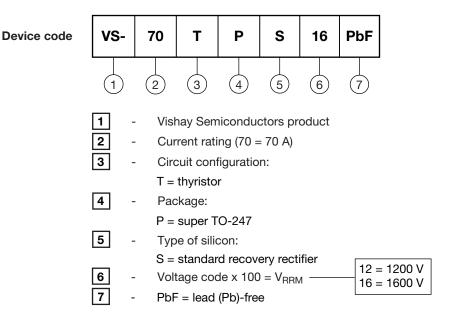
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ORDERING INFORMATION TABLE



ORDERING INFORMATION (example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-70TPS12PbF	25	500	Antistatic plastic tube			
VS-70TPS16PbF	25	500	Antistatic plastic tube			

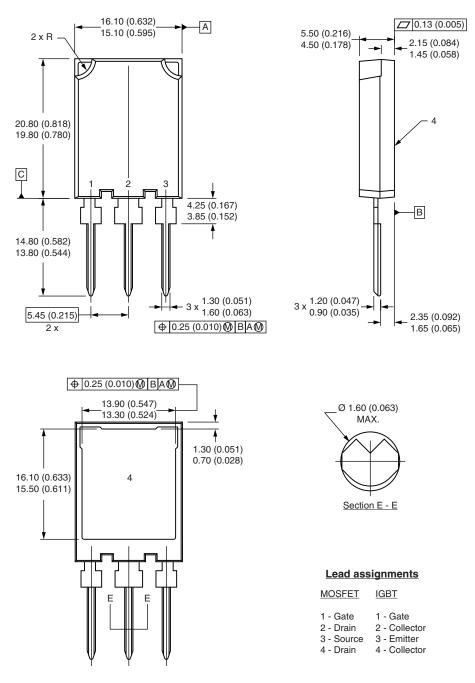
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95073			
Part marking information	www.vishay.com/doc?95070			
SPICE model VS-70TPS12	www.vishay.com/doc?96760			
SPICE model VS-70TPS16	www.vishay.com/doc?96761			

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Super TO-247

DIMENSIONS in millimeters (inches)



Notes

- ⁽¹⁾ Dimension and tolerancing per ASME Y14.5M-1994
- ⁽²⁾ Controlling dimension: millimeter
- (3) Outline conforms to JEDEC® outline TO-274AA

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