TSM7N65

650V N-Channel Power MOSFET

Thermal Performance

Parameter		Symbol	Limit	Unit
Thermal Resistance - Junction to Case	TO-220	$R\Theta_{JC}$	1.0	°C/W
	ITO-220		4.2	
Thermal Resistance - Junction to Ambient		$R\Theta_{JA}$	62.5	°C/W

Note: Surface mounted on FR4 board t ≤ 10sec

Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250uA	BV_{DSS}	650	--	--	V
Drain-Source On-State Resistance	V_{GS} = 10V, I_D = 3A	$R_{DS(ON)}$	--	1.0	1.2	Ω
Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250uA	$V_{GS(TH)}$	2.0	--	4.0	V
Zero Gate Voltage Drain Current	V_{DS} = 650V, V_{GS} = 0V	I_{DSS}	--	--	1	uA
	V_{DS} = 650V, V_{GS} = 0V, T_C=125ºC		--	--	50	
Gate Body Leakage	V_{GS} = ±20V, V_{DS} = 0V	I_{GSS}	--	--	±10	uA
Forward Transfer Conductance	V_{DS} = 8V, I_D = 1A	g_{fs}	--	3.7	--	S
Diode Forward Voltage	I_S = 6A, V_{GS} = 0V	V_{SD}	--	--	1.6	V
Dynamic						
Total Gate Charge	V_{DS} = 300V, I_D = 6A, V_{GS} = 10V	Q_g	--	32	46	nC
Gate-Source Charge		Q_{gs}	--	6	--	
Gate-Drain Charge		Q_{gd}	--	11	--	
Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	C_{iss}	--	905	--	pF
Output Capacitance		C_{oss}	--	115	--	
Reverse Transfer Capacitance		C_{rss}	--	25	--	
Switching						
Turn-On Delay Time	V_{GS} = 10V, I_D = 6A, V_{DD} = 300V, R_G = 25Ω	$t_{d(on)}$	--	14	--	nS
Turn-On Rise Time		t_r	--	14	--	
Turn-Off Delay Time		$t_{d(off)}$	--	47	--	
Turn-Off Fall Time		t_f	--	19	--	
Reverse Recovery Time	V_{GS} = 0V, I_S = 6A, dI_F/dt = 100A/us	t_{fr}	--	638	--	nS
Reverse Recovery Charge		Q_{fr}	--	4.8	--	uC

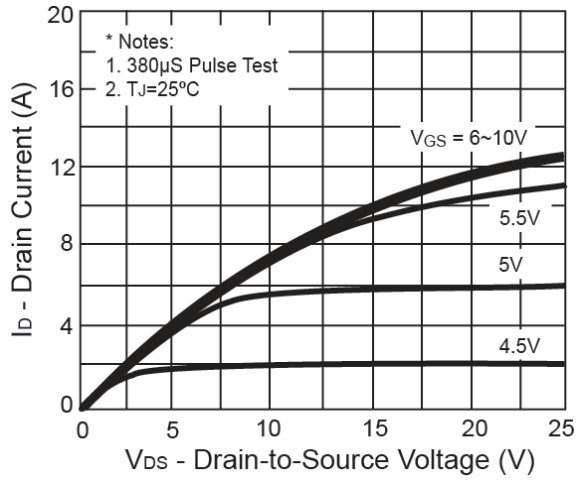
Note:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. V_{DD} = 50V, I_{AS}=3.6A, L=30mH, V_{DS}=500V
3. Pulse test: pulse width ≤300uS, duty cycle ≤2%
4. Essentially Independent of Operating Temperature

TSM7N65

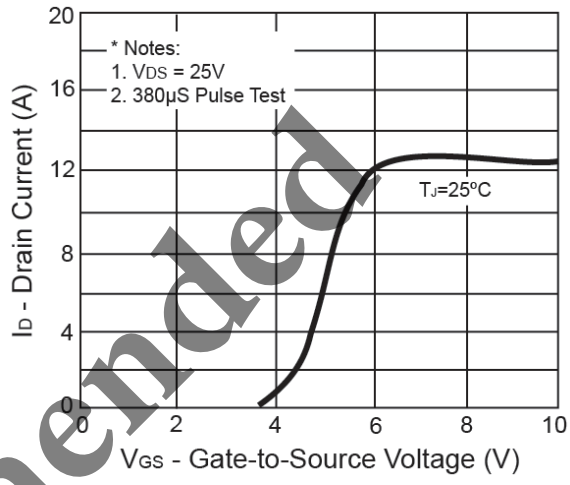
650V N-Channel Power MOSFET

Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

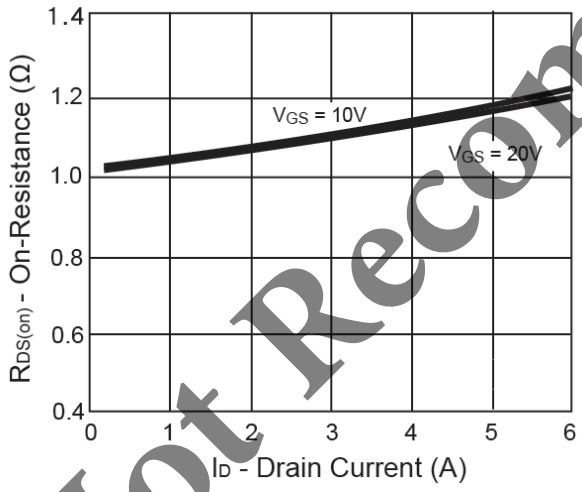
Output Characteristics



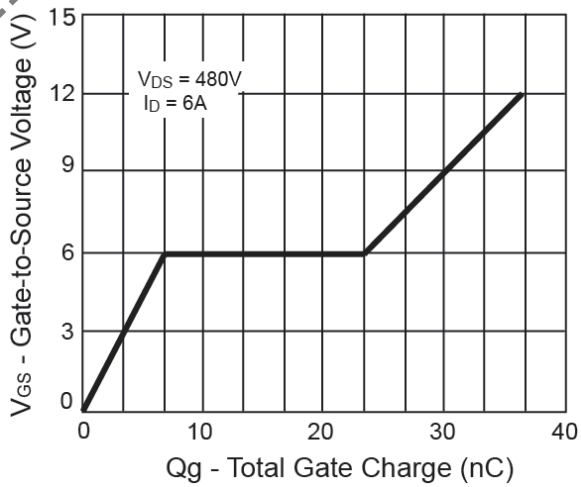
Transfer Characteristics



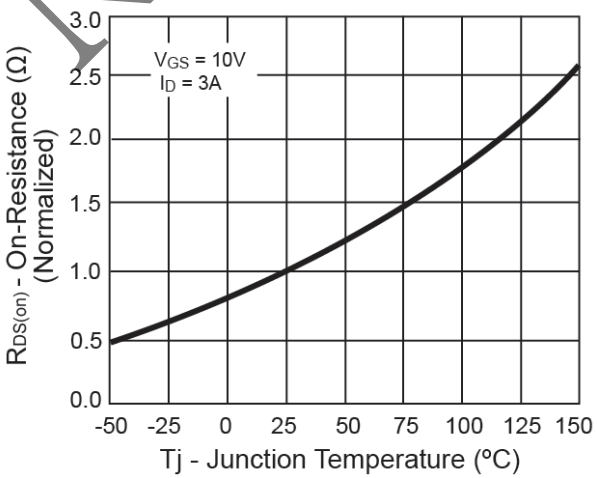
On-Resistance vs. Drain Current



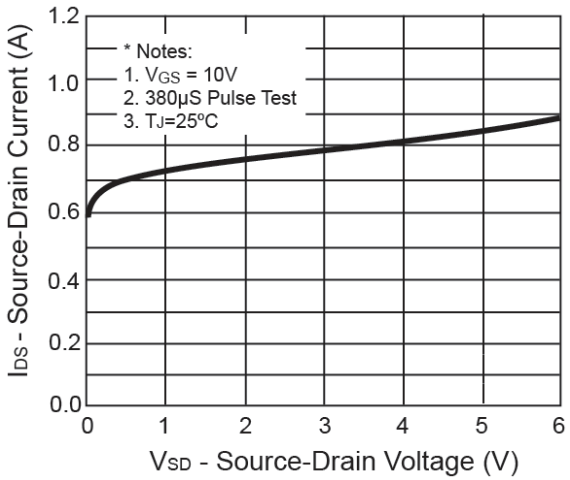
Gate Charge



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

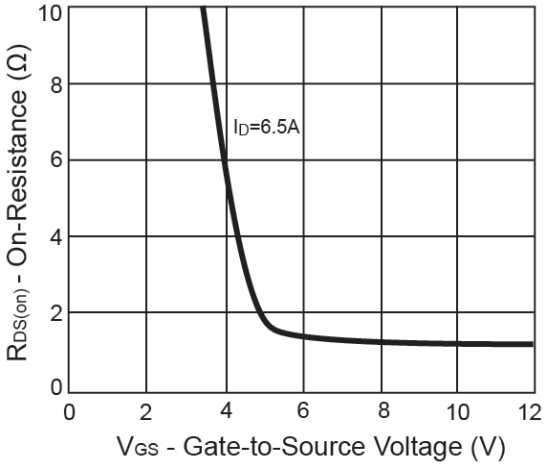


TSM7N65

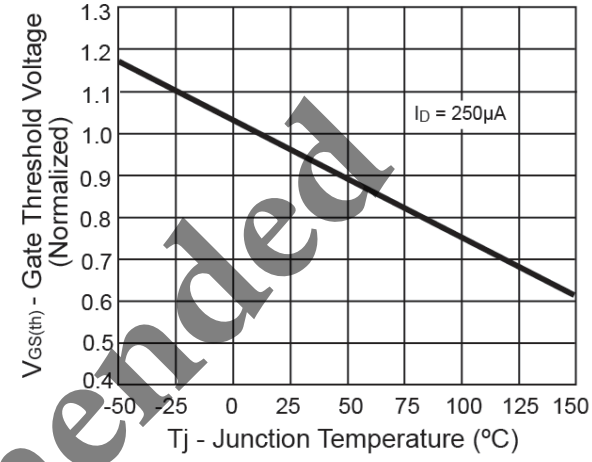
650V N-Channel Power MOSFET

Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

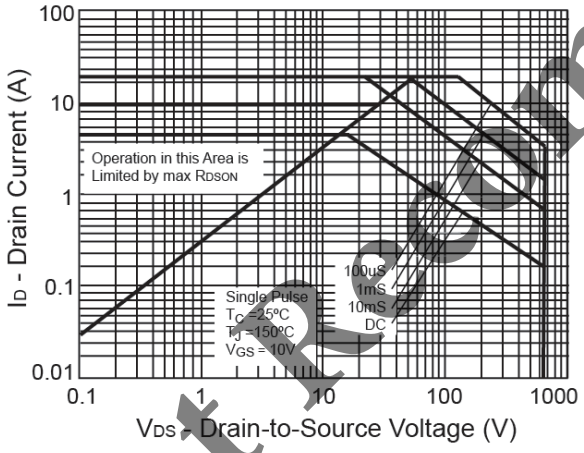
On-Resistance vs. Gate-Source Voltage



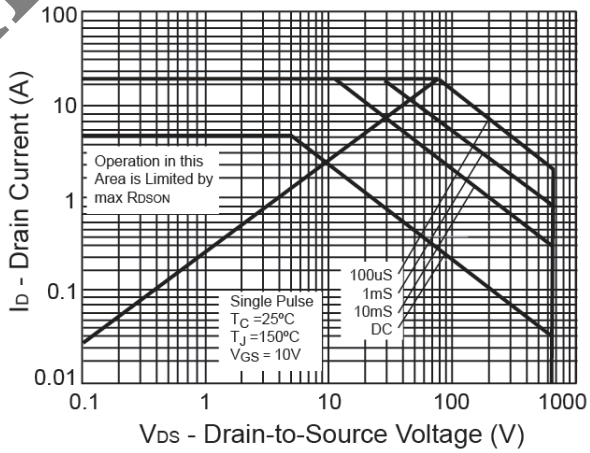
Threshold Voltage



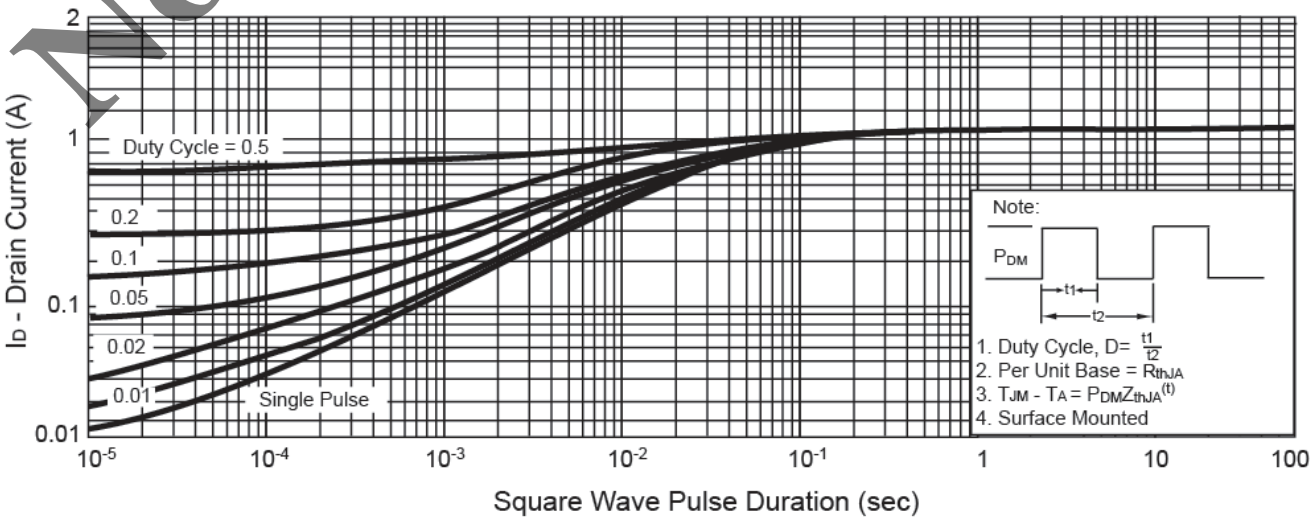
Maximum Safe Operating Area - TO-220



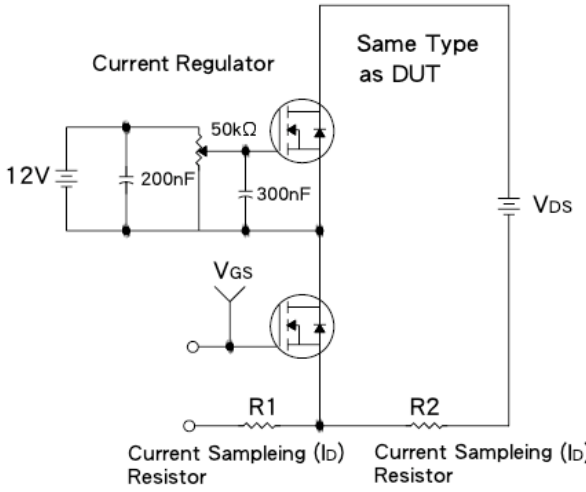
Maximum Safe Operating Area - ITO-220

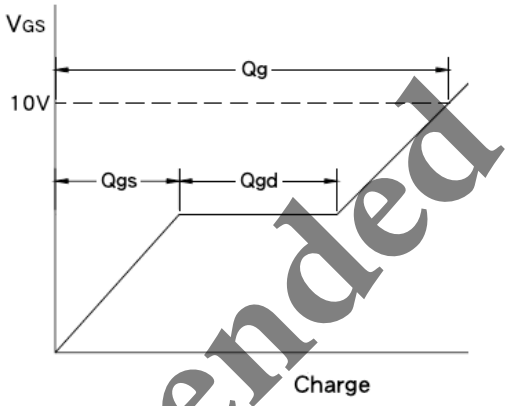


Normalized Thermal Transient Impedance, Junction-to-Ambient

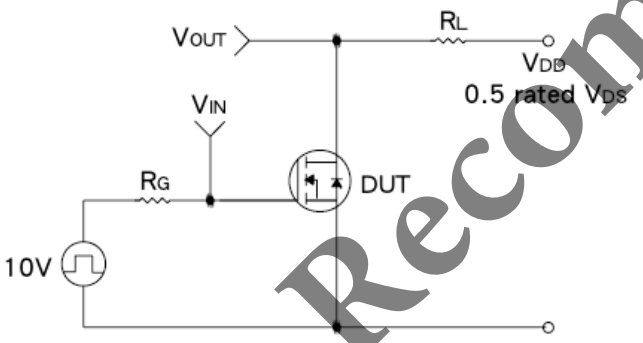


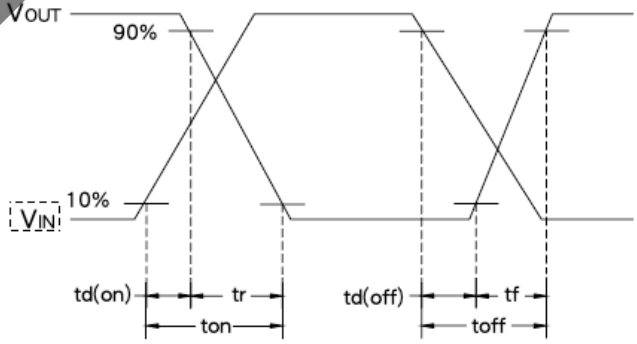
Gate Charge Test Circuit & Waveform



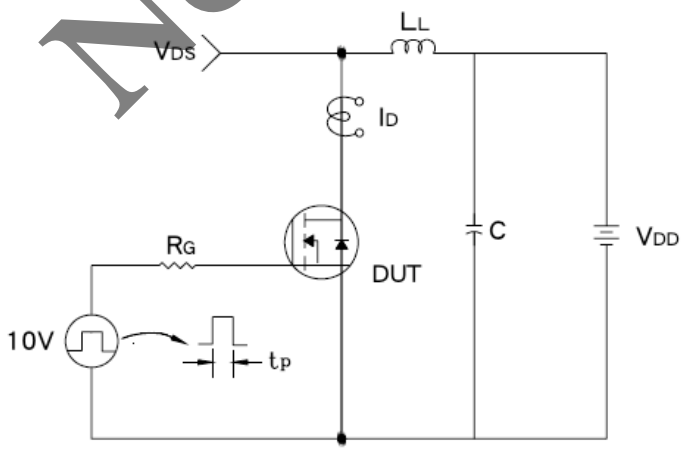


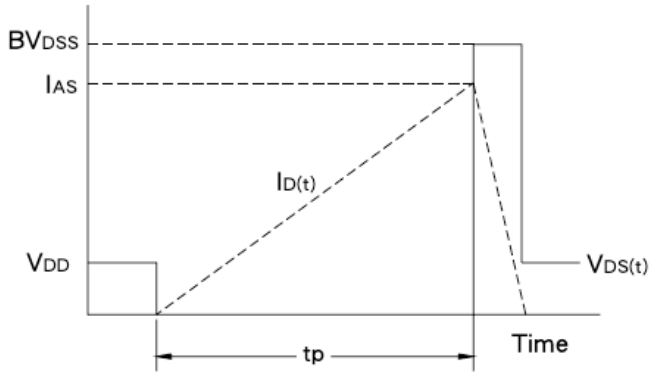
Resistive Switching Test Circuit & Waveform



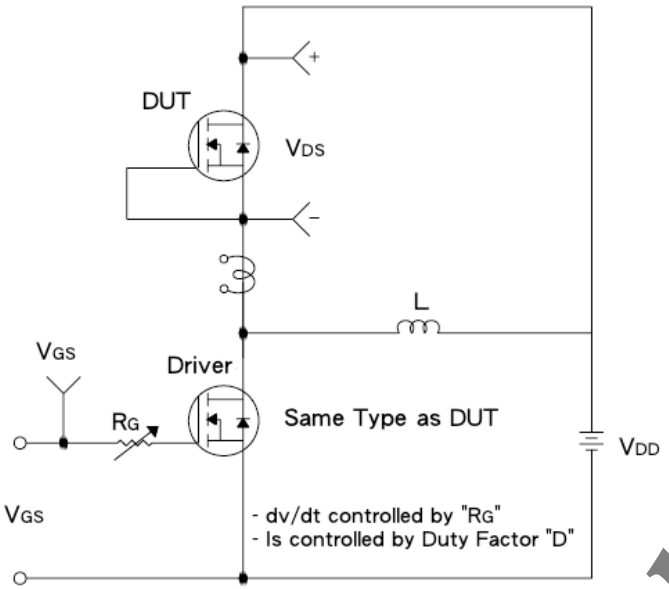


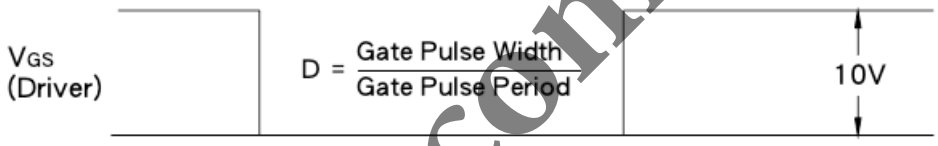
E_{AS} Test Circuit & Waveform

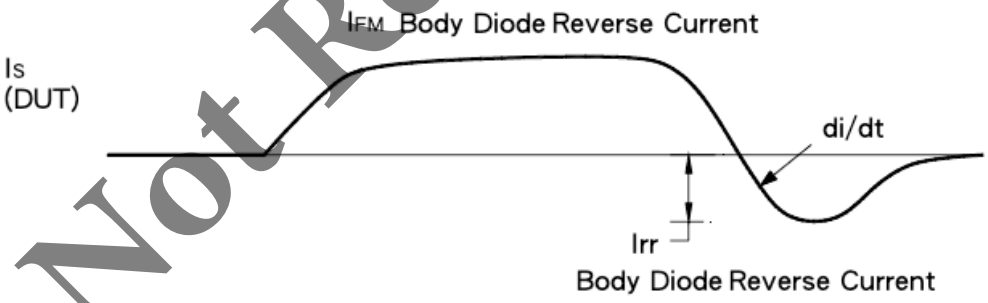


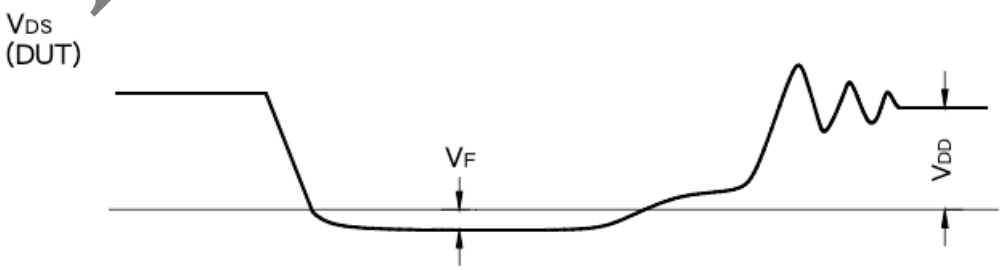


Diode Reverse Recovery Time Test Circuit & Waveform

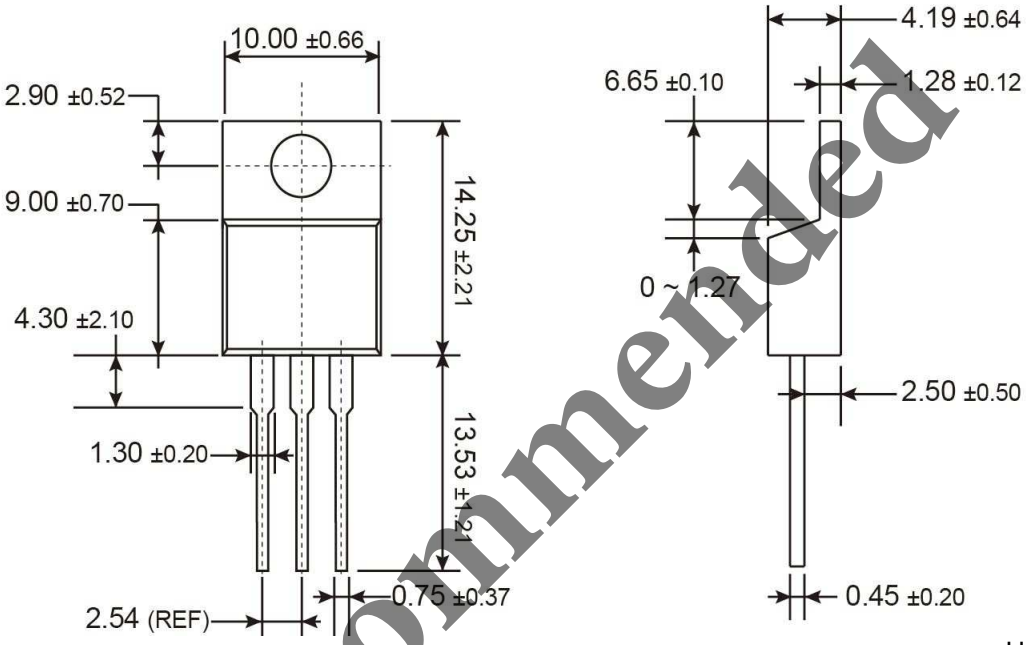






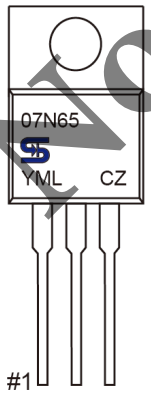


TO-220 Mechanical Drawing



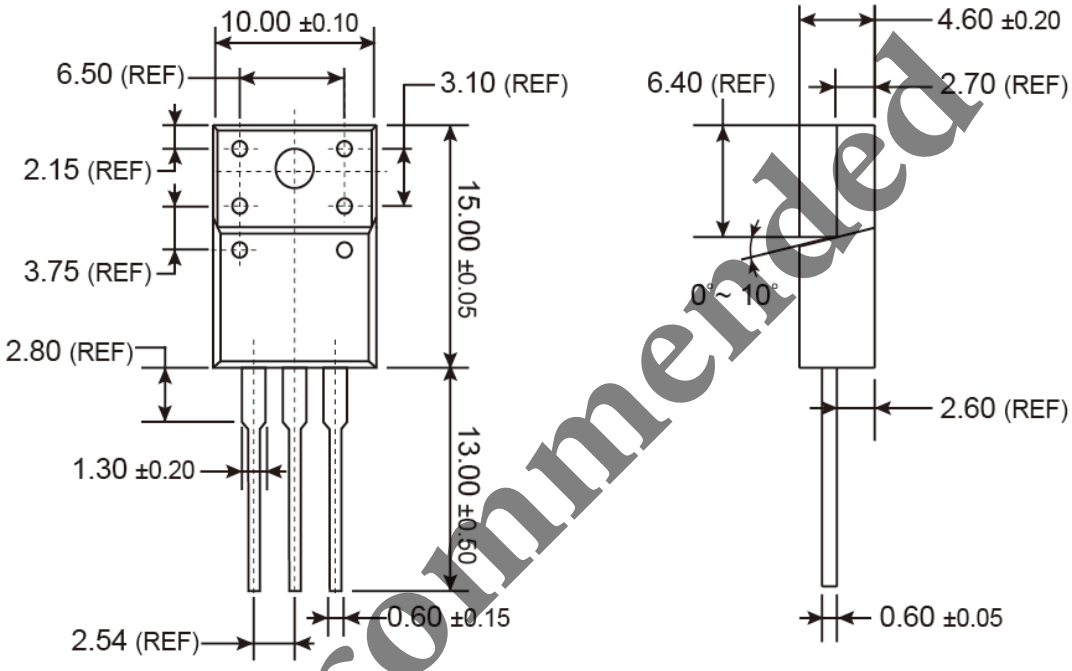
Unit: Millimeters

Marking Diagram



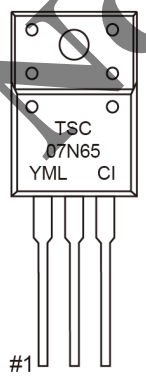
Y = Year Code
M = Month Code
(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apl, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)
L = Lot Code

ITO-220 Mechanical Drawing



Unit: Millimeters

Marking Diagram



Y = Year Code
M = Month Code
(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apl, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)
L = Lot Code

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Taiwan Semiconductor:

TSM7N65CI C0 TSM7N65CZ C0