# **Double-Sided Chip Resistors**



### **DSC Series**

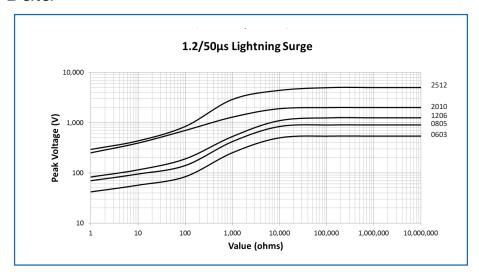
## Performance Data

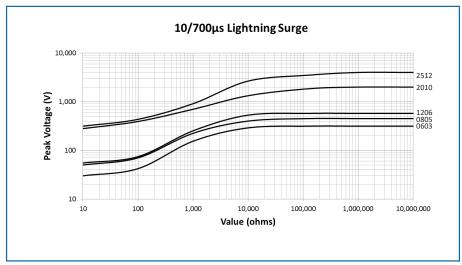
			Maximum	Typical	
Load at rated power: 1000 hours at 70°C $\Delta$ R%			1	0.25	
Derating from rated power at 70°C			Zero at 155°C		
Overload: 6.25 x rated power for 2 seconds			1	0.1	
Shelf life test: 12 months at re	oom temperature	ΔR%	0.1	0.02	
Dry heat: 1000 hours at 155°C			1	0.2	
Long term damp heat			1	0.25	
Temperature rapid change			0.25	0.05	
Anti-sulphur grade (AS)	ASTM-B-809 (1000 hours, 50°C, 91-93% RH)	ΔR%	0.25	0.05	
Sulphur-resistant grade (SR)	EIA-977 (750 hours, 105°C)		0.25	0.05	
	ASTM-B-809 (1000 hours, 50°C, 91-93% RH)	ΔR% 0.25 ΔR% 0.25	0.05		
	Modified ASTM-B-809 (1000 hours, 105°C, 85% RH)		1	0.25	
Resistance to solder heat		ΔR%	0.25	0.05	

### Pulse Performance Data

### **Lightning Surge**

Resistors are tested in accordance with IEC 60 115-1 using both 1.2/50µs and 10/700µs pulse shapes. 10 pulses are applied. The limit of acceptance is a shift in resistance of less than 1% from the initial value.





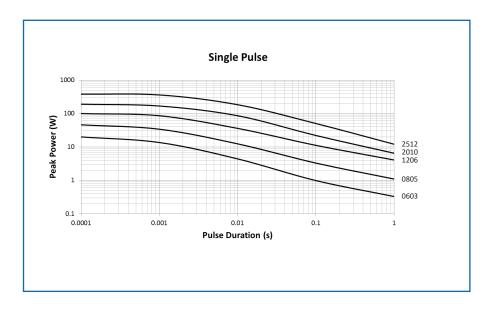
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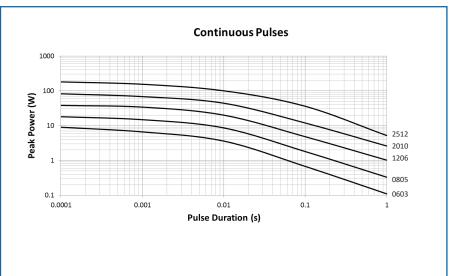
#### **Single Pulse**

The single impulse graph is the result of 50 impulses of rectangular shape applied at one-minute intervals. The limit of acceptance was a shift in resistance of less than 1% from the initial value.



# Continuous Load Due to Repetitive Pulses

The continuous load graph was obtained by applying repetitive rectangular pulses where the pulse period was adjusted so that the average power dissipated in the resistor was equal to its rated power at 70°C. Again the limit of acceptance was a shift in resistance of less than 1% from the initial value.

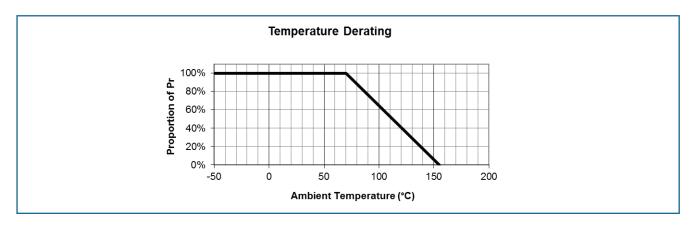


## **Double-Sided Chip Resistors**



#### **DSC Series**

### Thermal Performance Data



## Packaging

0603, 0805 and 1206 resistors are supplied on 8mm carrier tape and 2010 and 2512 resistors are supplied on 12mm carrier tape, all on 7 inch reels as per IEC 286-3.

## **Application Notes**

DSC resistors are ideally suited for handling by automatic methods due to their rectangular shape and the small dimensional tolerances. Electrical connection to a ceramic substrate or to a printed circuit board can be made by reflow or wave soldering of wrap-around terminations.

Wrap-around terminations provide good leach properties and ensure reliable contact. Due to the robust construction, the DSC can be immersed in the solder bath for 30 seconds at 260°C. This enables the resistor to be mounted on one side

of a printed circuit board and wire-leaded components applied on the other side. DSC is compatible with typical Pb-free soldering materials and temperature profiles.

DSC resistors themselves can operate at a maximum temperature of 155°C. For soldered resistors, the joint temperature should not exceed 110°C. This condition is met when the stated power levels at 70°C and recommended pad and trace areas are used. Pad and trace area is defined as the total area of the solder pad plus all copper trace within two squares of the edge of the solder pad. Allowance should be made if smaller areas of copper are used.

## Ordering Procedure

Example: DSC2512-10KFT18 (DSC2512, 10 kilohms ±1%, Pb-free)



1	2	3		4	5		6		
Type	Size	Sulphur Grade <sup>1</sup>		Value	Tolerance		Termination & Packing		
DSC	0603		Omit for Standard	E24 = 3/4 characters	D	±0.5%	Standard Pb-free finish		
	0805	AS	Anti-Sulphur	E96 = 3/4 characters	F	±1%	T5	0603	5000/reel standard
	1206	SR	Sulphur Resistant	R = ohms	J	±5%		0805	
	2010			K = kilohms			Т3	1206	3000/reel standard
	2512			M = megohms				2010	
							T18	2512	1800/reel standard
						T1	All sizes	1000/reel available	
								SnPb finish	
Note 1. For now designs requiring registered to sulphur bearing ago. CD grade is proferred						PB	All sizes	Standard quantities as for Pb-free	

General Note

BI Technologies IRC Welwyn