

	-			
PΔ	RTS	TA	RI	F

AGC		LEGACY, FOR LONG BURST REMOTE CONTROLS (AGC2)	RECOMMENDED FOR LONG BURST CODES (AGC4)	
	30 kHz	TSOP6230	TSOP6430	
	33 kHz	TSOP6233	TSOP6433	
Carrier	36 kHz	TSOP6236	TSOP6436 ⁽¹⁾⁽²⁾⁽³⁾	
frequency	38 kHz	TSOP6238	TSOP6438 ⁽⁴⁾⁽⁵⁾	
	40 kHz	TSOP6240	TSOP6440	
	56 kHz	TSOP6256	TSOP6456 ⁽⁶⁾⁽⁷⁾	
Package		Panhe	ad	
Pinning		1 = GND, 2 = N.C., 3	3 = V _S , 4 = OUT	
Dimensions (mm)	7.5 W x 5.3 H	H x 4.0 D	
Mounting		SMD	SMD	
Application		Remote control		
Best choice f	or	⁽¹⁾ RC-5 ⁽²⁾ RC-6 ⁽³⁾ Panasonic ⁽⁴⁾ NEC ⁽⁵⁾ Sharp ⁽⁶⁾ r-step ⁽⁷⁾ Thomson RCA		
Special optio	ns	 Narrow optical filter: <u>www.vishay.com/doc?81590</u> Wide optical filter: <u>www.vishay.com/doc?82726</u> 		

ABSOLUTE MAXIMUM RA	TINGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		Vs	-0.3 to +6	V
Supply current		I _S	5	mA
Output voltage		Vo	-0.3 to (V _S + 0.3)	V
Output current		Ι _Ο	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

ELECTRICAL AND OPT	CAL CHARACTERISTICS	(T _{amb} = 25 °C	C, unless o	therwise sp	pecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5	-	5.5	V
Cupply current	$V_{S} = 5 V, E_{v} = 0$	I _{SD}	0.55	0.7	0.9	mA
Supply current	E _v = 40 klx, sunlight	I _{SH}	-	0.8	-	mA
Transmission distance	$E_v = 0$, IR diode TSAL6200, $I_F = 50$ mA, test signal see Fig. 1	d	-	18	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V _{OSL}	-	-	100	mV
Minimum irradiance	$\begin{array}{c} \mbox{Pulse width tolerance:} \\ t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_{o,} \\ \mbox{test signal see Fig. 1} \end{array}$	E _{e min.}	-	0.2	0.4	mW/m ²
Maximum irradiance	t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see Fig. 1	E _{e max.}	50	-	-	W/m ²
Directivity	Angle of half transmission distance	Φ1/2	-	± 50	-	deg

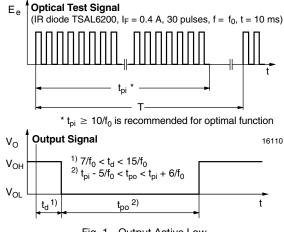
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TSOP62.., TSOP64..



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TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)





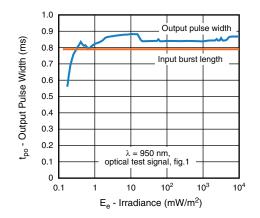
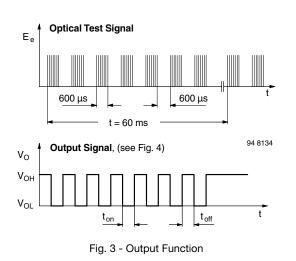


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient



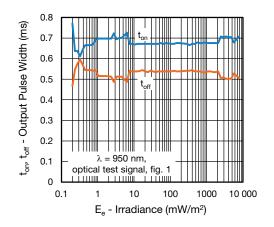


Fig. 4 - Output Pulse Diagram

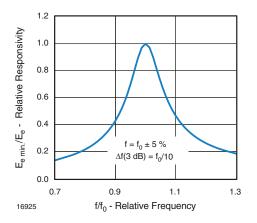


Fig. 5 - Frequency Dependence of Responsivity

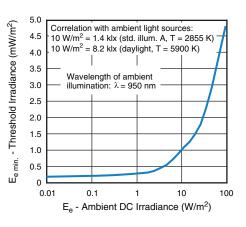


Fig. 6 - Sensitivity in Bright Ambient

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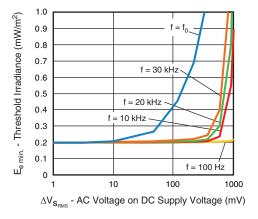


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

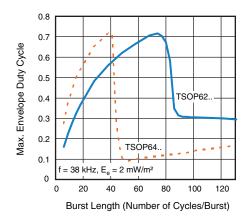


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

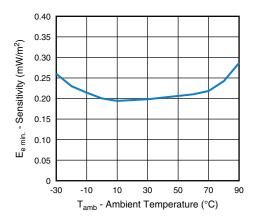
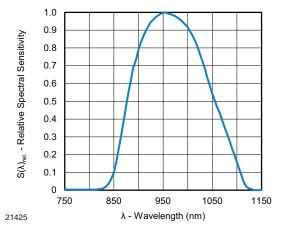


Fig. 9 - Sensitivity vs. Ambient Temperature



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Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

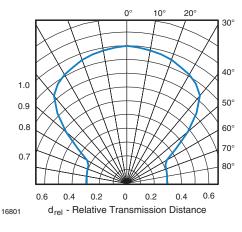


Fig. 11 - Horizontal Directivity

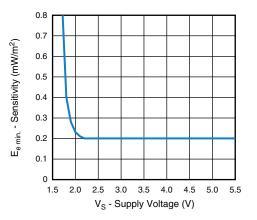


Fig. 12 - Sensitivity vs. Supply Voltage

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SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output.

Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)
- 2.4 GHz and 5 GHz Wi-Fi

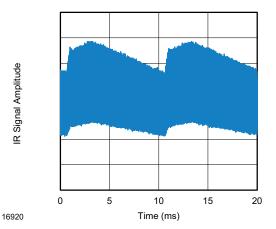


Fig. 13 - IR Disturbance from Fluorescent Lamp With Low Modulation

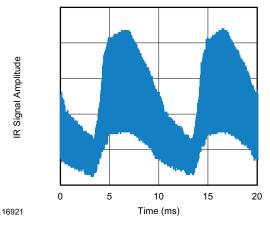


Fig. 14 - IR Disturbance from Fluorescent Lamp With High Modulation

	TSOP62	TSOP64
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 12 cycles	10 to 35 cycles ≥ 12 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	800	1300
NEC code	Yes	Preferred
RC5 / RC6 code	Yes	Preferred
Thomson 56 kHz code	Yes	Preferred
Sharp code	Yes	Preferred
Suppression of interference from fluorescent lamps	Mild disturbance patterns are suppressed (example: signal pattern of Fig. 13)	Complex and critical disturbance patterns are suppressed (example: signal pattern of Fig. 14 or highly dimmed LCDs)

Note

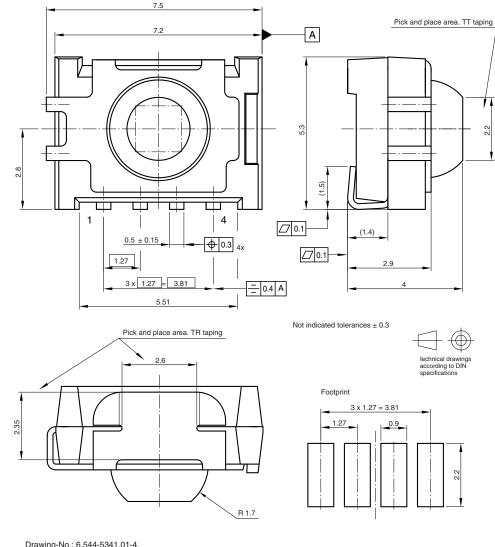
• For data formats with short bursts please see the datasheet of TSOP61.., TSOP63.., TSOP65..



TSOP62.., TSOP64..

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PACKAGE DIMENSIONS in millimeters



Drawing-No.: 6.544-5341.01-4 Issue: 8; 02.09.09

ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

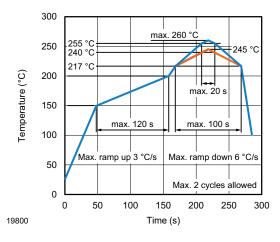
Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

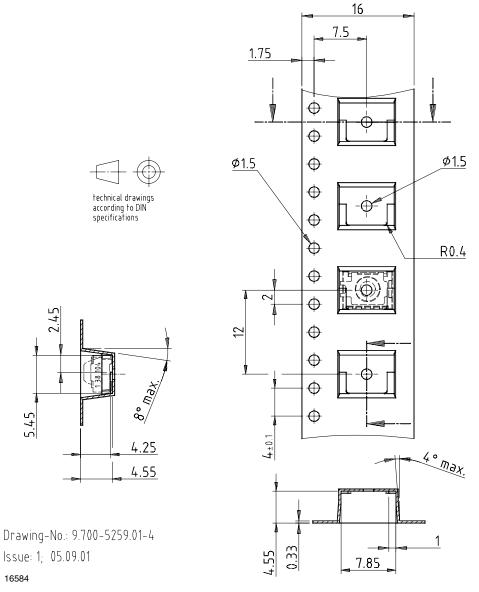
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VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP..TT DIMENSIONS in millimeters



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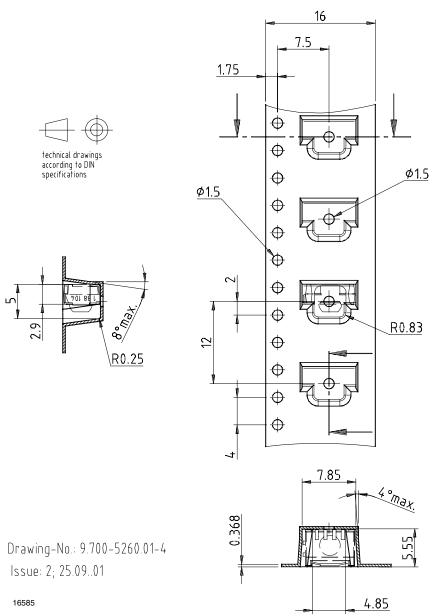
7

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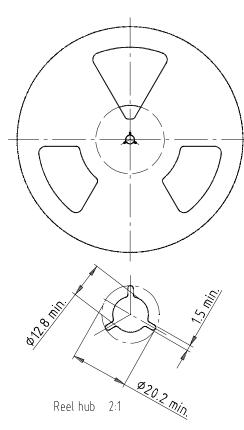
TAPING VERSION TSOP..TR DIMENSIONS in millimeters

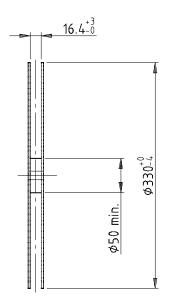


8



REEL DIMENSIONS in millimeters





Form of the leave open of the wheel is supplier specific.

Dimension acc. to IEC EN 60 286-3

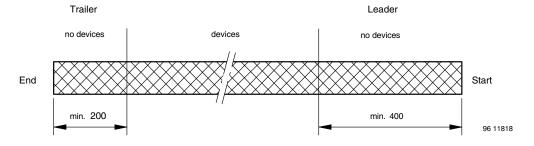
Tape width 16



technical drawings according to DIN specifications

Drawing-No.: 9.800-5052.V2-4 Issue: 1; 07.05.02

LEADER AND TRAILER DIMENSIONS in millimeters



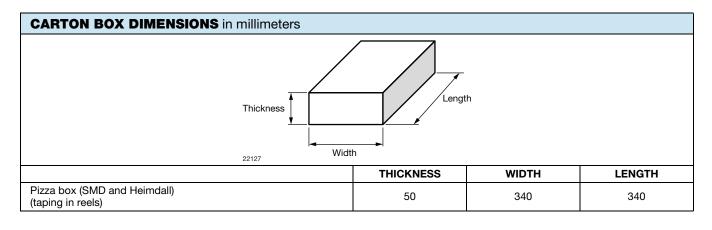
TSOP62.., TSOP64..

Vishay Semiconductors



OUTER PACKAGING

The sealed reel is packed into a pizza box.



COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 mm/min. \pm 10 mm/min. 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxx+	Company logo
LONG BAR CODE TOP	ТҮРЕ	LENGTH
Item-number	Ν	8
Plant-code	Ν	2
Sequence-number	Х	3
Quantity	Ν	8
Total length	-	21
SHORT BAR CODE BOTTOM	ТҮРЕ	LENGTH
Selection-code	Х	3
Data-code	Ν	3
Batch-number	Х	10
Filter	-	1
Total length	-	17

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Proper storage and handling procedures should be followed

to prevent ESD damage to the devices especially when they

are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific

VISHAY SEMICONDUCTORS STANDARD

ESD PRECAUTION

BAR CODE LABELS

SOP6238TB

120

data.

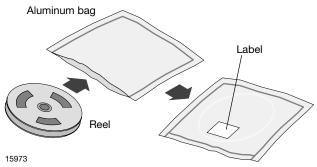


Vishay Semiconductors

22645

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

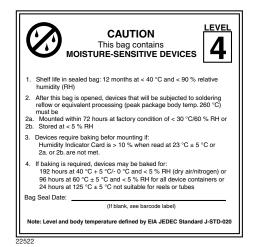
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 $^\circ\text{C}$ + 5 $^\circ\text{C}$ and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC[®] standard J-STD-020 level 4 label is included on all dry bags.



EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags

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