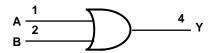


Pin Descriptions

Pin Name	Pin No	Description
А	1	Data Input
В	2	Data Input
GND	3	Ground
Y	4	Data Output
V _{CC}	5	Supply Voltage

Logic Diagram



Function Table

Inpi	Output	
Α	В	Υ
Н	Х	Н
Х	Н	Н
L	L	L



Absolute Maximum Ratings (Note 2)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	K۷
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state	-0.5 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I <0	-20	mA
I _{OK}	Output Clamp Current (V _O < 0 or V _O > V _{CC})	±20	mA
Ιο	Continuous output current (V _O = 0 to V _{CC})	±25	mA
I _{CC}	Continuous current through V _{CC}	50	mA
I _{GND}	Continuous current through GND	-50	mA
TJ	Operating Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

Recommended Operating Conditions (Note 3)

Symbol		Parameter	Min	Max	Unit
V _{CC}	Operating Voltage		4.5	5.5	V
V_{IH}	High-level Input Voltage		2.0		V
V _{IL}	Low-level input voltage			0.8	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V_{CC}	V
I _{OH}	High-level output current			-8	mA
I _{OL}	Low-level output current			8	mA
Δt/ΔV	Input transition rise or fall rate			20	ns/V
T _A	Operating free-air temperature		-40	125	°C

Notes: 3. Unused inputs should be held at $V_{\mbox{CC}}$ or Ground.



Electrical Characteristics

0	5	T O	.,		25°C		-40°C t	o 85°C	-40°C to	o 125ºC	11.24
Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур.	Max	Min	Max	Min	Max	Unit
.,	High Level	$I_{OH} = -50\mu A$	4.5V	4.4	4.5		4.4		4.4		.,
V _{OH}	Output Voltage	$I_{OH} = -8mA$	4.5V	3.94			3.8		3.70		V
\/	Low Level	$I_{OL} = 50\mu A$	4.5V		0	0.1		0.1		0.1	V
V _{OL}	Output Voltage	$I_{OL} = 8mA$	4.5V			0.36		0.44		0.55	V
l _l	Input Current	$V_I = 5.5V$ or GND	0 to 5.5V			± 0.1		± 1		±2	μΑ
I _{CC}	Supply Current	$V_I = 5.5V$ or GND $I_O=0$	5.5V			1		10		40	μΑ
C _i	Input Capacitance	$V_I = V_{CC} - or$ GND	5.5V		2.0	10		10		10	pF
ΔI _{CC}	Additional Supply Current	One input at 3.4 V Other inputs at V _{CC} or GND	5.5V			1.35		1.5			mA
	Thermal Resistance	SOT25			204						0000
θ _{JA} Junction-to- Ambient		SOT353	(Note 4)		371						°C/W
Thermal Resistance		SOT25	(Nata 4)		52						°C/W
θ_{JC}	Junction-to- Case	SOT353	(Note 4)		143						C/VV

Note: 4. Test conditions for SOT25, and SOT353: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

 $V_{CC} = 5V \pm 0.5V$ (see Figure 1)

Parameter	From	TO			25°C		-40°C t	o 85ºC	-40°C to	125ºC	Unit
Parameter	(Input)	(OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Offic
4	A or D	A == D	C _L =15pF	0.6	3.3	6.9	0.6	8.0	0.6	9.0	ns
t _{pd}	A or B	ř	C _L =50pF	0.6	4.8	7.9	0.6	9.0	0.6	10.0	ns

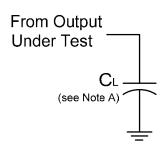
Operating Characteristics

 $T_A = 25$ °C

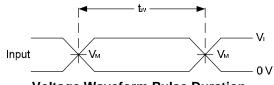
	Parameter	Test Conditions	V _{CC} = 5 V Typ.	Unit
C_pd	Power dissipation capacitance	f = 1 MHz No Load	11.5	pF



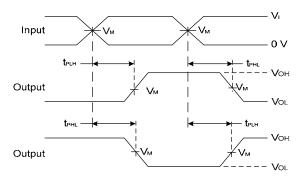
Parameter Measurement Information



V	In	puts	V	C	
V _{CC}	VI	t _r /t _f	V _M	CL	
5V±0.5V	3V	≤3ns	1.5V	15pF	
5V±0.5V	3V	≤3ns	1.5V	50pF	



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Notes: A. Includes test lead and test apparatus capacitance.

B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.

C. Inputs are measured separately one transition per measurement.

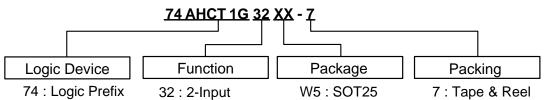
D. t_{PLH} and t_{PHL} are the same as t_{pd}.

Pb,



SINGLE 2 INPUT POSITIVE OR GATE

Ordering Information



AHCT: 2 to 5.5V

Family with TTL input level

1G: One gate

Package 7" Tape and Reel **Packaging Device** Code **Part Number Suffix** (Note 5) Quantity 74AHCT1G32W5-7 W5 SOT25 3000/Tape & Reel -7 74AHCT1G32SE-7 SE **SOT353** 3000/Tape & Reel -7

Notes: 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

4

OR-Gate

Marking Information

(Top View)

XX Y W X

5

XX : Identification code

Y: Year 0~9

<u>W</u>: Week: A~Z: 1~26 week;

SE: SOT353

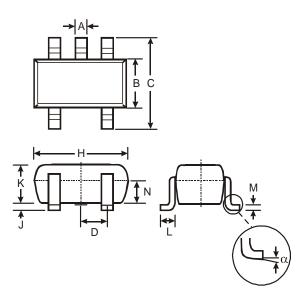
a~z: 27~52 week; z represents

Part Number	Package	Identification Code
74AHCT1G32W5	SOT25	ZW
74AHCT1G32SE	SOT353	ZW



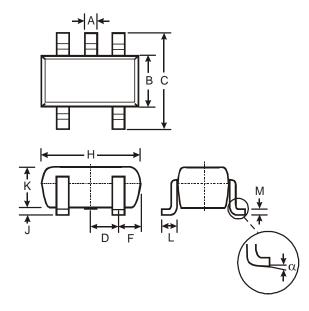
Package Outline Dimensions (All Dimensions in mm)

(1) Package Type: SOT25



	SOT25					
Dim	Min	Max	Тур			
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D	_	_	0.95			
Н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
K	1.00	1.30	1.10			
L	0.35	0.55	0.40			
M	0.10	0.20	0.15			
Ν	0.70	0.80	0.75			
α	0°	8°	_			
All D	imensi	ons in	mm			

(2) Package Type: SOT353



SOT353				
Dim	Min	Max		
Α	0.10	0.30		
В	1.15	1.35		
O	2.00	2.20		
D	0.65 Typ			
F	0.40	0.45		
H	1.80	2.20		
J	0	0.10		
K	0.90	1.00		
L	0.25	0.40		
М	0.10	0.22		
α	0°	8°		
All Dimensions in mm				



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