

Electrical Specifications ($-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ unless otherwise specified)

INPUT CHARACTERISTICS	Limits	Units
Input Current Range (see figure 4)	2.0 to 50	mA _(DC)
Maximum Forward Voltage Drop @ 10mA, 25°C (see figure 5)	1.4	V _(DC)
Maximum Reverse Voltage	6.0	V _(DC)
Maximum Reverse Current @ -6.0V (DC), 25°C	100	μA _(DC)
Maximum Pulsed Input Current @ 25°C (see figure 6)	1.0	A _(peak)

OUTPUT CHARACTERISTICS	Limits	Units
Maximum Forward Voltage @ 10μA	8.0 per channel	V _(DC)
Maximum Reverse Current @ -10V _{DC}	10	μA _(DC)

COUPLED CHARACTERISTICS	Limits PVI5050N	Limits PVI1050N	Units
Minimum Open Circuit Voltage @ ILED = 10mA, 25°C, RL = >10MΩ (see figures 1 to 2)	5.0	5.0/channel 10 series	V _(DC)
Minimum Short Circuit Current @ ILED = 10mA, 25°C (see figures 1 to 2)	5.0	5.0/channel 10 series	μA _(DC)
Maximum Capacitance (Input/Output)	1.0	2.0	pF
Maximum Ton Time @ ILED=10mA, CLOAD=10pF (See Figure7) RL > 20MΩ RL=10MΩ RL=4.7MΩ	300		μS
	160		μS
	90		μS
Maximum Toff Time @ ILED=10mA, CLOAD=10pF (See Figure7)	220		μS

GENERAL CHARACTERISTICS	Limits PVI5050N	Limits PVI1050N	Units
Minimum Dielectric Strength, Input-Output	4000	2500	V _{RMS}
Minimum Dielectric Strength, Output-to-Output	1200		V _{DC}
Minimum Insulation Resistance, Input-to-Output, @T _A =+25°C, 50%RH, 100V _{DC}	10 ¹²		Ω
Maximum Pin Soldering Temperature (10 seconds maximum)	+260		°C
Ambient Temperature Range:			

Infineon Technology does not recommend the use of this product in aerospace, avionics, military or life support applications. Users of this Infineon Technology product in such applications assume all risks of such use and indemnify Infineon Technology against all damages resulting from such use.

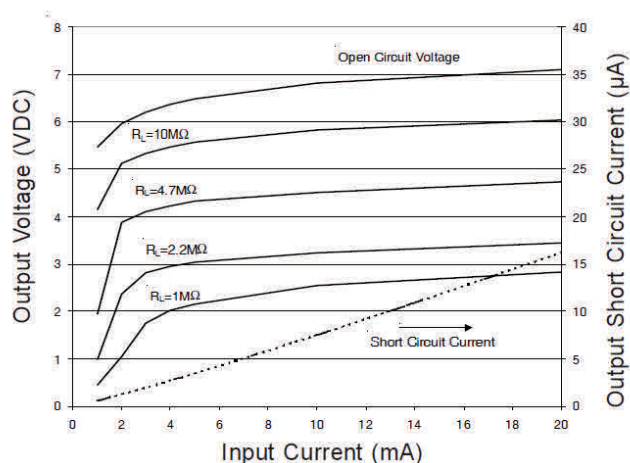


Figure 1. Typical Output Characteristics

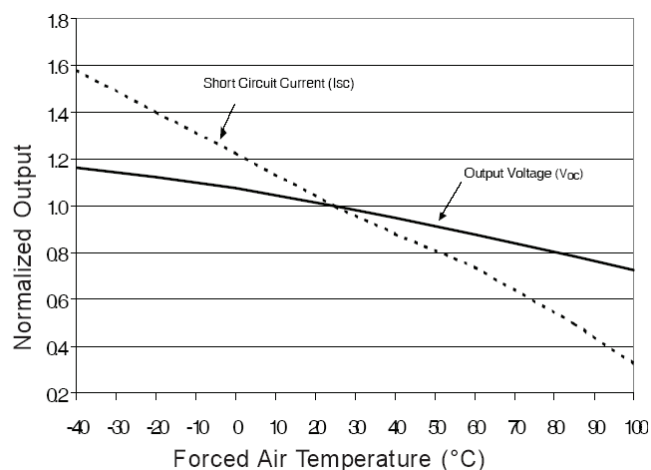


Figure 2. Typical Variation of Output

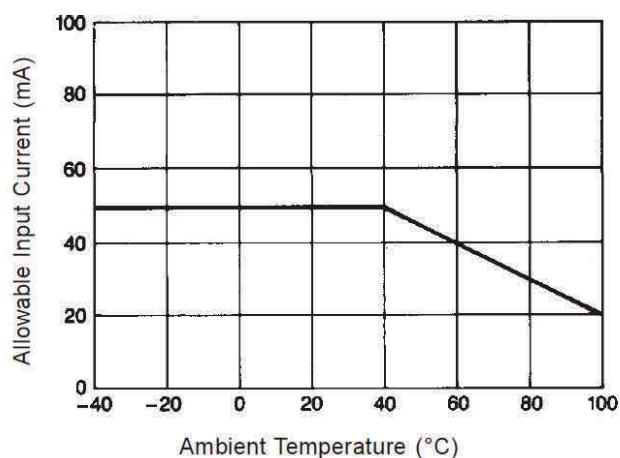


Figure 3. Input Current Derating

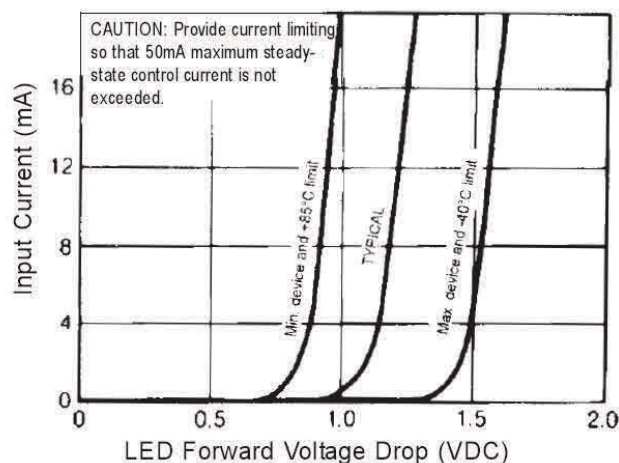


Figure 4. Input Characteristics

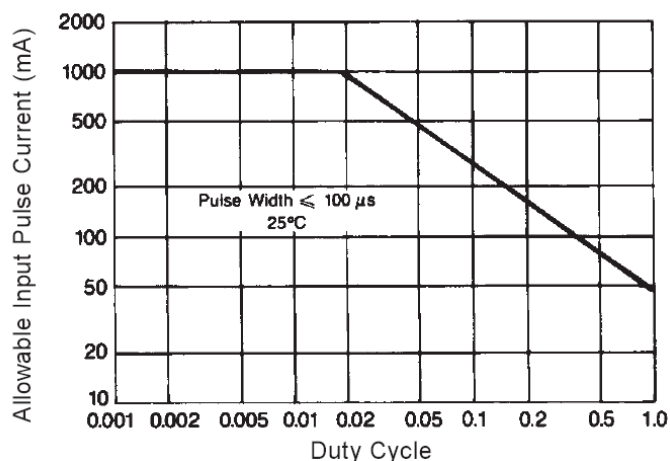


Figure 5. Input Pulse Capability

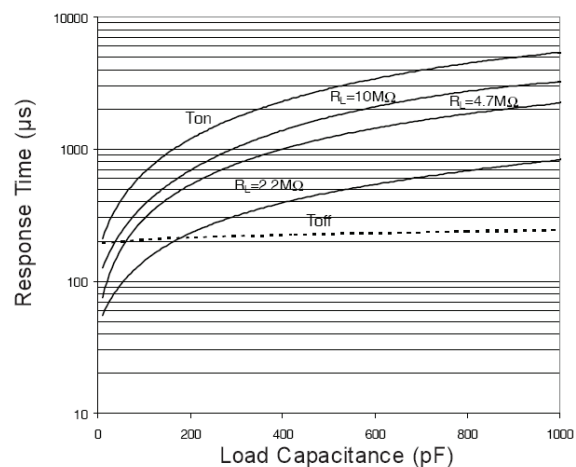
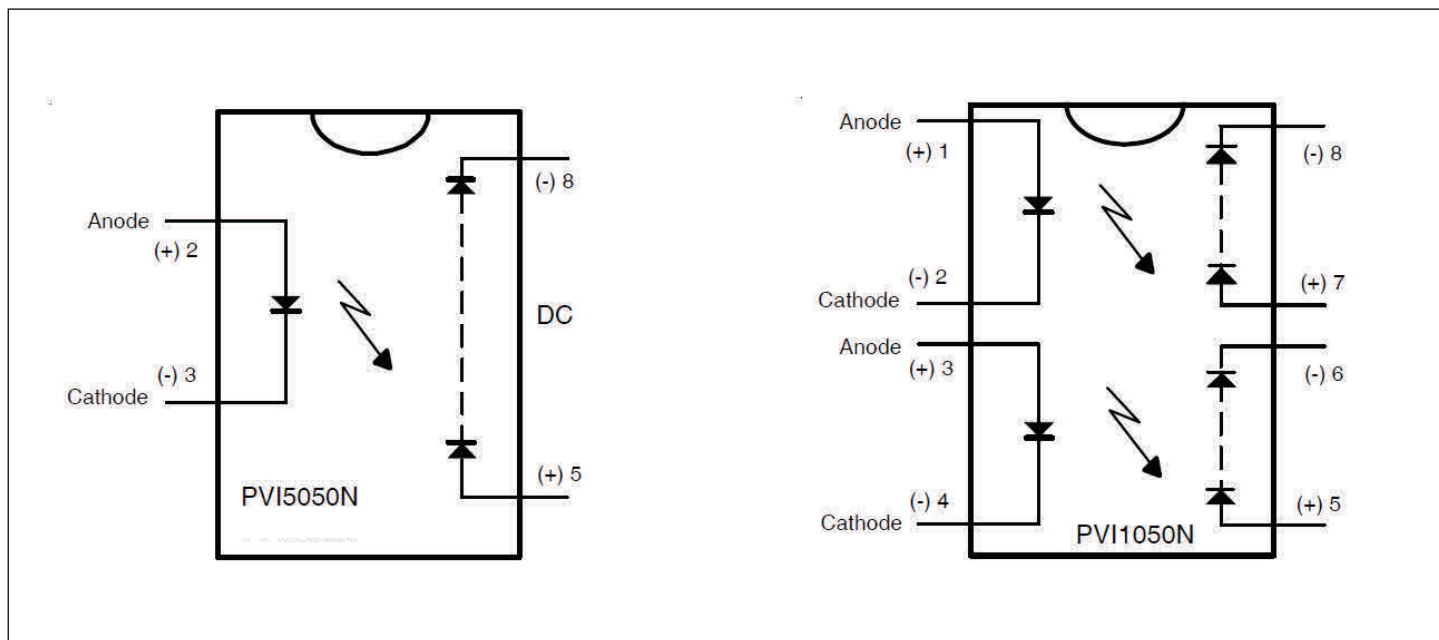


Figure 6. Typical Response Time

Wiring Diagram



Application Note:

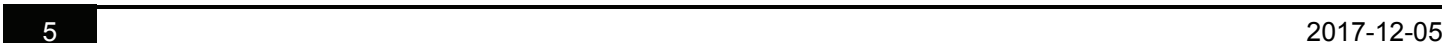
The outputs of the PVI1050N (pins 5-6 and 7-8) may be placed in series connection to produce a 10-volt output with a 5 μ A minimum short circuit current. Alternatively, the two outputs of the PVI1050 may be connected in parallel to produce a 5.0-volt output with a 10 μ A minimum short circuit current.

The two outputs of the PVI1050N may be applied separately with a maximum 1200VDC between the outputs. Input-to-output isolation to either output is 2500V (RMS).

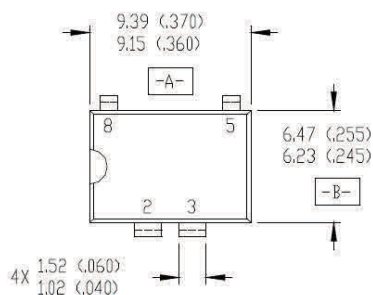
PVI1050N

NOTES:

- DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982.
- CONTROLLING DIMENSION: INCH.
- DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- [4] DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS SHALL NOT EXCEED 0.25 [.010].
- PRODUCTS AFFECTED: PVI1050N, PVT322, PVT322A, PVT422, PVI5013R AND PVI5033R.

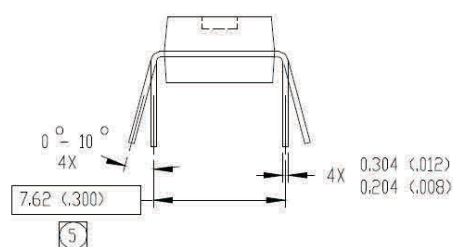
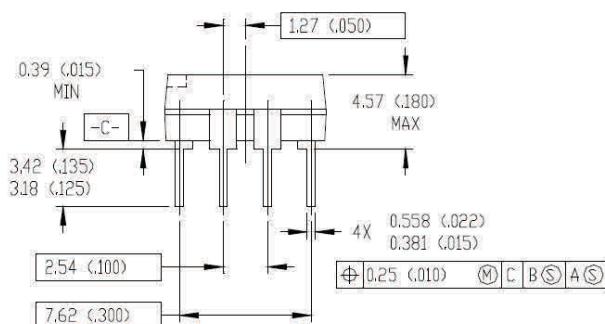


Case Outlines



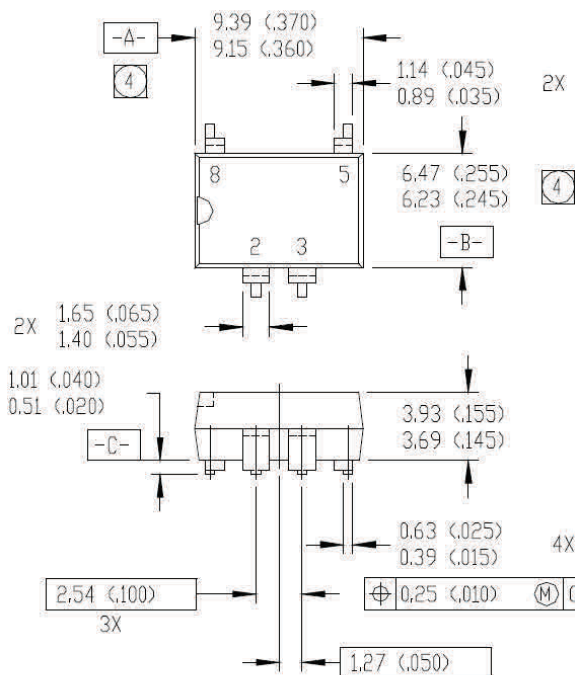
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2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-001AB.
5. MEASURED WITH THE LEADS CONSTRAINED TO BE PERPENDICULAR TO DATUM PLANE C.



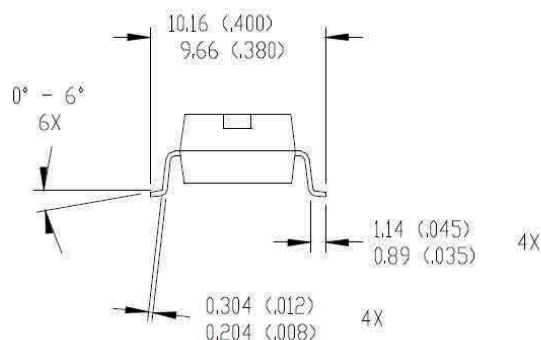
PVI5050N

01-2013 00 (MS-001AB)



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PVI5050NS

01-2019 00

Qualification Information

Qualification Level	Industrial (per JEDEC JESD47F [†] guidelines)	
Moisture Sensitivity Level	PVI1050NPbF	N/A
	PVI5050NPbF	
	PVI1050NSPbF	MSL4 (per JEDEC J-STD-020E & JEDEC J-STD-033C) [†]
	PVI5050NSPbF	
	PVI1050NS-TPbF	
RoHS Compliant	Yes	

† Applicable version of JEDEC standard at the time of product release.

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